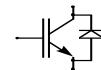


# Technische Information / Technical Information

IGBT-Module  
IGBT-Modules

BSM 50 GD 60 DLC E3226

**eupc**



## Höchstzulässige Werte / Maximum rated values

### Elektrische Eigenschaften / Electrical properties

Kollektor-Emitter-Sperrspannung collector-emitter voltage		V <sub>CES</sub>	600	V
Kollektor-Dauergleichstrom DC-collector current	T <sub>C</sub> = 80°C T <sub>C</sub> = 25°C	I <sub>C,nom.</sub> I <sub>C</sub>	50 70	A A
Periodischer Kollektor Spitzstrom repetitive peak collector current	t <sub>p</sub> = 1ms, T <sub>C</sub> = 80°C	I <sub>CRM</sub>	100	A
Gesamt-Verlustleistung total power dissipation	T <sub>C</sub> = 25°C, Transistor	P <sub>tot</sub>	250	W
Gate-Emitter-Spitzenspannung gate-emitter peak voltage		V <sub>GES</sub>	+/- 20V	V
Dauergleichstrom DC forward current		I <sub>F</sub>	50	A
Periodischer Spitzstrom repetitive peak forw. current	t <sub>p</sub> = 1ms	I <sub>FRM</sub>	100	A
Grenzlastintegral der Diode I <sup>2</sup> t - value, Diode	V <sub>R</sub> = 0V, t <sub>p</sub> = 10ms, T <sub>vj</sub> = 125°C	I <sup>2</sup> t	760	A <sup>2</sup> s
Isolations-Prüfspannung insulation test voltage	RMS, f= 50Hz, t= 1min.	V <sub>ISOL</sub>	2,5	kV

## Charakteristische Werte / Characteristic values

### Transistor / Transistor

			min.	typ.	max.	
Kollektor-Emitter Sättigungsspannung collector-emitter saturation voltage	I <sub>C</sub> = 50A, V <sub>GE</sub> = 15V, T <sub>vj</sub> = 25°C I <sub>C</sub> = 50A, V <sub>GE</sub> = 15V, T <sub>vj</sub> = 125°C	V <sub>CE sat</sub>	- -	1,95 2,20	2,45 -	V V
Gate-Schwellenspannung gate threshold voltage	I <sub>C</sub> = 1mA, V <sub>CE</sub> = V <sub>GE</sub> , T <sub>vj</sub> = 25°C	V <sub>GE(th)</sub>	4,5	5,5	6,5	V
Eingangskapazität input capacitance	f= 1MHz, T <sub>vj</sub> = 25°C, V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V	C <sub>ies</sub>	-	2,2	-	nF
Rückwirkungskapazität reverse transfer capacitance	f= 1MHz, T <sub>vj</sub> = 25°C, V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V	C <sub>res</sub>	-	0,2	-	nF
Kollektor-Emitter Reststrom collector-emitter cut-off current	V <sub>CE</sub> = 600V, V <sub>GE</sub> = 0V, T <sub>vj</sub> = 25°C V <sub>CE</sub> = 600V, V <sub>GE</sub> = 0V, T <sub>vj</sub> = 125°C	I <sub>CES</sub>	- -	1 1	500 -	μA mA
Gate-Emitter Reststrom gate-emitter leakage current	V <sub>CE</sub> = 0V, V <sub>GE</sub> = 20V, T <sub>vj</sub> = 25°C	I <sub>GES</sub>	-	-	400	nA

prepared by: Andreas Vetter

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approved by: Michael Hornkamp

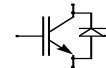
revision: 1

# Technische Information / Technical Information

IGBT-Module  
IGBT-Modules

BSM 50 GD 60 DLC E3226

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## Charakteristische Werte / Characteristic values

### Transistor / Transistor

			min.	typ.	max.
Einschaltverzögerungszeit (ind. Last) turn on delay time (inductive load)	$I_C = 50A, V_{CC} = 300V$ $V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 25^\circ C$ $V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 125^\circ C$	$t_{d,on}$	-	40	-
Anstiegszeit (induktive Last) rise time (inductive load)	$I_C = 50A, V_{CC} = 300V$ $V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 25^\circ C$ $V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 125^\circ C$	$t_r$	-	9	-
Abschaltverzögerungszeit (ind. Last) turn off delay time (inductive load)	$I_C = 50A, V_{CC} = 300V$ $V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 25^\circ C$ $V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 125^\circ C$	$t_{d,off}$	-	120	-
Fallzeit (induktive Last) fall time (inductive load)	$I_C = 50A, V_{CC} = 300V$ $V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 25^\circ C$ $V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 125^\circ C$	$t_f$	-	12	-
Einschaltverlustenergie pro Puls turn-on energy loss per pulse	$I_C = 50A, V_{CC} = 300V, V_{GE} = 15V$ $R_G = 2,7\Omega, T_{vj} = 125^\circ C, L_\sigma = 15nH$	$E_{on}$	-	0,5	-
Abschaltverlustenergie pro Puls turn-off energy loss per pulse	$I_C = 50A, V_{CC} = 300V, V_{GE} = 15V$ $R_G = 2,7\Omega, T_{vj} = 125^\circ C, L_\sigma = 15nH$	$E_{off}$	-	1,0	-
Kurzschlußverhalten SC Data	$t_p \leq 10\mu sec, V_{GE} \leq 15V$ $T_{vj} \leq 125^\circ C, V_{CC} = 360V, V_{CEmax} = V_{CES} - L_{\sigma CE} \cdot dI/dt$	$I_{sc}$	-	225	-
Modulinduktivität stray inductance module		$L_{\sigma CE}$	-	60	-
Modul-Leitungswiderstand, Anschlüsse - Chip lead resistance, terminals - chip	$T_C = 25^\circ C$	$R_{CC+EE'}$	-	8,0	-

## Charakteristische Werte / Characteristic values

### Diode / Diode

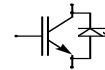
			min.	typ.	max.
Durchlaßspannung forward voltage	$I_F = 50A, V_{GE} = 0V, T_{vj} = 25^\circ C$ $I_F = 50A, V_{GE} = 0V, T_{vj} = 125^\circ C$	$V_F$	-	1,25	1,6
Rückstromspitze peak reverse recovery current	$I_F = 50A, -di_F/dt = 2900A/\mu sec$ $V_R = 300V, V_{GE} = -10V, T_{vj} = 25^\circ C$ $V_R = 300V, V_{GE} = -10V, T_{vj} = 125^\circ C$	$I_{RM}$	-	88	-
Sperrverzögerungsladung recovered charge	$I_F = 50A, -di_F/dt = 2900A/\mu sec$ $V_R = 300V, V_{GE} = -10V, T_{vj} = 25^\circ C$ $V_R = 300V, V_{GE} = -10V, T_{vj} = 125^\circ C$	$Q_r$	-	3,4	-
Abschaltenergie pro Puls reverse recovery energy	$I_F = 50A, -di_F/dt = 2900A/\mu sec$ $V_R = 300V, V_{GE} = -10V, T_{vj} = 25^\circ C$ $V_R = 300V, V_{GE} = -10V, T_{vj} = 125^\circ C$	$E_{rec}$	-	1,5	-

# Technische Information / Technical Information

IGBT-Module  
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## Thermische Eigenschaften / Thermal properties

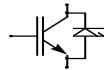
			min.	typ.	max.	
Innerer Wärmewiderstand thermal resistance, junction to case	Transistor / transistor, DC Diode / diode, DC	R <sub>thJC</sub>	-	-	0,50	K/W
Übergangs-Wärmewiderstand thermal resistance, case to heatsink	pro Modul / per module $\lambda_{\text{Paste}} = 1 \text{W/m}^{\circ}\text{K}$ / $\lambda_{\text{grease}} = 1 \text{W/m}^{\circ}\text{K}$	R <sub>thCK</sub>	-	0,02	-	K/W
Höchstzulässige Sperrschichttemperatur maximum junction temperature		T <sub>vj</sub>	-	-	150	°C
Betriebstemperatur operation temperature		T <sub>op</sub>	-40	-	125	°C
Lagertemperatur storage temperature		T <sub>stg</sub>	-40	-	125	°C

## Mechanische Eigenschaften / Mechanical properties

Gehäuse, siehe Anlage case, see appendix					
Innere Isolation internal insulation				Al <sub>2</sub> O <sub>3</sub>	
CTI comperative tracking index				225	
Anzugsdrehmoment f. mech. Befestigung mounting torque		M	-15	4	+15 Nm %
Gewicht weight		G	180		g

Mit dieser technischen Information werden Halbleiterbauelemente spezifiziert, jedoch keine Eigenschaften zugesichert.  
Sie gilt in Verbindung mit den zugehörigen Technischen Erläuterungen.

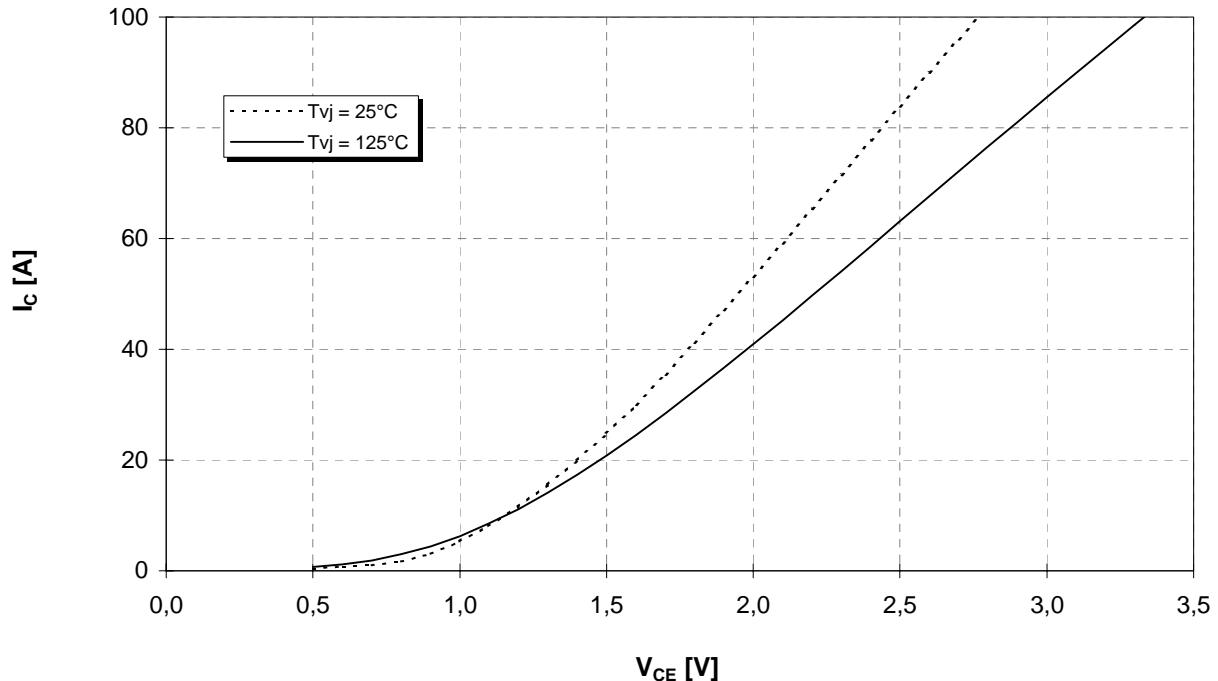
This technical information specifies semiconductor devices but promises no characteristics.  
It is valid in combination with the belonging technical notes.



**Ausgangskennlinie (typisch)**  
**Output characteristic (typical)**

$$I_C = f(V_{CE})$$

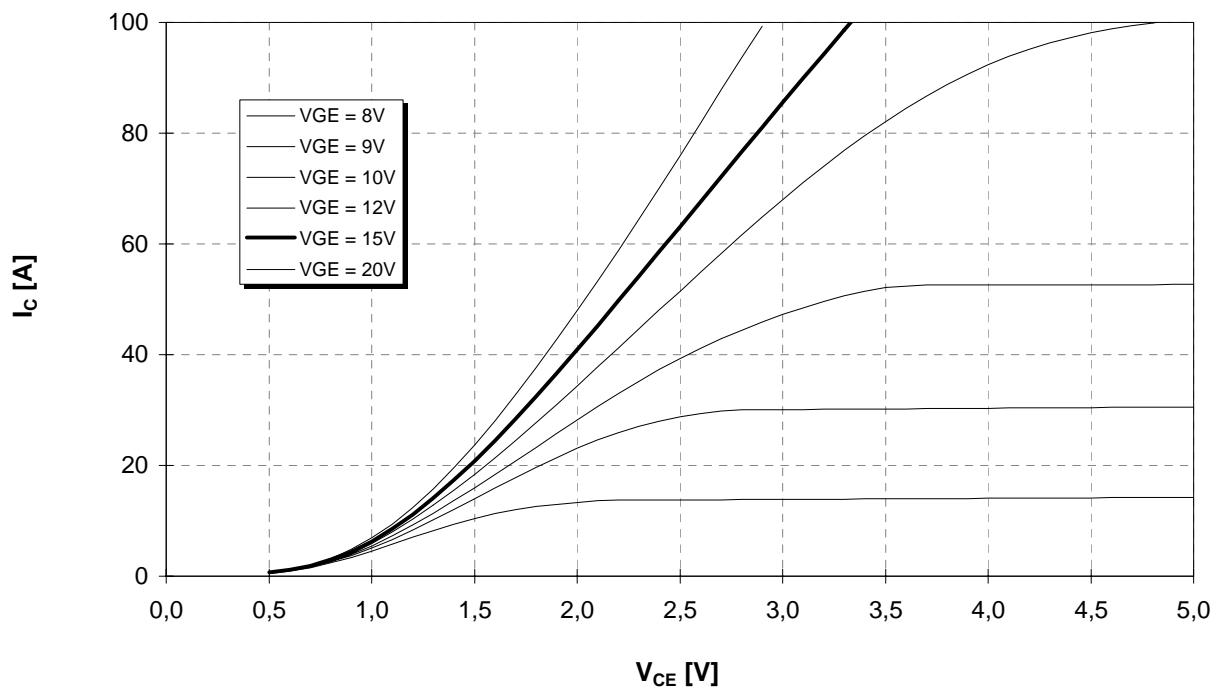
$V_{GE} = 15V$

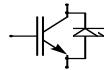


**Ausgangskennlinienfeld (typisch)**  
**Output characteristic (typical)**

$$I_C = f(V_{CE})$$

$T_{vj} = 125^\circ C$

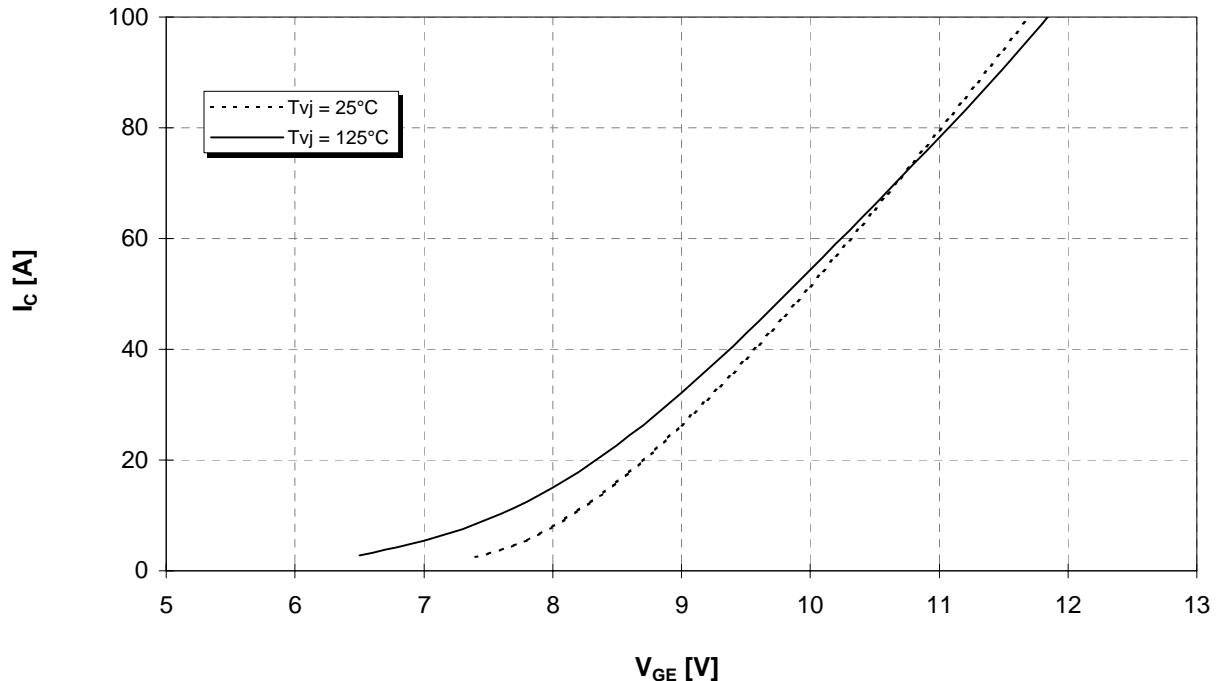




**Übertragungscharakteristik (typisch)**  
**Transfer characteristic (typical)**

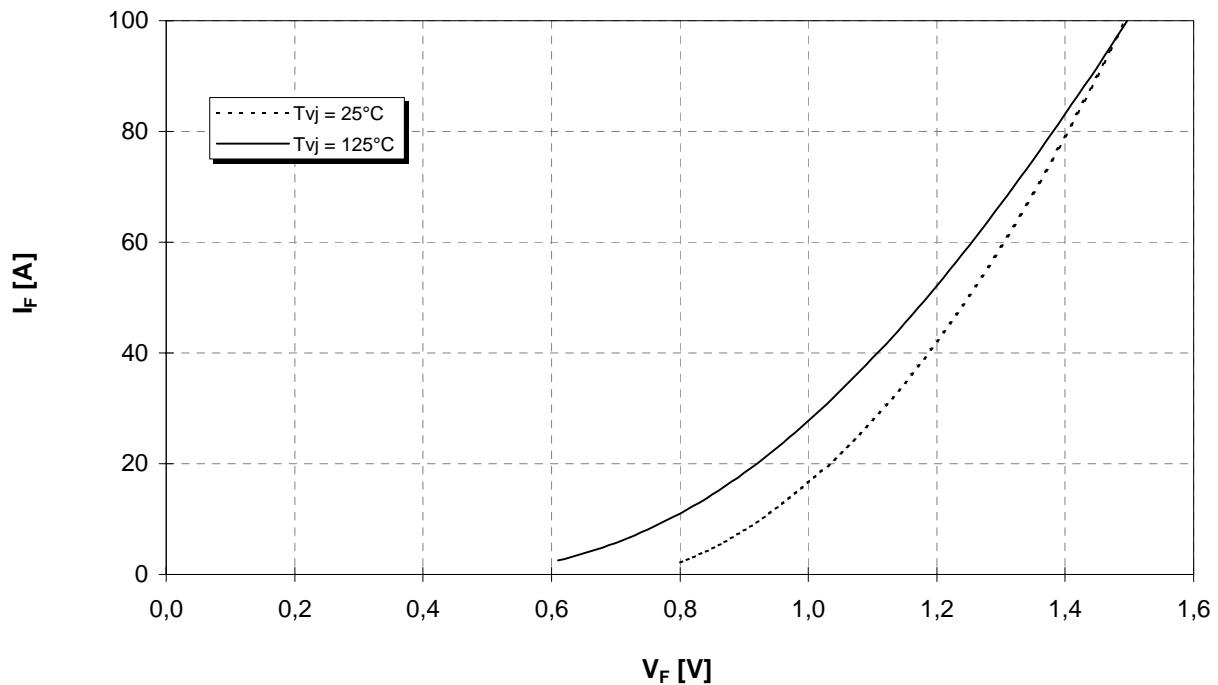
$$I_C = f(V_{GE})$$

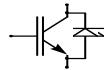
$V_{CE} = 20V$



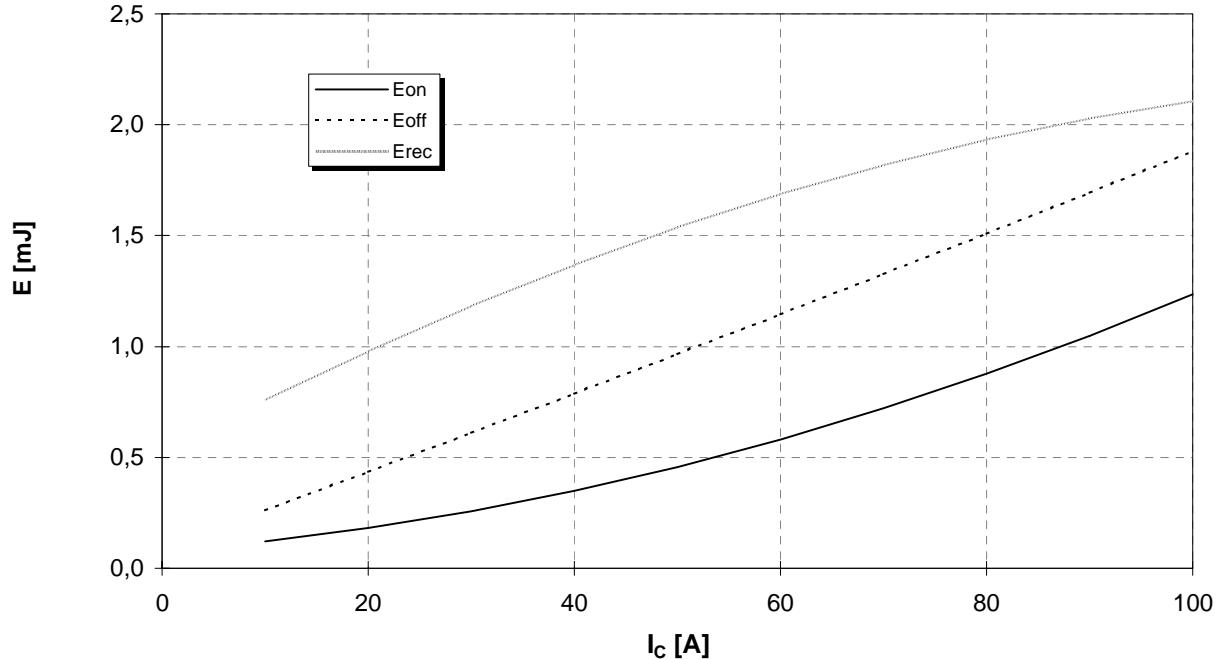
**Durchlaßkennlinie der Inversdiode (typisch)**  
**Forward characteristic of inverse diode (typical)**

$$I_F = f(V_F)$$

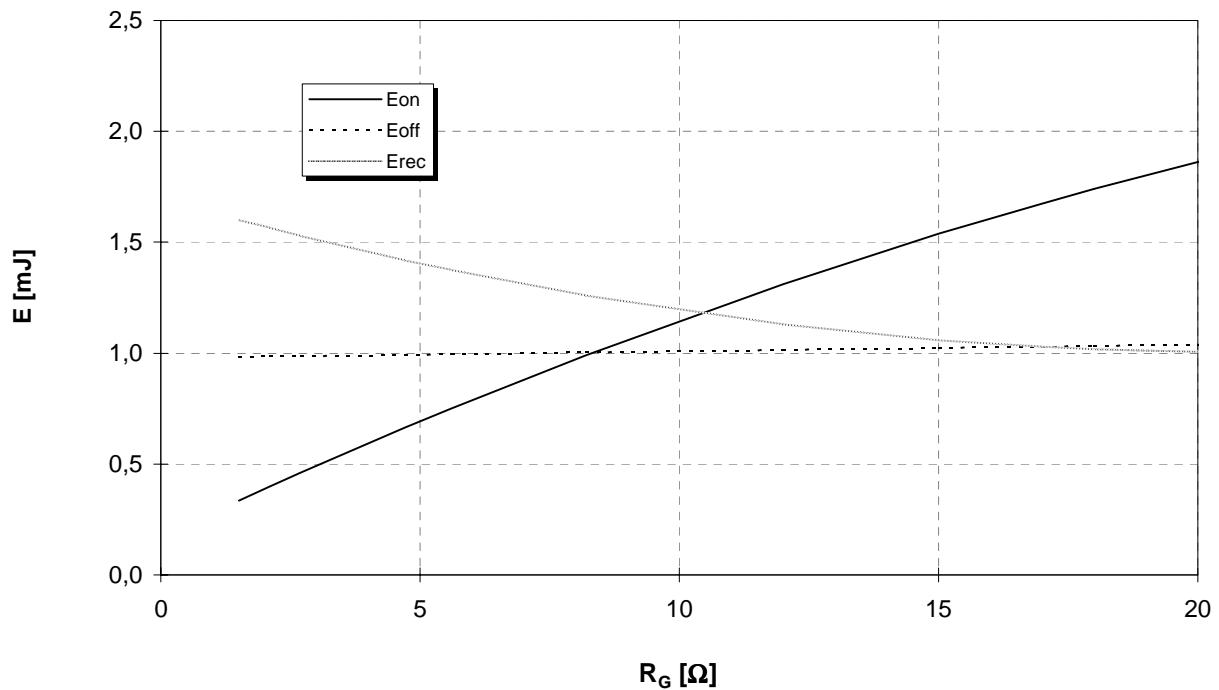


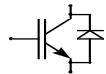

**Schaltverluste (typisch)**  
**Switching losses (typical)**

$$E_{on} = f(I_c), E_{off} = f(I_c), E_{rec} = f(I_c)$$

 $R_{G, on} = 2,7\Omega, R_{G, off} = 2,7\Omega, V_{CC} = 300V, T_{vj} = 125^{\circ}C$ 

**Schaltverluste (typisch)**  
**Switching losses (typical)**

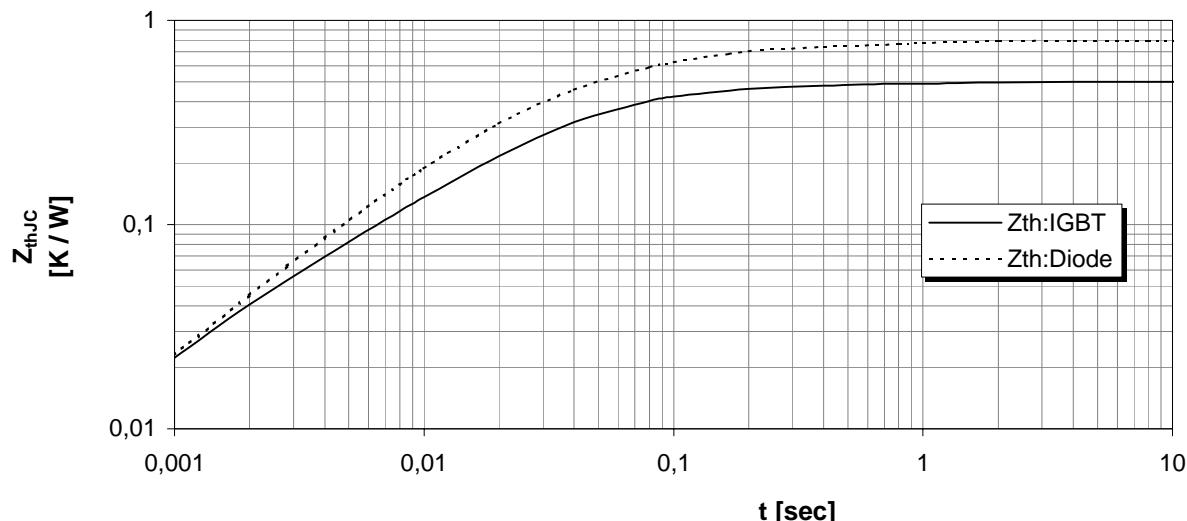
$$E_{on} = f(R_G), E_{off} = f(R_G), E_{rec} = f(R_G)$$

 $I_c = 50A, V_{CE} = 300V, T_{vj} = 125^{\circ}C$ 




**Transienter Wärmewiderstand**  
**Transient thermal impedance**

$$Z_{thJC} = f(t)$$

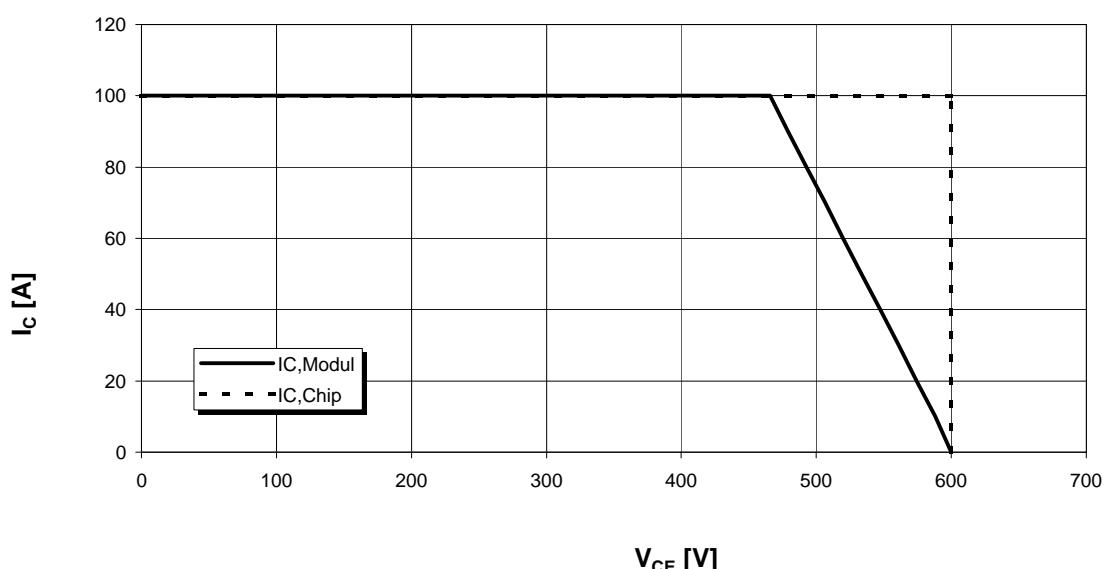


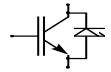
i	1	2	3	4
$r_i$ [K/kW] : IGBT	21,2	262,0	176,2	40,6
$\tau_i$ [sec] : IGBT	0,0018	0,0240	0,0651	0,6626
$r_i$ [K/kW] : Diode	281,9	270,4	169,8	77,9
$\tau_i$ [sec] : Diode	0,0487	0,0169	0,1069	0,9115

**Sicherer Arbeitsbereich (RBSOA)**

**Reverse bias safe operation area (RBSOA)**

$R_{G,off} = 2,7\Omega$ ,  $T_{vj} = 125^\circ C$





**Gehäusemaße / Schaltbild**  
**Package outline / Circuit diagram**

