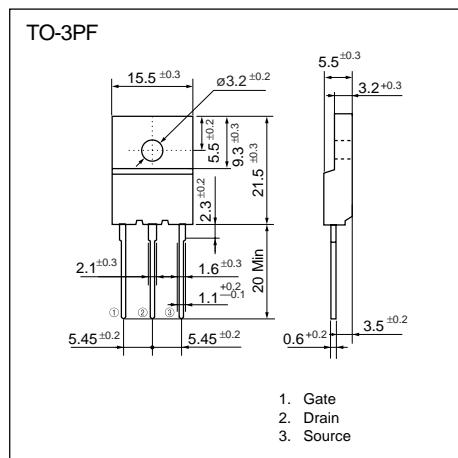


N-CHANNEL SILICON POWER MOS-FET**■ Features**

- High speed switching**
- Low on-resistance**
- No secondary breakdown**
- Low driving power**
- Avalanche-proof**

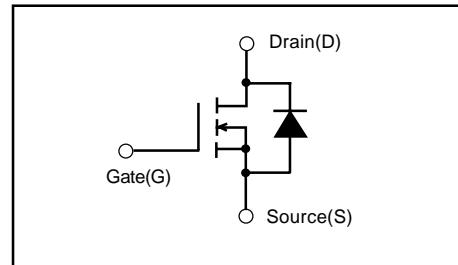
■ Applications

- Switching regulators**
- UPS (Uninterruptible Power Supply)**
- DC-DC converters**

**■ Maximum ratings and characteristic Absolute maximum ratings****($T_c=25^\circ\text{C}$ unless otherwise specified)**

Item	Symbol	Rating	Unit
Drain-source voltage	V_{DS}	60	V
Continuous drain current	I_D	± 100	A
Pulsed drain current	$I_D(\text{puls})$	± 400	A
Gate-source voltage	V_{GS}	± 30	V
Maximum Avalanche Energy	E_{AV}^*	1268.3	mJ
Max. power dissipation	P_D	125	W
Operating and storage temperature range	T_{ch}	+150	$^\circ\text{C}$
	T_{stg}	-55 to +150	$^\circ\text{C}$

*1 $L=0.169\text{mH}$, $V_{cc}=24\text{V}$

■ Equivalent circuit schematic**($T_c = 25^\circ\text{C}$ unless otherwise specified)**

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain-source breakdown voltage	BV_{DS}	$I_D=1\text{mA}$ $V_{GS}=0\text{V}$	60			V
Gate threshold voltage	$V_{GS(\text{th})}$	$I_D=10\text{mA}$ $V_{DS}=V_{GS}$	2.5	3.0	3.5	V
Zero gate voltage drain current	I_{DSS}	$V_{DS}=60\text{V}$ $T_{ch}=25^\circ\text{C}$	10	500	500	μA
		$V_{GS}=0\text{V}$ $T_{ch}=125^\circ\text{C}$	0.2	1.0	1.0	mA
Gate-source leakage current	I_{GSS}	$V_{GS}=\pm 30\text{V}$ $V_{DS}=0\text{V}$	10	100	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$I_D=50\text{A}$ $V_{GS}=10\text{V}$		5.7	7.8	$\text{m}\Omega$
Forward transconductance	g_{fs}	$I_D=50\text{A}$ $V_{DS}=25\text{V}$	25	55		S
Input capacitance	C_{iss}	$V_{DS}=25\text{V}$		5400	8100	pF
Output capacitance	C_{oss}	$V_{GS}=0\text{V}$		2100	3150	
Reverse transfer capacitance	C_{rss}	$f=1\text{MHz}$		550	830	
Turn-on time t_{on}	$t_{d(on)}$	$V_{cc}=30\text{V}$ $I_D=100\text{A}$		29	50	ns
	t_r	$V_{GS}=10\text{V}$		200	350	
Turn-off time t_{off}	$t_{d(off)}$	$R_{GS}=10\Omega$		160	240	
	t_f			150	230	
Avalanche capability	I_{AV}	$L=100\ \mu\text{H}$ $T_{ch}=25^\circ\text{C}$	100			A
Diode forward on-voltage	V_{SD}	$I_F=100\text{A}$ $V_{GS}=0\text{V}$ $T_{ch}=25^\circ\text{C}$		1.0	1.5	V
Reverse recovery time	t_{rr}	$I_F=50\text{A}$ $V_{GS}=0\text{V}$		85		ns
Reverse recovery charge	Q_{rr}	$-di/dt=100\text{A}/\mu\text{s}$ $T_{ch}=25^\circ\text{C}$		0.21		μC

■ Thermal characteristics

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal resistance	$R_{th(ch-c)}$	channel to case			1.0	$^\circ\text{C/W}$
	$R_{th(ch-a)}$	channel to ambient			30.0	$^\circ\text{C/W}$

Characteristics

