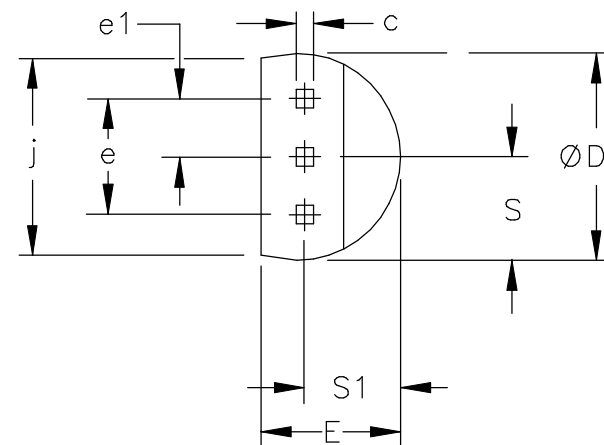
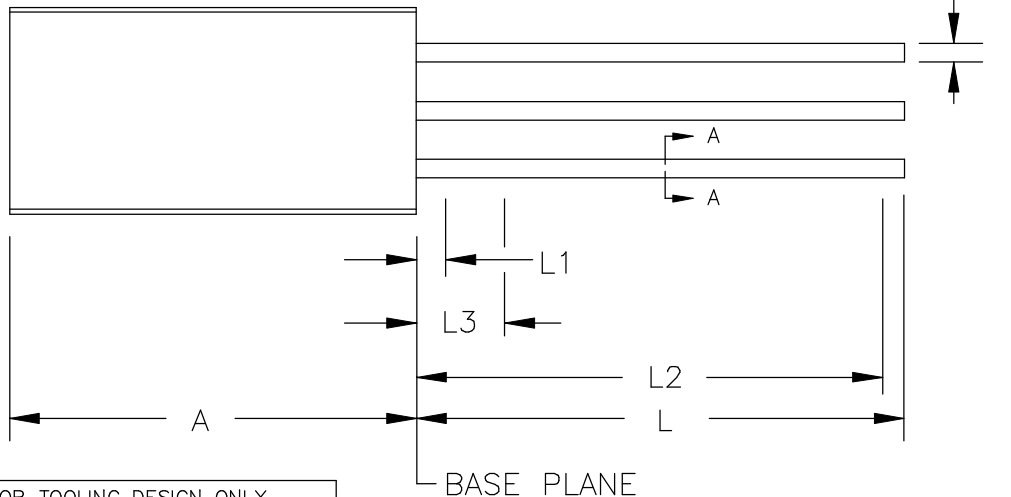
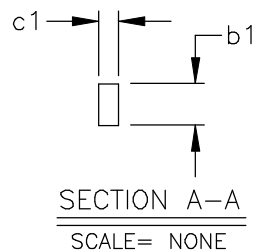




FOR TOOLING DESIGN ONLY				
SYMBOL	MIN	TYP	MAX	NOTE
A	.348	.350	.353	
b	.015	—	.022	
b1	.015	—	.018	2
c	.014	—	.020	
c1	.014	—	.016	2
Ø D	.178	.180	.183	
E	.115	.120	.135	
e	.095	.100	.105	
e1	.045	.050	.055	
j	.160	—	—	4
L	.410	—	.440	
L1	—	—	.050	1
L2	.250	—	—	2
L3	—	—	.100	2
S	.083	.090	.099	3
S1	.080	.0825	.090	3



NOTES:

1. TERMINAL DIMENSIONS ARE UNCONTROLLED WITHIN L1.
2. b1 AND c1 APPLY BETWEEN L3 AND L2.
3. S DIMENSION IS DISTANCE FROM TRUE POSITION CENTERLINE OF THE MIDDLE LEAD POSITION TO THE EXTREMITY OF THE BODY.
4. FLAT INDEX SURFACE.

UNLESS OTHERWISE SPECIFIED		SIGNATURE	DATE	Dallas Semiconductor			
ALL DIMENSIONS ARE IN INCHES. TOLERANCES: FRACTIONS ANGLES DECIMALS .XX ±.01 .XXX ±.005 SURFACE FINISH IN AA MICROINCHES. REMOVE ALL BURRS AND SHARP EDGES. DO NOT SCALE THIS DRAWING.		QA APVD:		PACKAGE OUTLINE, PLASTIC RADIAL, EXTRA LONG BODY			
		ENGR MGR:					
		ENGR BY:					
		CHECK BY:					
		DRAWN BY:		SIZE B	FSCM NO	DWG NO	REV B
				SCALE NONE	SHEET 1 OF 1		