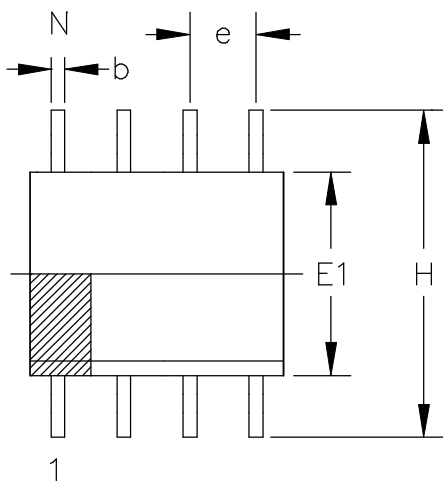


LTR	MIN	MAX
A IN. MM	0.072 1.83	0.084 2.13
A1 IN. MM	0.004 0.102	0.010 0.25
A2 IN. MM	0.070 1.78	0.080 2.03
b IN. MM	0.013 0.33	0.020 0.51
C IN. MM	0.006 0.15	0.010 0.25
D IN. MM	0.203 5.16	0.215 5.46
e IN. MM	.050 BSC 1.27 BSC	
E1 IN. MM	0.203 5.16	0.213 5.41
H IN. MM	0.302 7.67	0.318 8.07
L IN. MM	0.019 0.48	0.030 0.76
θ	0°	8°

THE CHAMFER ON THE BODY IS OPTIONAL.
IF IT IS NOT PRESENT, A TERMINAL 1 IDENTIFIER
MUST BE POSITIONED SO THAT 1/2 OR MORE OF
IT'S AREA IS CONTAINED IN THE HATCHED ZONE.

SIGNATURE		DATE		 Dallas Semiconductor			
DOC. CONTROL:							
ENGR. MGR:				TITLE PACKAGE OUTLINE .208" 8 LEAD SOIC			
MFG. ENGR:							
CHECKED BY:							
DRAWN BY:				SIZE A	FSCM NO	PART NO.	REV A
DO NOT SCALE DWG.		SCALE N/A		SHEET 1 OF 1			