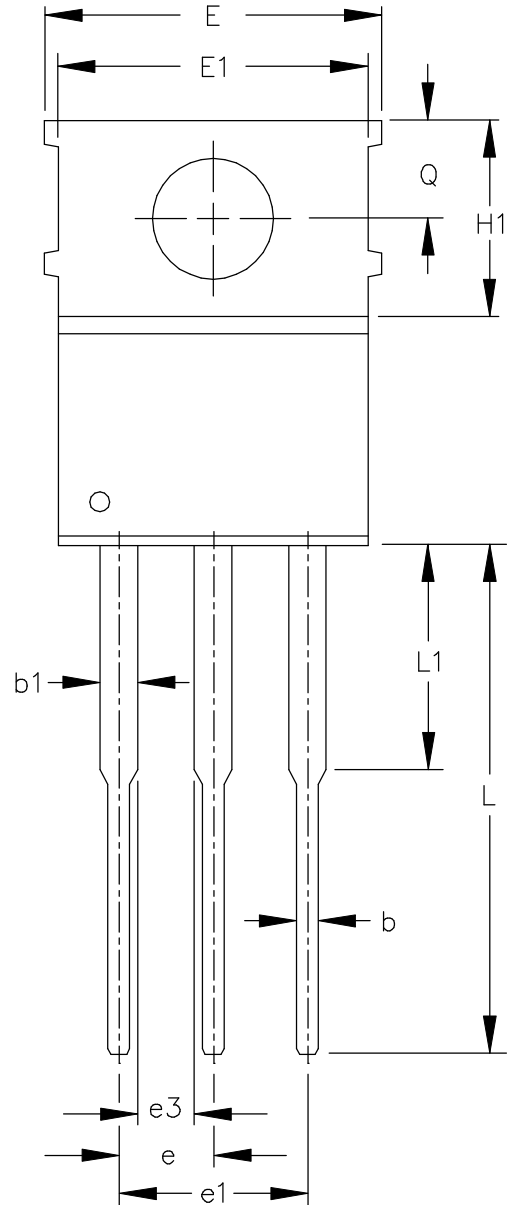
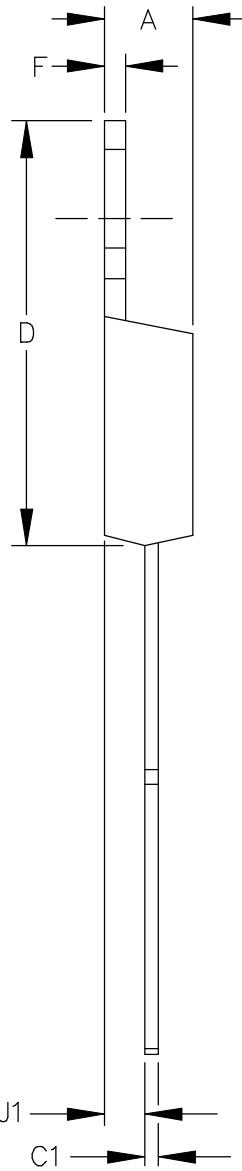
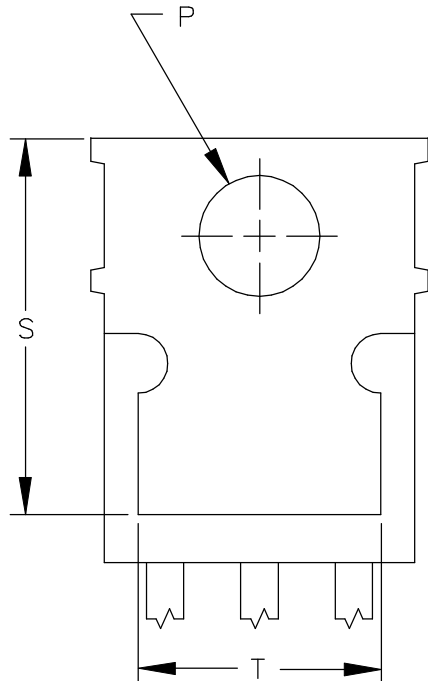




LTR	MIN	NOM	MAX
A	0.165	0.175	0.185
b	0.024	0.032	0.040
b1	0.042	0.050	0.058
C1	0.014	0.018	0.022
D	0.573	0.588	0.603
E	0.394	0.404	0.414
E1	0.390	0.400	0.410
e	0.090	0.100	0.110
e1	0.190	0.200	0.210
e3	0.045	0.050	0.055
F	0.045	0.050	0.055
H1	0.236	0.248	0.260
J1	0.050	0.065	0.080
L	0.525	0.545	0.565
L1	0.220	0.230	0.240
P	0.146	0.151	0.156
Q	0.100	0.108	0.116
S	0.500	0.525	0.550
T	0.300	0.350	0.400

ALL DIMENSIONS SHOWN ARE IN INCHES.

SIGNATURE		DATE		 Dallas Semiconductor PACKAGE OUTLINE DRAWING, 3L TO-220			
DOC. CONTROL:							
ENGR. MGR:							
MFG. ENGR:							
CHECKED BY:							
DRAWN BY:		SIZE A	FSCM NO	PART NO.		REV B	
DO NOT SCALE DWG.		SCALE N/A		SHEET 1 OF 1			