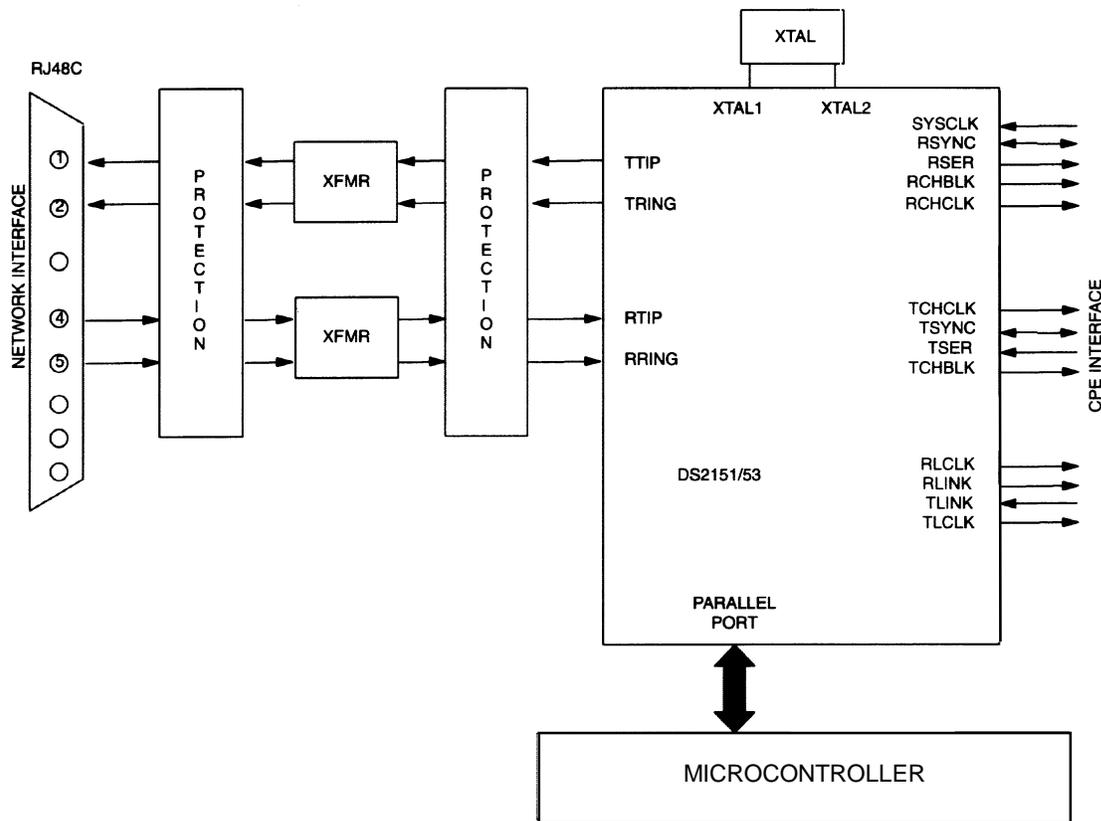


Figure 1. describes a method for building a CSU using the DS2151 or DS2153. For information on line protection see the application note “Secondary Over Voltage Protection”. Also, see the application notes “Crystal Selection Guide” and “Transformer Selection Guide”

CSU CIRCUIT Figure 1



Note:

Contact the factory for pre-written (T1) code for CSU functions and FDL control.