



### NOTES:

1. PMC device is programmed to clock data into the RDAT pin on the falling edge of RCLK.
2. PMC device is programmed to update the TDAT pin on the rising edge of TICLK.
3. RSYNC is programmed to output a frame sync; TSYNC is programmed to input a frame sync.
4. Receive and transmit elastic stores are disabled.
5. A “looped-timed” application is shown.
6. Timing is shown below:

