



DS3131

BoSS – Bit Synchronous HDLC Controller

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INTRODUCTION

The DS3131 BoSS HDLC Controller is based on Dallas Semiconductor's DS3134 CHATEAU HDLC Controller. BoSS & CHATEAU share the same DMA & FIFO structure and the signal locations for the Local Bus and the PCI Bus are the same. The primary difference between the two devices is in the Layer One functionality. CHATEAU supports channelized T1/E1 whereas BoSS does not. Hence the Layer One functions in CHATEAU that support channelized interfaces does not exist in BoSS.

The DS3131 BoSS consists of 40 Bit Synchronous HDLC controllers. The primary features of the device are listed in Table 1A. This data sheet is split in Sections along the major blocks of the device as shown in Figure 1A. Throughout the data sheet, certain terms will be used and these terms are defined in Table 1B. The DS3131 device is designed to meet certain specifications and a listing of these governing specifications is shown in Table 1C. The DS3131 can be operated in two configurations depending on whether the Local Bus is enabled or not. See Figure 1B.

DS3131 Block Diagram Figure 1A



