

TEMIC

Siliconix

TP0610L/T, VP0610L/T, BS250**P-Channel Enhancement-Mode MOS Transistors****TP0610L****TP0610T****VP0610L****VP0610L****BS250****Product Summary**

Part Number	V _{(BR)DSS} Min (V)	r _{D(on)} Max (Ω)	V _{GS(th)} (V)	I _D (A)
TP0610L	-60	10 @ V _{GS} = -10 V	-1 to -2.4	-0.18
TP0610T	-60	10 @ V _{GS} = -10 V	-1 to -2.4	-0.12
VP0610L	-60	10 @ V _{GS} = -10 V	-1 to -3.5	-0.18
VP0610L	-60	10 @ V _{GS} = -10 V	-1 to -3.5	-0.12
BS250	-45	14 @ V _{GS} = -10 V	-1 to -3.5	-0.18

For applications information see AN804.

Features

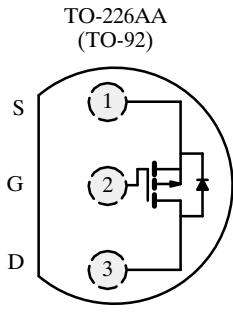
- High-Side Switching
- Low On-Resistance: 8 Ω
- Low Threshold: -1.9 V
- Fast Switching Speed: 16 ns
- Low Input Capacitance: 15 pF

Benefits

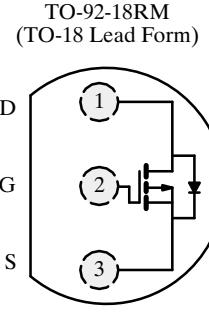
- Ease in Driving Switches
- Low Offset (Error) Voltage
- Low-Voltage Operation
- High-Speed Switching
- Easily Driven Without Buffer

Applications

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Battery Operated Systems
- Power Supply, Converter Circuits
- Motor Control

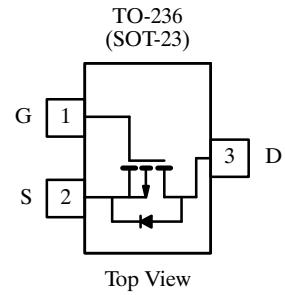


Top View

TP0610L
VP0610L

Top View

BS250



Top View

TP0610T (T0)*
VP0610T (V0)*

*Marking Code for TO-236

Absolute Maximum Ratings (T_A = 25°C Unless Otherwise Noted)

Parameter	Symbol	TP0610L	TP0610T	VP0610L	VP0610L	BS250	Unit
Drain-Source Voltage	V _{DS}	-60	-60	-60	-60	-45	V
Gate-Source Voltage	V _{GS}	±30	±30	±30	±30	±25	
Continuous Drain Current (T _J = 150°C)	I _D	-0.18	-0.12	-0.18	-0.12	-0.18	A
		-0.11	-0.07	-0.11	-0.07		
Pulsed Drain Current ^a	I _{DM}	-0.8	-0.4	-0.8	-0.4		
Power Dissipation	P _D	0.8	0.36	0.8	0.36	0.83	W
		0.32	0.14	0.32	0.14		
Maximum Junction-to-Ambient	R _{thJA}	156	350	156	350	150	°C/W
Operating Junction & Storage Temperature Range	T _J , T _{stg}			-55 to 150			°C

Notes

a. Pulse width limited by maximum junction temperature.

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Specifications^a

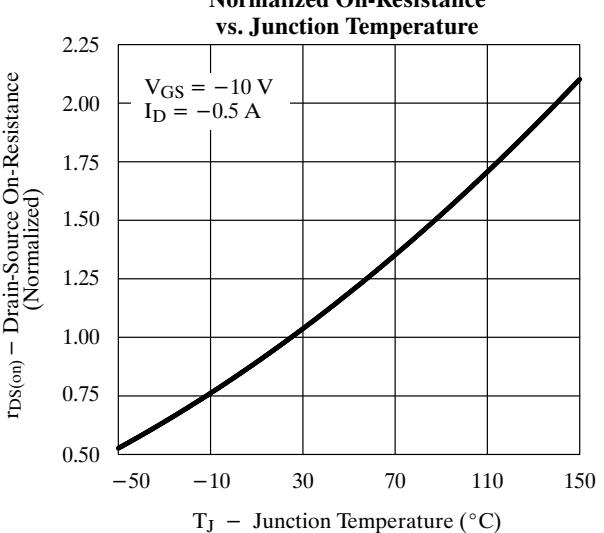
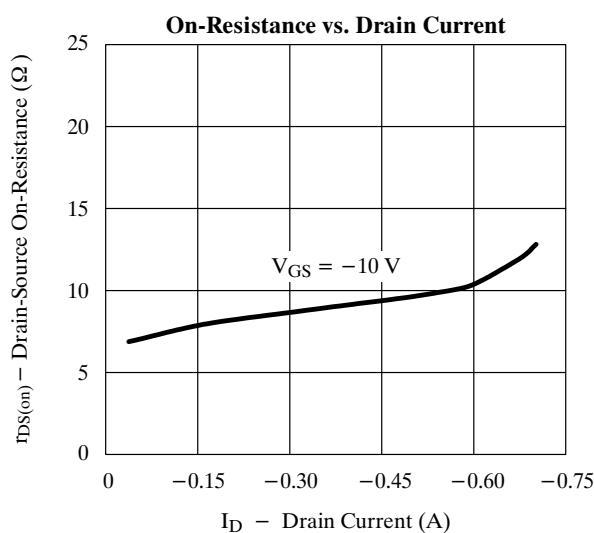
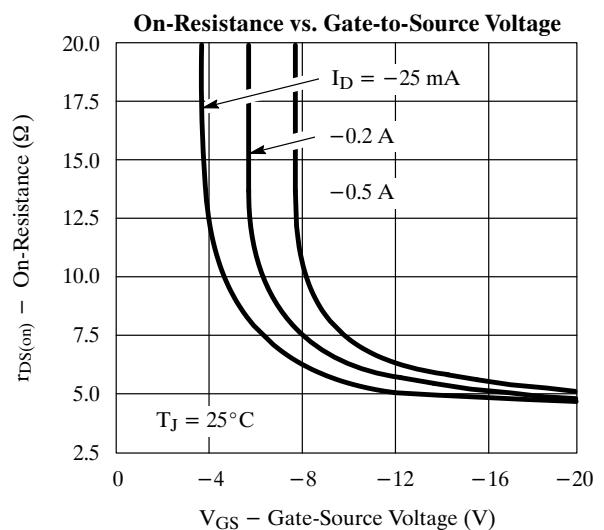
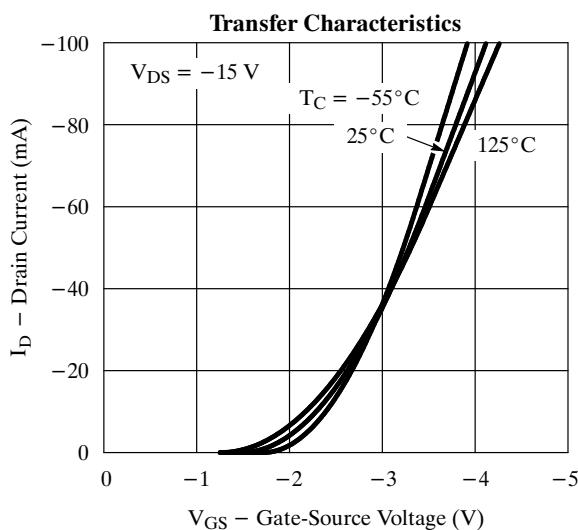
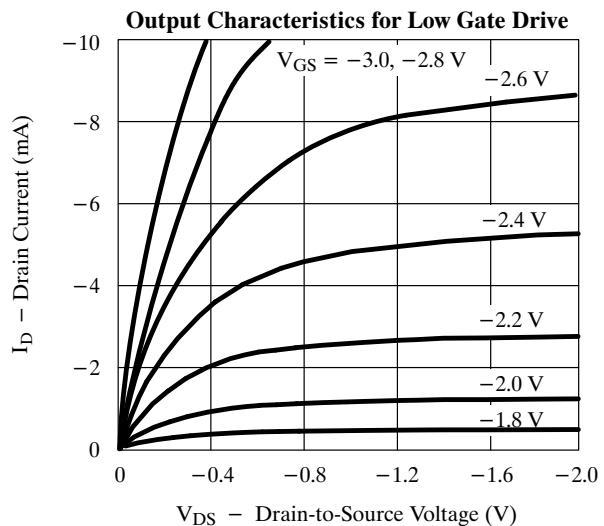
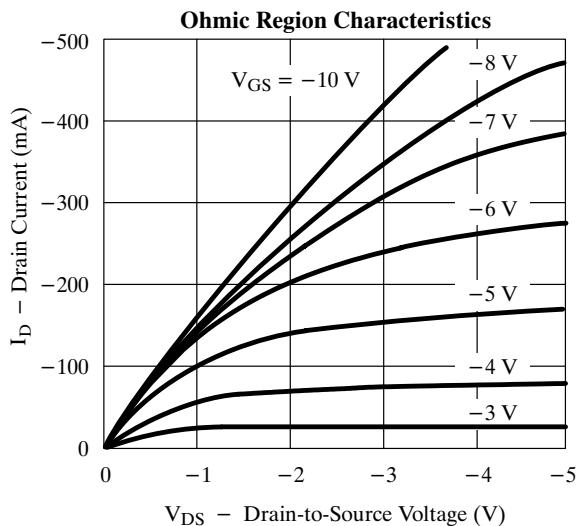
Parameter	Symbol	Test Conditions	Typ ^b	Limits						Unit	
				TP0610L/T		VP0610L/T		BS250			
Min	Max	Min	Max	Min	Max	Min	Max	Min	Max		
Static											
Drain-Source Breakdown Voltage	V _{(BR)D} SS	V _{GS} = 0 V, I _D = -10 µA	-70	-60		-60				V	
		V _{GS} = 0 V, I _D = -100 µA						-45			
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -1 mA	-1.9	-1	-2.4	-1	-3.5	-1	-3.5		
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±10		±10			nA	
		T _J = 125°C			±50						
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -48 V, V _{GS} = 0 V			-1		-1			µA	
		T _J = 125°C			-20	0	-20	0			
		V _{DS} = -25 V, V _{GS} = 0 V							-0.5		
On-State Drain Current ^c	I _{D(on)}	V _{DS} = -10 V, V _{GS} = -4.5 V	-180	-50						mA	
		V _{DS} = -10 V V _{GS} = -10 V	L	-750			-60	0			
Drain-Source On-Resistance ^c	r _{DS(on)}		T				-22	0		Ω	
	V _{GS} = -4.5 V, I _D = -25 mA	11		25							
	V _{GS} = -10 V I _D = -0.5 A	L	8		10		10				
Forward Transconductance ^c	g _{fs}	T _J = 125°C	15		20		20			mS	
			T	6.5		10		10			
		V _{DS} = -10 V I _D = -0.2 A							14		
Forward Transconductance ^c	g _{fs}	V _{DS} = -10 V, I _D = -0.5 A	L	125	80		80			mS	
		V _{DS} = -10 V I _D = -0.1 A	T	90	60		70				
Diode Forward Voltage	V _{SD}	I _S = -0.5 A, V _{GS} = 0 V	-1.1							V	
Dynamic											
Input Capacitance	C _{iss}	V _{DS} = -25 V, V _{GS} = 0 V f = 1 MHz	15		60		60			pF	
Output Capacitance	C _{oss}		10		25		25				
Reverse Transfer Capacitance	C _{rss}		3		5		5				
Switching^d											
Turn-On Time	t _{ON}	V _{DD} = -25 V, R _L = 133 Ω I _D ≈ -0.18 A, V _{GEN} = -10 V R _G = 25 Ω	8						10	ns	
	t _{d(on)}		6		10		10				
	t _r		10		15		15				
Turn-Off Time	t _{OFF}		8						10		
	t _{d(off)}		7		15		15				
	t _f		8		20		20				

Notes

- a. T_A = 25°C unless otherwise noted.
- b. For DESIGN AID ONLY, not subject to production testing.
- c. Pulse test: PW ≤ 300 µs duty cycle ≤ 2%.
- d. Switching time is essentially independent of operating temperature.

VPDS06

Typical Characteristics (25°C Unless Otherwise Noted)



TP0610L/T, VP0610L/T, BS250

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Typical Characteristics (25°C Unless Otherwise Noted) (Cont'd)

