The RF Line

Microwave Pulse Power Transistors

... designed for Class B and C common base amplifier applications in short and long pulse TACAN, IFF, DME, and radar transmitters.

- Guaranteed Performance @ 1090 MHz, 50 Vdc Output Power = 15 Watts Peak Minimum Gain = 10 dB
- 100% Tested for Load Mismatch at All Phase Angles with 10:1 VSWR
- · Industry Standard Package
- Nitride Passivated
- Gold Metallized, Emitter Ballasted for Long Life and Resistance to Metal Migration
- · Internal Input Matching for Broadband Operation
- Circuit board photomaster available upon request by contacting RF Tactical Marketing in Phoenix, AZ.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCES	60	Vdc
Collector-Base Voltage	VCBO	60	Vdc
Emitter–Base Voltage	VEBO	4.0	Vdc
Collector Current — Continuous	IC	1.0	Adc
Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C	PD	17.5 100	Watts mW/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

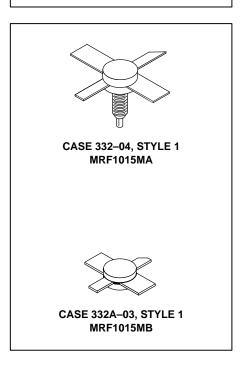
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case (2)	$R_{\theta JC}$	10	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

MRF1015MA MRF1015MB

15 W (PEAK), 960-1215 MHz MICROWAVE POWER TRANSISTORS NPN SILICON



Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I _C = 10 mAdc, V _{BE} = 0)	V(BR)CES	60	_	_	Vdc
Collector–Base Breakdown Voltage (I _C = 10 mAdc, I _E = 0)	V(BR)CBO	60	_	_	Vdc
Emitter–Base Breakdown Voltage (IE = 1.0 mAdc, IC = 0)	V(BR)EBO	4.0	_	_	Vdc
Collector Cutoff Current (V _{CB} = 50 Vdc, I _E = 0)	ICBO	_	_	1.0	mAdc
ON CHARACTERISTICS					
DC Current Gain (I _C = 250 mAdc, V _{CE} = 5.0 Vdc)	hFE	10	40	100	_

NOTES:

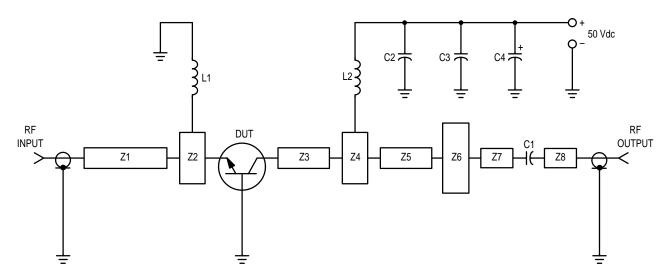
(continued)

- 1. These devices are designed for RF operation. The total device dissipation rating applies only when the device is operated as RF amplifiers.
- 2. Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques.



ELECTRICAL CHARACTERISTICS — **continued** $(T_C = 25^{\circ}C)$ unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
DYNAMIC CHARACTERISTICS					
Output Capacitance (V _{CB} = 50 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	5.0	7.5	pF
FUNCTIONAL TESTS (Pulse Width = 10 μs, Duty Cycle = 1.0%)					
Common-Base Amplifier Power Gain (V _{CC} = 50 Vdc, P _{out} = 15 W Peak, f = 1090 MHz)	GPB	10	12.5	_	dB
Collector Efficiency (V _{CC} = 50 Vdc, P _{out} = 15 W Peak, f = 1090 MHz)	η	30	35	_	%
Load Mismatch (V _{CC} = 50 Vdc, P _{Out} = 15 W Peak, f = 1090 MHz, VSWR = 10:1 All Phase Angles)	Ψ	No Degradation in Power Output		out	



C1, C2 — 220 pF 100 mil Chip Capacitor C3 — 0.1 μ F C4 — 47 μ F/75 V Electrolytic Capacitor L1, L2 — 3 Turns #18 AWG, 1/8" ID Z1–Z8 — Microstrip, See Photomaster Board Material — 0.032" Glass Teflon ϵ_Γ = 2.5

Figure 1. 1090 MHz Test Circuit

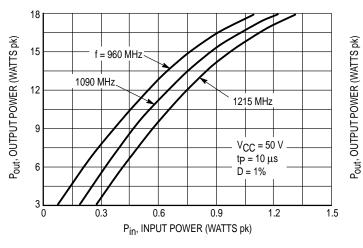
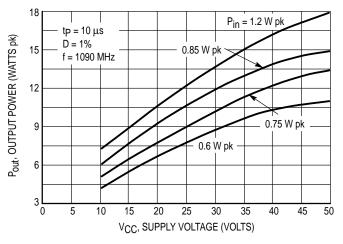


Figure 2. Output Power versus Input Power

Figure 3. Output Power versus Frequency



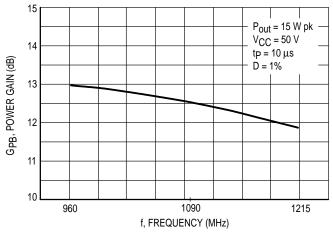
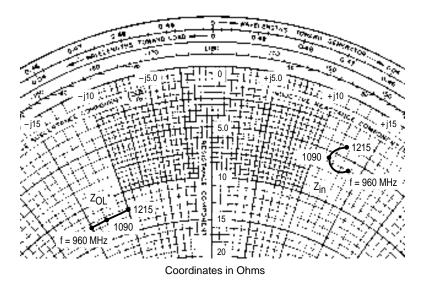


Figure 4. Output Power versus Supply Voltage

Figure 5. Power Gain versus Frequency



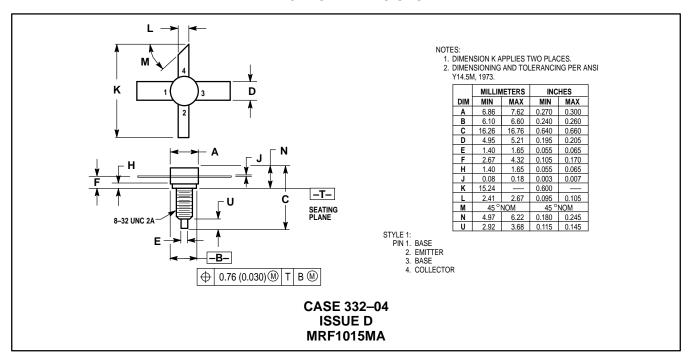
 $P_{out} = 15 \text{ W pk}$ $V_{CC} = 50 \text{ V}$ $t_p = 10 \text{ µs}$ D = 1%

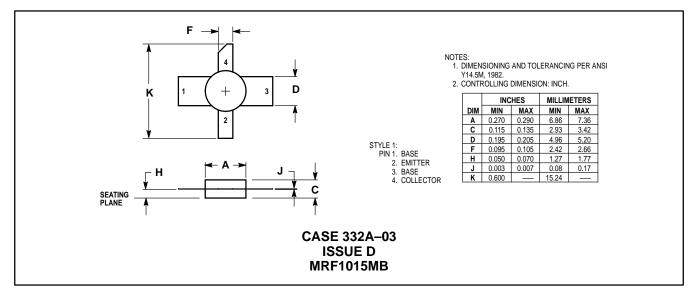
f MHz	Z _{in} Ohms	Z _{OL} * Ohms
960	5.9 + j13.6	12.5 – j15
1090	5.5 + j11.5	12.4 – j12.8
1215	4.0 + j12.5	12.1 – j10

Z_{OL}* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage, and frequency.

Figure 6. Series Equivalent Input/Output Impedances

PACKAGE DIMENSIONS





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