BUV90F

GENERAL DESCRIPTION

High-voltage, monolithic npn power Darlington transistor in a SOT199 envelope intended for use in car ignition systems, DC and AC motor controls, solenoid drivers, etc.

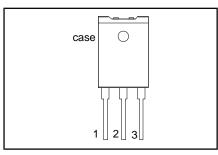
QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _{CESM}	Collector-emitter voltage peak value	$V_{BF} = 0 \text{ V}$	-	650	V
V _{CEO}	Collector-emitter voltage (open base)		-	400	V
I _C	Collector current (DC)		-	12	Α
1 1	Collector current peak value		-	30	Α
P _{tot}	Total power dissipation	$T_{hs} \le 25 ^{\circ}C$	-	34	W
V _{CEsat}	Collector-emitter saturation voltage	$I_{C} = 5 \text{ A}$: $I_{R} = 0.05 \text{ A}$	-	1.5	V
V _{CEsat}	Collector-emitter saturation voltage	$I_{\rm C} = 10 {\rm A}; I_{\rm B} = 0.3 {\rm A}$	-	2	V
Csat	Collector saturation current		10		Α
t _f	Fall time	$I_{\rm C} = 5 \text{ A}; I_{\rm B(op)} = 50 \text{ mA}$	0.7	-	μs
l t _r	Fall time	$I_{C} = 5 \text{ A}; I_{B(on)} = 50 \text{ mA}$ $I_{C} = 10 \text{ A}; I_{B(on)} = 300 \text{ mA}$	1	-	μs

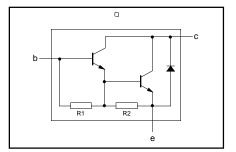
PINNING - SOT199

DESCRIPTION	
base	
collector	
emitter	
isolated	

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0 V$	-	650	V
V_{CEO}	Collector-emitter voltage (open base)		-	400	V
E _(BR)	Turn-off breakdown energy with	$I_C = 10 \text{ A}; I_{B(on)} = 0.3 \text{ A}; L_C = 8 \text{ mH}$	-	400	mJ
(5.1)	inductive load	2(01)			
I _C	Collector current (DC)		-	12	Α
I _{CM}	Collector current peak value		-	30	Α
I _B	Base current (DC)		-	4	Α
1 1	Base current peak value		-	6	Α
P _{tot}	Total power dissipation	$T_{hs} \leq 25 ^{\circ}C$	-	34	W
T _{stg}	Storage temperature		-65	150	°C
$T_{j}^{s,s}$	Junction temperature		-	150	°C

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
R _{th j-hs}	Junction to heatsink	without heatsink compound	-	3.7	K/W
R _{th j-hs}	Junction to heatsink	with heatsink compound	-	2.8	K/W

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ISOLATION

 T_{hs} = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Repetitive peak voltage from all three terminals to external	R.H. ≤ 65 % ; clean and dustfree	-	-	2500	V
C _{isol}	heatsink Capacitance from T2 to external heatsink	f = 1 MHz	-	22	-	pF

STATIC CHARACTERISTICS

 T_{hs} = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CES}	Collector cut-off current 1	$V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax}$	-	-	1.0	mΑ
I _{CES}		$egin{array}{l} V_{BE} = 0 \ V; \ V_{CE} = V_{CESMmax} \ V_{BE} = 0 \ V; \ V_{CE} = V_{CESMmax}; \ T_i = 125 \ ^{\circ}C \end{array}$	-	-	3.0	mA
I _{EBO}	Emitter cut-off current	$V_{EB} = 6 \text{ V}; I_C = 0 \text{ A}$	-	_	20	mA
R1	Base-emitter resistor - driver		-	500	-	Ω
R2	transistor. Base-emitter resistor - output		_	500	_	Ω
	transistor.					
V_{F}	Diode forward voltage	$I_{F} = 8 \text{ A}; I_{B} = 0 \text{ A}$	-	-	3	V
V _{CEOsust}	Collector-emitter sustaining voltage	$I_{B} = 0 \text{ A}; I_{C} = 100 \text{ mA};$ L = 25 mH	400	-	-	V
V_{CEsat}	Saturation voltages	$I_{\rm C} = 5 \text{ A}; I_{\rm B} = 0.05 \text{ A}$	-	-	1.5	V
V _{BEsat}	_		-	-	2.0	V
V _{CEsat}		$I_{\rm C} = 6 \text{ A}; I_{\rm B} = 0.1 \text{ A};$	-	-	1.5	V
V _{BEsat}		$I_{C} = 6 \text{ A}; I_{B} = 0.1 \text{ A};$ $T_{hs} = 150 ^{\circ}\text{C}$	-	-	2.0	V
V _{CEsat}		$I_{\rm C} = 10 \text{A}; I_{\rm B} = 0.3 \text{A}$	-	-	2.0	V
V _{BEsat}		-	-	-	2.5	V

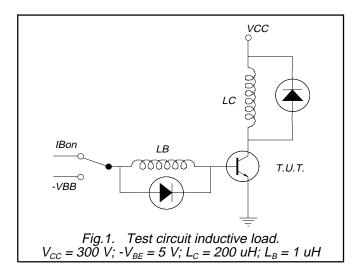
DYNAMIC CHARACTERISTICS

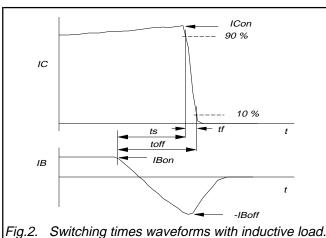
 T_{hs} = 25 $^{\circ}$ C unless otherwise specified

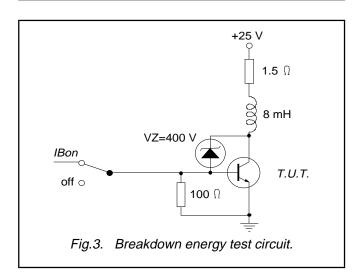
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Switching times inductive load					
l t _f	Turn-off fall time	$I_{\rm C} = 5 \text{ A}; I_{\rm B(on)} = 50 \text{ mA}$	-	0.7	-	μs
l t _f	Turn-off fall time	$I_{\rm C} = 10 \text{Å}; I_{\rm B(on)} = 300 \text{mA}$	-	1	-	μs

¹ Measured with half sine-wave voltage (curve tracer).

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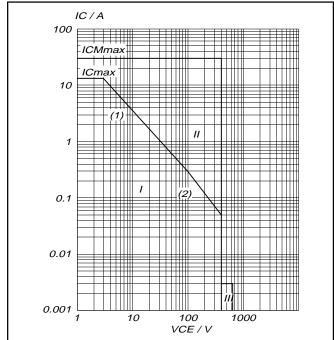
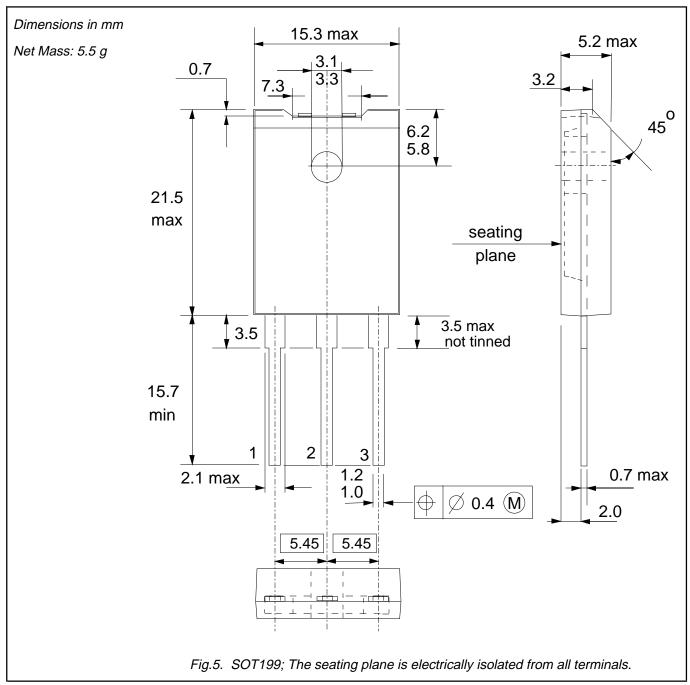


Fig.4. Forward bias safe operating area. T_{hs} = 25 °C

- Region of permissible DC operation. Extension for repetitive pulse operation. II
- Repetitive pulse operation is permissible in this region provided $V_{\rm BE} < 0$ and III
- $t_{p} < 5$ ms. $P_{\text{tot max}}$ line. Second breakdown limits (independent (2)
- of temperature.
- NB: Mounted without heatsink compound and 30 ± 5 newton force on the centre of the envelope.

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MECHANICAL DATA



Notes

1. Accessories supplied on request: refer to mounting instructions for F-pack envelopes.

Philips Semiconductors Product specification

Silicon Diffused Darlington Power Transistor

BUV90F

DEFINITIONS

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			

Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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