Active Errata List

- **PGM: PSCxRB Fuse**
- **PSC: Prescaler**
- **PSC: PAOCnA and PAOCnB Register Bits (Asynchronous output control)**
- **PSC: PEVxA/B Flag Bits**
- **PSC : Output Polarity in Centered Mode**
- **PSC : Output Activity**
- **VREF**
- **DALI**
- **DAC: Register Update**

Errata History

Mask Revision	Lot Number	Errata List
REV A	04103x	1, 2, 3, 4, 5, 6, 7, 8, 9

Errata Description

1. **PGM: PSCxRB Fuse**

   The use of PSCXRB fuse can make the ISP fail.

   Workaround:

   When PSCxRB fuses are used, use the parallel programming mode to load a new program version.

2. **PSC: Prescaler**

   The use of PSC's prescaler blocks the sample of PSC inputs during the two first cycles following the set of PSC run bit.

   Workaround:

   Clear the prescaler PPREx bit when stopping the PSC (prun = 0), and set them to appropriate value when starting the PSC (prun = 1), these bits are in the same PCTL register.

3. **PSC: PAOCnA and PAOCnB Register Bits (Asynchronous output control)**

   These register bits are malfunctioning.

   Workaround:

   Do not use this feature.

4. **PSC: PEVxA/B flag bits**

   These flags are set when a fault arises, but can also be set during the fault itself.

   Workaround:

   Don't clear these flags before the fault disappears.

5. **PSC: Output Polarity in Centered Mode**

   In centered mode, PSCOUTn1 outputs are not inverted, so they are active at the same time as PSCOUTn0.

   Workaround:

   Use an external inverter (or a driver with inverting output) to drive the load on PSCOUTn1.

AVR MCUs

AT90PWM2
AT90PWM3

Errata Sheet

4437A–AVR–03/05

6. **PSC : POACnA/B Output Activity**

   These register bits are not implemented in rev A.

   Workaround:

   Do not use this feature.

7. **VREF**

   Remark: To have Internal Vref on AREF pin select an internal analog feature such as DAC or ADC.

8. **DALI**

   Some troubles on Dali extension when edges are not symmetric.

   Workaround:

   Use an optocoupler providing symmetric edges on Rx and Tx DALI lines.

9. **DAC: Register Update**

   Registers DACH & DACH are not written when the DAC is not enabled.

   Workaround:

   Enable DAC with DAEN before writing in DACL & DACH. To prevent an unwanted zero output on DAC pin, enable DAC output, with DAOE afterwards.

Atmel Corporation

2325 Orchard Parkway
San Jose, CA 95131, USA
Tel: 1(408) 441-0311
Fax: 1(408) 487-2600

Regional Headquarters

Europe
Atmel Sarl
Route des Arsenaux 41
Case Postale 80
CH-1705 Fribourg
Switzerland
Tel: (41) 26-426-5555
Fax: (41) 26-426-5500

Asia
Room 1219
Chinachem Golden Plaza
77 Mody Road Tsimshatsui
East Kowloon
Hong Kong
Tel: (852) 2721-9778
Fax: (852) 2722-1369

Japan
9F, Tonetsu Shinkawa Bldg.
1-24-8 Shinkawa
Chuo-ku, Tokyo 104-0033
Japan
Tel: (81) 3-3523-3551
Fax: (81) 3-3523-7581

Atmel Operations

Memory
2325 Orchard Parkway
San Jose, CA 95131, USA
Tel: 1(408) 441-0311
Fax: 1(408) 436-4314

Microcontrollers
2325 Orchard Parkway
San Jose, CA 95131, USA
Tel: 1(408) 441-0311
Fax: 1(408) 436-4314

La Chantrerie
BP 70602
44306 Nantes Cedex 3, France
Tel: (33) 2-40-18-18-18
Fax: (33) 2-40-18-19-60

ASIC/ASSP/Smart Cards
Zone Industrielle
13106 Rousset Cedex, France
Tel: (33) 4-42-53-60-00
Fax: (33) 4-42-53-60-01

1150 East Cheyenne Mtn. Blvd.
Colorado Springs, CO 80906, USA
Tel: 1(719) 576-3300
Fax: 1(719) 540-1759

Scottish Enterprise Technology Park
Maxwell Building
East Kilbride G75 0QR, Scotland
Tel: (44) 1355-803-000
Fax: (44) 1355-242-743

RF/Automotive
Theresienstrasse 2
Postfach 3535
74025 Heilbronn, Germany
Tel: (49) 71-31-67-0
Fax: (49) 71-31-67-2340

1150 East Cheyenne Mtn. Blvd.
Colorado Springs, CO 80906, USA
Tel: 1(719) 576-3300
Fax: 1(719) 540-1759

Biometrics/Imaging/Hi-Rel MPU/
High Speed Converters/RF Datacom
Avenue de Rochepleine
BP 123
38521 Saint-Egreve Cedex, France
Tel: (33) 4-76-58-30-00
Fax: (33) 4-76-58-34-80

Literature Requests
www.atmel.com/literature

4437A–AVR–03/05