1-of-10 Decoder/Driver Open-Collector

The SN74LS145, 1-of-10 Decoder/Driver, is designed to accept BCD inputs and provide appropriate outputs to drive 10-digit incandescent displays. All outputs remain off for all invalid binary input conditions. It is designed for use as indicator/relay drivers or as an open-collector logic circuit driver. Each of the high breakdown output transistors will sink up to 80 mA of current. Typical power dissipation is 35 mW. This device is fully compatible with all TTL families.

- Low Power Version of 74145
- Input Clamp Diodes Limit High Speed Termination Effects

GUARANTEED OPERATING RANGES

Symbol	Parameter	Min	Тур	Max	Unit
V _{CC}	Supply Voltage	4.75	5.0	5.25	V
T _A	Operating Ambient Temperature Range	0	25	70	°C
V _{OH}	Output Voltage – High			15	V
I _{OL}	Output Current – Low			24	mA



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> LOW POWER SCHOTTKY



PLASTIC N SUFFIX CASE 648



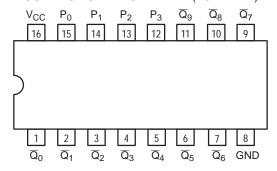
SOIC D SUFFIX CASE 751B

ORDERING INFORMATION

Device	Package	Shipping
SN74LS145N	16 Pin DIP	2000 Units/Box
SN74LS145D	16 Pin	2500/Tape & Reel

1

CONNECTION DIAGRAM DIP (TOP VIEW)



LOADING (Note a)

PIN NAMES

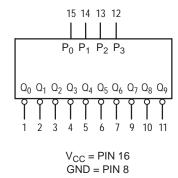
 $\begin{array}{ll} \underline{P_0}, \, \underline{P_1}, \, \underline{P_2}, \, \underline{P_3} & \quad \text{BCD Inputs} \\ \overline{Q_0} - \overline{Q_9} & \quad \text{Outputs} \end{array}$

HIGH	LOW
0.5 U.L.	0.25 U.L.
Open Collector	15 U.L.

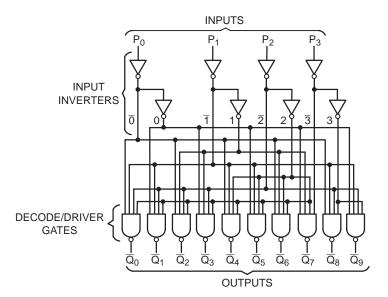
NOTES:

a) 1 TTL Unit Load (U.L.) = 40 μ A HIGH/1.6 mA LOW.

LOGIC SYMBOL



LOGIC DIAGRAM



TRUTH TABLE

	INP	UTS						OUT	PUTS				
P ₃	P ₂	P ₁	P ₀	\overline{Q}_0	\overline{Q}_1	\overline{Q}_2	\overline{Q}_3	\overline{Q}_4	\overline{Q}_{5}	\overline{Q}_6	\overline{Q}_7	Q ₈	\overline{Q}_9
L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н
L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н
L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н
L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Н	Н
L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	н
L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	н
L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н
Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	Н
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L
Н	L	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	н
Н	Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	н
Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н

H = HIGH Voltage Level L = LOW Voltage Level

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

			Limits					
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions		
V _{IH}	Input HIGH Voltage	2.0			V	Guaranteed Input HIGH Voltage for All Inputs		
V _{IL}	Input LOW Voltage			0.8	V	Guaranteed Input LOW Voltage for All Inputs		
V _{IK}	Input Clamp Diode Voltage		-0.65	-1.5	V	V _{CC} = MIN, I _{IN} = -18 mA		
I _{OH}	Output HIGH Current			250	μΑ	V _{CC} = MIN, V _{OH} = MAX		
			0.25	0.4	V	I _{OL} = 12 mA	$V_{CC} = V_{CC} MIN,$	
V _{OL}	Output LOW Voltage		0.35	0.5	V	I _{OL} = 24 mA	$V_{IN} = V_{IL}$ or V_{IH}	
			2.3	3.0	V	I _{OL} = 80 mA	per Truth Table	
l	Input HIGH Current			20	μΑ	$V_{CC} = MAX, V_{IN} = 2.7 V$		
l _{IH}	input nigh Current			0.1	mA	V _{CC} = MAX, V _{IN} = 7.0 V		
I _{IL}	Input LOW Current			-0.4	mA	V _{CC} = MAX, V _{IN} = 0.4 V		
Icc	Power Supply Current			13	mA	V _{CC} = MAX, V _{IN}	= GND	

AC CHARACTERISTICS $(T_A = 25^{\circ}C)$

			Limits				
l	Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
	t _{PHL} t _{PLH}	Propagation Delay P _n Input to Q _n Output			50 50	ns	$V_{CC} = 5.0 \text{ V}$ $C_{L} = 45 \text{ pF}$

AC WAVEFORMS

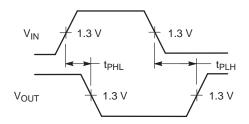


Figure 1.

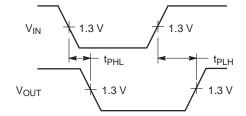
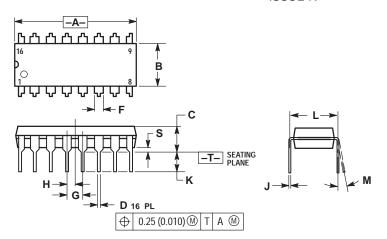


Figure 2.

PACKAGE DIMENSIONS

N SUFFIX PLASTIC PACKAGE CASE 648-08 ISSUE R

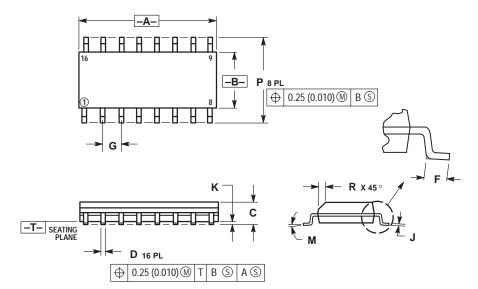


- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
 4. DIMENSION B DOES NOT INCLUDE MOLD FLASH.
 5. ROUNDED CORNERS OPTIONAL.

	INC	HES	MILLIN	IETERS	
DIM	M MIN MAX		MIN	MAX	
Α	0.740	0.770	18.80	19.55	
В	0.250	0.270	6.35	6.85	
С	0.145	0.175	3.69	4.44	
D	0.015	0.021	0.39	0.53	
F	0.040	0.70	1.02	1.77	
G	0.100	BSC	2.54 BSC		
Н	0.050	BSC	1.27 BSC		
J	0.008	0.015	0.21	0.38	
K	0.110	0.130	2.80	3.30	
L	0.295 0.305		7.50	7.74	
М	0°	10 °	0°	10 °	
S	0.020	0.040	0.51	1.01	

PACKAGE DIMENSIONS

D SUFFIX PLASTIC SOIC PACKAGE CASE 751B-05 **ISSUE J**



NOTES:

- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: MILLIMETER.

 3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.

 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.

 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIN	IETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	9.80	10.00	0.386	0.393	
В	3.80	4.00	0.150	0.157	
С	1.35	1.75	0.054	0.068	
D	0.35	0.49	0.014	0.019	
F	0.40	1.25	0.016	0.049	
G	1.27	BSC	0.050	BSC	
J	0.19	0.25	0.008	0.009	
K	0.10	0.25	0.004	0.009	
M	0 °	7°	0 °	7°	
Р	5.80	6.20	0.229	0.244	
R	0.25	0.50	0.010	0.019	

Notes

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