Product Preview

TMOS E-FET TM

Power Field Effect Transistor

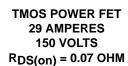
N-Channel Enhancement-Mode Silicon Gate

This advanced TMOS E-FET is designed to withstand high energy in the avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for low voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional safety margin against unexpected voltage transients.

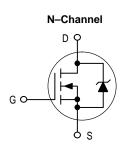
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- · Diode is Characterized for Use in Bridge Circuits
- IDSS and VDS(on) Specified at Elevated Temperature

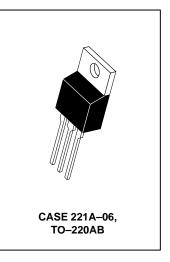






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MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	VDSS	150	Vdc
Drain-to-Gate Voltage (R _{GS} = 1.0 MΩ)	VDGR	150	Vdc
Gate–to–Source Voltage — Continuous — Non–Repetitive (t _p ≤ 10 ms)	V _{GS} V _{GSM}	±20 ±40	Vdc Vpk
Drain Current — Continuous — Continuous @ 100°C — Single Pulse (t _p ≤ 10 μs)	I _D I _D	29 19 102	Adc Apk
Total Power Dissipation Derate above 25°C	PD	125 1.0	Watts W/°C
Operating and Storage Temperature Range	TJ, T _{stg}	-55 to 150	°C
Single Pulse Drain–to–Source Avalanche Energy — STARTING T _J = 25° C (V _{DD} = 25 Vdc, V _{GS} = 10 Vdc, PEAK I _L = 29 Apk, L = 1.0 mH, R _G = 25 Ω)	EAS	421	mJ
Thermal Resistance — Junction to Case — Junction to Ambient	R _θ JC R _θ JA	1.0 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C

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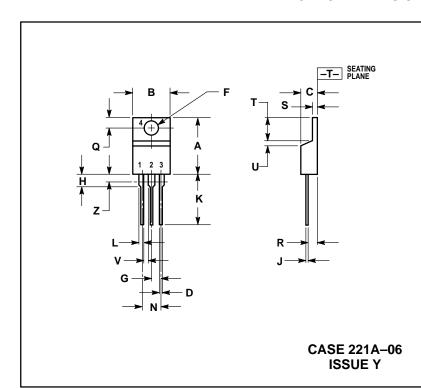
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ELECTRICAL CHARACTERISTICS ($T_J = 25$ °C unless otherwise noted)

Cha	racteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS				•	•	'
Drain-to-Source Breakdown Volta (V _{GS} = 0 Vdc, I _D = 0.25 mAdc) Temperature Coefficient (Positive		V(BR)DSS	150 —	— TBD	_	Vdc mV/°C
Zero Gate Voltage Drain Current (VDS = 150 Vdc, VGS = 0 Vdc) (VDS = 150 Vdc, VGS = 0 Vdc, TJ = 125°C)		IDSS	_ _ _	_	10 100	μAdc
Gate-Body Leakage Current (VGS	= ±20 Vdc, V _{DS} = 0 Vdc)	IGSS	_	_	100	nAdc
ON CHARACTERISTICS (1)						
Gate Threshold Voltage (V _{DS} = V _{GS} , I _D = 250 μAdc) Threshold Temperature Coefficie	nt (Negative)	V _{GS(th)}	2.0 —	2.7 TBD	4.0 —	Vdc mV/°C
Static Drain-to-Source On-Resista (V _{GS} = 10 Vdc, I _D = 14.5 Adc)	ance	R _{DS(on)}	1	0.055	0.07	Ohms
Drain-to-Source On-Voltage (V _{GS} = 10 Vdc, I _D = 29 Adc) (V _{GS} = 10 Vdc, I _D = 14.5 Adc, T _J = 125°C)		V _{DS(on)}	1 1	_	2.4 2.1	Vdc
Forward Transconductance (VDS =	= 8.6 Vdc, I _D = 14.5 Adc)	9FS	10	18	_	mhos
DYNAMIC CHARACTERISTICS						
Input Capacitance		C _{iss}	_	2250	3150	pF
Output Capacitance	$(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$	C _{oss}	_	455	910]
Transfer Capacitance	,,	C _{rss}	_	133	190	1
SWITCHING CHARACTERISTICS (2)					•
Turn-On Delay Time		^t d(on)	_	17.5	40	ns
Rise Time	(V _{DD} = 75 Vdc, I _D = 29 Adc,	t _r	_	108	220	
Turn-Off Delay Time	$V_{GS} = 10 \text{ Vdc},$ $R_{G} = 9.1 \Omega)$	td(off)	_	90	180	
Fall Time	_	t _f	_	85	170	1
Gate Charge		QT	_	78	110	nC
	(V _{DS} = 120 Vdc, I _D = 29 Adc,	Q ₁	_	12	_	1
	V _{GS} = 10 Vdc)	Q ₂	_	37	_	1
		Q ₃	_	23	_	1
SOURCE-DRAIN DIODE CHARAC	TERISTICS					
Forward On–Voltage	(I _S = 29 Adc, V _{GS} = 0 Vdc) (I _S = 29 Adc, V _{GS} = 0 Vdc, T _J = 125°C)	V _{SD}	_	0.92 TBD	1.3 —	Vdc
Reverse Recovery Time		t _{rr}	_	174	_	ns
	$(I_S = 29 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, \\ dI_S/dt = 100 \text{ A}/\mu \text{s})$	t _a	_	140	_	
		t _b	_	34	_	
Reverse Recovery Stored Charge		Q _{RR}	_	1.4	_	μС
INTERNAL PACKAGE INDUCTANO	E			•	•	•
Internal Drain Inductance (Measured from contact screw o (Measured from the drain lead 0.	n tab to center of die) 25" from package to center of die)	L _D	_ 	3.5 4.5	_	nH
Internal Source Inductance (Measured from the source lead 0.25" from package to source bond pad)		LS	_	7.5	_	

⁽¹⁾ Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
(2) Switching characteristics are independent of operating junction temperature.

PACKAGE DIMENSIONS



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES MILLIMETER			IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
Г	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
Т	0.235	0.255	5.97	6.47
C	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

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