Product Preview

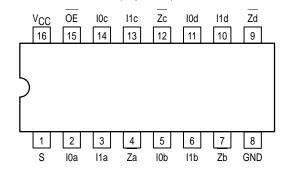
Low-Voltage CMOS Quad 2-Input Multiplexer With 5V-Tolerant Inputs and Outputs (3-State, Inverting)

The MC74LCX258 is a high performance, quad 2–input inverting multiplexer with 3–state outputs operating from a 2.7 to 3.6V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A V $_{\parallel}$ specification of 5.5V allows MC74LCX258 inputs to be safely driven from 5V devices.

Four bits of data from two sources can be selected using the Select input. The four outputs present the selected data in the inverted form. The outputs may be switched to <u>a high</u> impedance state by placing a logic HIGH on the Output Enable (OE) input. Current drive capability is 24mA at the outputs.

- Designed for 2.7 to 3.6V VCC Operation
- 5V Tolerant Interface Capability With 5V TTL Logic
- · Supports Live Insertion and Withdrawal
- IOFF Specification Guarantees High Impedance When VCC = 0V
- LVTTL Compatible
- LVCMOS Compatible
- 24mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current in All Three Logic States (10μA) Substantially Reduces System Power Requirements
- Latchup Performance Exceeds 500mA
- ESD Performance: Human Body Model >2000V; Machine Model >200V

Pinout: 16-Lead Plastic Package (Top View)



MC74LCX258



LOW-VOLTAGE CMOS
QUAD 2-INPUT MULTIPLEXER



D SUFFIX

PLASTIC SOIC CASE 751B-05



M SUFFIX

PLASTIC SOIC EIAJ CASE 966-01



SD SUFFIX

PLASTIC SSOP CASE 940B-03



DT SUFFIX

PLASTIC TSSOP CASE 948F-01

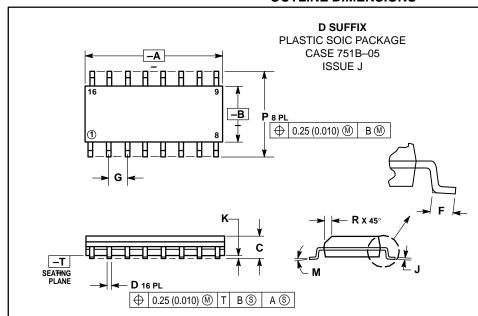
PIN NAMES

Pins	Function
10n	Source 0 Data Inputs
11n	Source 1 Data Inputs
OE	Output Enable Input
S	Select Input
Zn	Outputs

This document contains information on a product under development. Motorola reserves the right to change or discontinue this product without notice.



OUTLINE DIMENSIONS



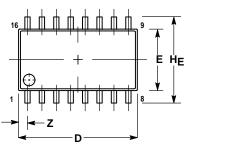
NOTES:

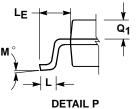
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: MILLIMETER.
- DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION. 3.
- MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
- PER SIDE.
 DIMENSION D DOES NOT INCLUDE DAMBAR
 PROTRUSION. ALLOWABLE DAMBAR
 PROTRUSION SHALL BE 0.127 (0.005) TOTAL
 IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

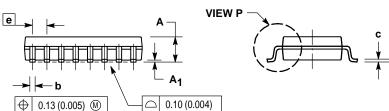
	MILLIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	9.80	10.00	0.386	0.393
В	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27 BSC		0.050 BSC	
J	0.19	0.25	0.008	0.009
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
Р	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

M SUFFIX

PLASTIC SOIC EIAJ PACKAGE CASE 966-01 ISSUE O







- 1 DIMENSIONING AND TOLERANCING PER ANSI
 - CONTROLLING DIMENSION: MILLIMETER.
- CONTROLLING DIMENSION: MILLIMETER.

 DIMENSIONS D AND E DO NOT INCLUDE MOLD
 FLASH OR PROTRUSIONS AND ARE MEASURED
 AT THE PARTING LINE. MOLD FLASH OR
 PROTRUSIONS SHALL NOT EXCEED 0.15 (0.006)
- PROTRUSIONS SHALL NOT EXCEED 0.15 (0.006)
 PER SIDE.

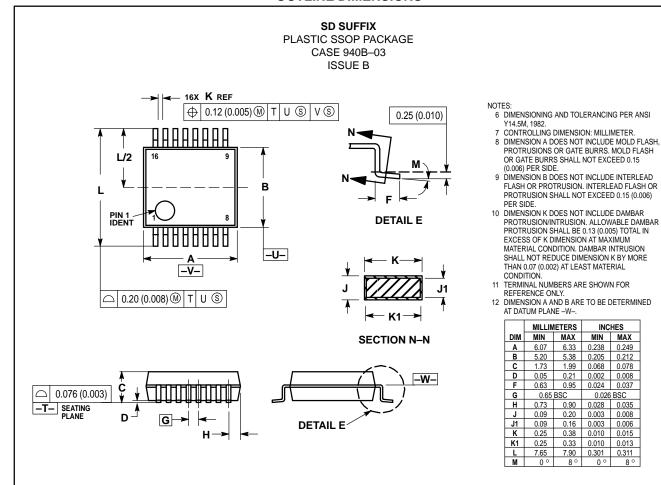
 4 TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.

 5 THE LEAD WIDTH DIMENSION (b) DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OR THE FOOT. MINIMUM SPACE BETWEEN PROTRUSIONS AND ADJACENT LEAD TO BE 0.46 (0.018). TO BE 0.46 (0.018).

	MILLIN	IETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α		2.05		0.081	
Α ₁	0.05	0.20	0.002	0.008	
b	0.35	0.50	0.014	0.020	
С	0.18	0.27	0.007	0.011	
D	9.90	10.50	0.390	0.413	
E	5.10	5.45	0.201	0.215	
е	1.27	BSC	0.050 BSC		
HE	7.40	8.20	0.291	0.323	
L	0.50	0.85	0.020	0.033	
LF	1.10	1.50	0.043	0.059	
M	0 °	10 °	0°	10°	
Q_1	0.70	0.90	0.028	0.035	
Z		0.78		0.031	

MOTOROLA 2

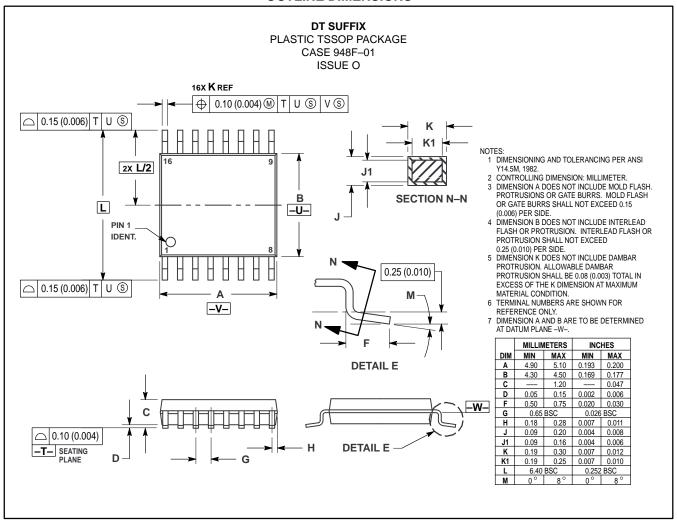
OUTLINE DIMENSIONS



3

MOTOROLA

OUTLINE DIMENSIONS



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MC74LCX258/D