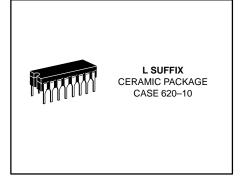
Quad 2-Input NOR Gate

ELECTRICAL CHARACTERISTICS

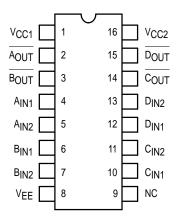
| | | −30°C | | +25°C | | +85°C | | |
|--------------------------------------|----------------|------------|------------|------------|------------|------------|------------|------|
| Characteristic | Symbol | Min | Max | Min | Max | Min | Max | Unit |
| Power Supply Drain Current | ΙE | ı | | | 56 | - | | mAdc |
| Input Current | linH | 1 | | | 350 | - | _ | μAdc |
| Switching Times Propagation Delay | t - + t + - | 0.6 0.6 | 1.6 1.8 | 0.6 0.6 | 1.5 1.7 | 0.6 0.6 | 1.7 1.9 | ns |
| Rise Time, Fall Time (10% to 90%) | t +, t - | 0.6 | 2.2 | 0.6 | 2.1 | 0.6 | 2.3 | ns |

LOGIC DIAGRAM A_{IN1} 2 A_{OUT} A_{IN2} B_{IN1} 6 3 BOUT B_{IN2} C_{IN1} 10 14 COUT C_{IN2} 11 D_{IN1} 12 15 DOUT D_{IN2} 13 OUT = IN1 + IN2V_{CC1} = PIN 1 V_{CC2} = PIN 16 V_{EE} = PIN 8 $t_{pd} = 0.9 \text{ ns typ (510 ohm load)}$ = 1.1 ns typ (50 ohm load) $P_D = 240 \text{ mW typ/pkg (No load)}$ Full Load Current, $I_L = -25 \text{ mAdc max}$

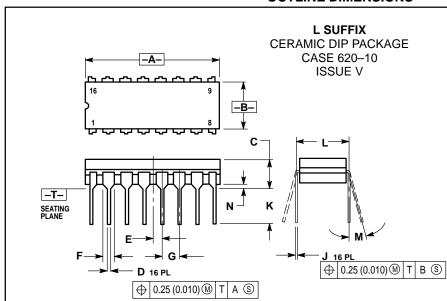
MC1662



PIN ASSIGNMENT



OUTLINE DIMENSIONS



NOTES:

- I. DIMENSIONING AND TOLERANCING PER
- ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
- 3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
- DIMENSION F MAY NARROW TO 0.76 (0.030)
 WHERE THE LEAD ENTERS THE CERAMIC RODY

| | INC | HES | MILLIMETERS | | | |
|-----|-------|-------|-------------|-------|--|--|
| DIM | MIN | MAX | MIN | MAX | | |
| Α | 0.750 | 0.785 | 19.05 | 19.93 | | |
| В | 0.240 | 0.295 | 6.10 | 7.49 | | |
| С | | 0.200 | | 5.08 | | |
| D | 0.015 | 0.020 | 0.39 | 0.50 | | |
| Е | 0.050 | BSC | 1.27 BSC | | | |
| F | 0.055 | 0.065 | 1.40 | 1.65 | | |
| G | 0.100 | BSC | 2.54 BSC | | | |
| Н | 0.008 | 0.015 | 0.21 | 0.38 | | |
| K | 0.125 | 0.170 | 3.18 | 4.31 | | |
| L | 0.300 | BSC | 7.62 BSC | | | |
| M | 0° | 15° | 0° | 15° | | |
| N | 0.020 | 0.040 | 0.51 | 1.01 | | |

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