MC145159-1

Serial-Input PLL Frequency Synthesizer with Analog Phase Detector

Interfaces with Dual–Modulus Prescalers

The MC145159–1 has a programmable 14–bit reference counter, as well as fully programmable divide–by–N/divide–by–A counters. The counters are programmed serially through a common data input and latched into the appropriate counter latch, according to the last data bit (control bit) entered.

When combined with a loop filter and VCO, this device can provide all the remaining functions for a PLL frequency synthesizer operating up to the device's frequency limit. For higher VCO frequency operations, a down mixer or a dual–modulus prescaler can be used between the VCO and the PLL.

- Operating Temperature Range: 40 to 85°C
- Low Power Consumption Through Use of CMOS Technology
- 3.0 to 9.0 V Supply Range
- On- or Off-Chip Reference Oscillator Operation
- Compatible with the Serial Peripheral Interface (SPI) on CMOS MCUs
- ÷ R Range = 3 to 16383
- ÷ N Range = 16 to 1023, ÷ A Range = 0 to 127
- High–Gain Analog Phase Detector
- See Application Note AN969

NOT RECOMMENDED FOR NEW DESIGNS; PRODUCT TO BE PHASED OUT. Please see the MC14519X and MC14520X series.

PIN ASSIGNMENTS

PLASTIC DIP AND SOG PACKAGE					SSOP		
R _O [1•	20	R _R	v _{ss} ′ D	1•	20 C _R	
osc _{in} [2	19	□ v _{dd} ⁄	APD _{out} [2	19 SR _{out}	
osc _{out} [3	18] с _н	с _н [3	18 🛛 ENB	
CHARGE	4	17	APD _{out}	V _{DD} ' [4	17 🛛 DATA	
v _{dd} C	5	16	□ v _{ss} ′	r _r [5	16 🛛 CLK	
FSO [6	15] C _R	R _O [6	15 🛛 f _{in}	
v _{ss} C	7	14	SR _{out}	osc _{in} [7	14 🛛 LD	
мс [8	13] ENB	OSC _{out} [8	13 🛛 MC	
LD [9	12] data	CHARGE [9	12 🛛 V _{SS}	
f _{in} [10	11	СГК	v _{dd} [10	11 🗍 FSO	



MC145159P1 MC145159DW1 MC145159VF1

Plastic DIP SOG Package SSOP



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