

MC145145-2

A 18-pin DIP package with pins numbered 1 to 18.

MC145145P2	Plastic DIP
MC145145DW2	SOG Package


- Closest equivalents are the MC145151–2, MC145170–1, MC14519X series, and MC14520X series.**

The block diagram illustrates the PLL circuitry. It features a 12-BIT R COUNTER and a 14-BIT N COUNTER. The R counter is driven by an oscillator (OSC_{in}) and provides feedback to the N counter. The N counter is driven by an input frequency (f_{in}). The output of the N counter is divided by 14 to produce the output frequency (f_{out}). The circuit also includes a LOCK DETECT block, a PHASE DETECTOR A, and a PHASE DETECTOR B. The phase detectors receive the reference frequency (f_R) and the feedback frequency (f_V) to generate phase-locked signals (φ_V and φ_R). The lock detect block monitors the lock status and outputs a LOCK DETECT signal (LD).

D1	1 ●	18	D2
D0	2	17	D3
f_{in}	3	16	REF _{out}
V _{SS}	4	15	ϕ_R
V _{DD}	5	14	ϕ_V
OSC _{in}	6	13	LD
OSC _{out}	7	12	PD _{out}
A0	8	11	ST
A1	9	10	A2

D1	1	20	D2
D0	2	19	D3
NC	3	18	REF _{out}
f _{in}	4	17	φ _R
V _{SS}	5	16	φ _V
V _{DD}	6	15	LD
OSC _{in}	7	14	PD _{out}
OSC _{out}	8	13	ST
A0	9	12	A2
A1	10	11	NC

NC = NO CONNECTION

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