MC143455RDK

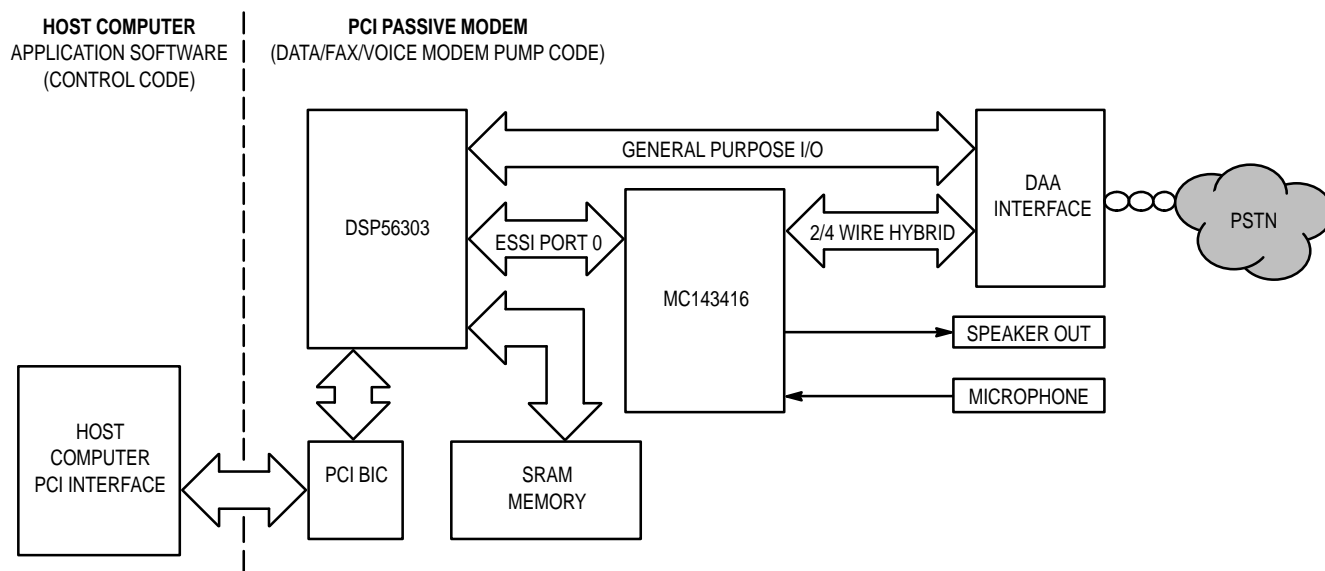*Product Preview*
PCI Passive Modem Reference Design Kit

The MC143455RDK PCI Passive Modem Reference Design Kit (RDK) was developed to facilitate the creation of unique Public Switched Telephone Network (PSTN) communication products using Motorola's DSP56303 24–Bit Digital Signal Processor (DSP) and Motorola's MC143416 Dual 16–Bit Linear Codec–Filter*. The critical component of this kit is an internal modem platform which provides a means of testing and evaluation of Motorola's 56K and V.34 1996 data/fax/voice modem activities.

In the passive modem design, modem control code operations are performed by the host computer, while the modem pump code for data/fax/voice applications are executed by the DSP56303. The application software uses the PCI interface to transmit/receive data to or from the DSP56303. All modem software is installed into the host computer and downloaded to the DSP's system memory through the PCI bus interface so that new modem features can be added. The DSP makes use of its built–in host port interface together with a PCI bus interface device to communicate directly with the host computer. Enhanced Synchronous Serial Interface (ESSI) communicates with the MC143416, which provides Analog–to–Digital (A/D) and Digital–to–Analog (D/A) conversions. The MC143416 is a single–chip implementation of the data/voice converters required for high–speed modems and features a telephony and acoustic codec, clock generation, signal conditioning, programmable sampling rates, built–in analog mixer, and speaker driver.

BLOCK DIAGRAM

* Codec = COder/DECoder

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Features

- Complete Passive Modem Turn–Key Design
- Programmable DSP and Memory System Consisting of SRAM for Software Upgradability
- Turn–Key Internationalization Support
- ITU–T V.80 (Video Ready), Enhanced Caller ID, PCI Bus Interface, and Distinctive Ring
- Data/Fax/Voice Modem with Host–Based Controller
- Operation System Support: Windows™ 95 and Windows NT™

Table 1. DSP Pump Features

Modem Type	Modulation/Function
Data Pump	K56flex™ Compatible Software Upgradable to ITU–T V.PCM ITU–T V.34 1996 — 33.6 kbps to 2.4 kbps ITU–T V.32bis — 14.4 kbps to 4.8 kbps ITU–T V.32 — 9.6 kbps to 4.8 kbps ITU–T V.22bis — 2.4 kbps ITU–T V.23 — 1.2 kbps ITU–T V.22 — 1.2 kbps ITU–T V.21 — 300 bps Automatic Mode Selection Automatic Rate Adaption Digital Near–End/Far–End Echo Cancellation ITU–T V.8 Signalling ITU–T V.8bis Signalling ITU–T V.54 Test Loopback Support Internationalization Support for Call Progress
Fax Pump	ITU–T V.17 — 14.4 kbps to 9.6 kbps ITU–T V.29 — 9.6 kbps to 4.8 kbps ITU–T V.27ter — 4.8 kbps and 2.4 kbps ITU–T V.21 Channel 2 — 300 bps
Voice Pump	Full–Duplex Speakerphone Pump with Automatic Gain Control and Room Monitor Answering Machine

Table 2. Host Control Features

Control Type	Control Code	AT Command Set
Data Control	ITU–T V.42/MNP®2–4 Error Correction ITU–T V.42bis/MNP5 Data Compression ITU–T V.80 Sync Access Support	Hayes™ AT Commands
Fax Control	TIA/EIA 578 Class 1 Fax Class 2 Fax ECM Fax T.30	AT+ Commands
Voice Control	Full–Duplex Speakerphone Controller Telephone Answering Machine Controller	AT# Voice Support

Documentation

More detailed documentation describing components and software is available from a local Motorola distributor or semiconductor sales office, or through a Motorola Literature Distribution Center.

Document Title	Order Number
MS143455SK PCI Passive Modem Chip Set and Software Product Preview	MS143455SKPP/D
MC143455RDK PCI Passive Modem Reference Design Kit Manual	MC143455RDK/D*
DSP56300 24–Bit Digital Signal Processor Family Manual	DSP56300FM/AD
DSP56303 24–Bit Digital Signal Processor User's Manual	DSP56303UM/AD
DSP56303 24–Bit Digital Signal Processor Technical Data	DSP56303/D
MC143416 Dual 16–Bit Linear Codec–Filter	MC143416/D

*Available December 1997

Note: For the most current information regarding this product, contact Motorola on the World Wide Web at http://www.motorola.com/modem–chipsets

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