# Four-Bit Universal Shift Register

The MC10H141 is a four-bit universal shift register. This device is a functional/pinout duplication of the standard MECL 10K part with 100% improvement in propagation delay and operation frequency and no increase in power supply current.

- Shift frequency, 250 MHz Min
- Power Dissipation, 425 mW Typical
- Improved Noise Margin 150 mV (over operating voltage and temperature range)
- Voltage Compensated
- MECL 10K-Compatible

#### **MAXIMUM RATINGS**

Characteristic	Symbol	Rating	Unit
Power Supply (V <sub>CC</sub> = 0)	V <sub>EE</sub>	-8.0 to 0	Vdc
Input Voltage (V <sub>CC</sub> = 0)	VI	0 to VEE	Vdc
Output Current — Continuous — Surge	lout	50 100	mA
Operating Temperature Range	T <sub>A</sub>	0 to +75	°C
Storage Temperature Range — Plastic — Ceramic	T <sub>stg</sub>	-55 to +150 -55 to +165	°C °C

# ELECTRICAL CHARACTERISTICS ( $V_{EE} = -5.2 \text{ V} \pm 5\%$ )

. == ,								
		<b>0</b> °		<b>25</b> °		<b>75</b> °		
Characteristic	Symbol	Min	Max	Min	Max	Min	Max	Unit
Power Supply Current	ΙE	_	112	_	102	-	112	mA
Input Current High Pins 5,6,9,11,12,13 Pins 7,10 Pin 4	<sup>l</sup> inH		405 416 510	  -  -	255 260 320	111	255 260 320	μА
Input Current Low	l <sub>inL</sub>	0.5	_	0.5		0.3		μΑ
High Output Voltage	Vон	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
Low Output Voltage	V <sub>OL</sub>	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
High Input Voltage	$V_{IH}$	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
Low Input Voltage	VIL	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc

# **AC PARAMETERS**

Propagation Delay	<sup>t</sup> pd	1.0	2.0	1.0	2.0	1.1	2.1	ns
Hold Time — Data, Select	<sup>t</sup> hold	1.0		1.0		1.0	_	ns
Set–up Time Data Select	t <sub>set</sub>	1.5 3.0		1.5 3.0		1.5 3.0		ns
Rise Time	t <sub>r</sub>	0.5	2.4	0.5	2.4	0.5	2.4	ns
Fall Time	t <sub>f</sub>	0.5	2.4	0.5	2.4	0.5	2.4	ns
Shift Frequency	<sup>f</sup> shift	250		250		250	_	MHz

# NOTE:

Each MECL 10H series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a 50 ohm resistor to -2.0 volts.

# MC10H141



# L SUFFIX

CERAMIC PACKAGE CASE 620-10



# P SUFFIX

PLASTIC PACKAGE CASE 648-08



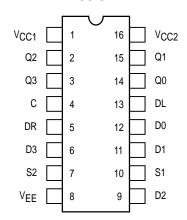
FN SUFFIX PLCC CASE 775–02

#### **TRUTH TABLE**

SELI	ECT	OPERATING	OUTPUTS				
S1	S2		Q0 <sub>n + 1</sub>	Q1 <sub>n + 1</sub>	Q2 <sub>n + 1</sub>	Q3 <sub>n + 1</sub>	
L	L	Parallel Entry	D0	D1	D2	D3	
L	Η	Shift Right*	Q1 <sub>n</sub>	Q2 <sub>n</sub>	Q3 <sub>n</sub>	DR	
Н	L	Shift Left*	DL	Q0 <sub>n</sub>	Q1 <sub>n</sub>	Q2 <sub>n</sub>	
Н	Н	Stop Shift	Q0 <sub>n</sub>	Q1 <sub>n</sub>	Q2 <sub>n</sub>	32 <sub>n</sub>	

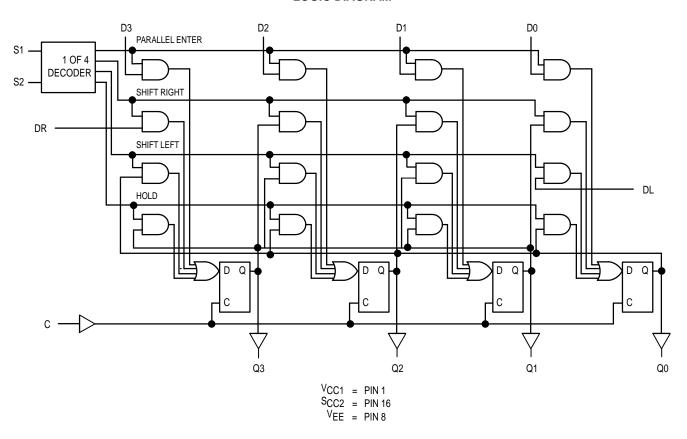
 Outputs as exist after pulse appears at "C" input with input conditions as shown (Pulse Positive transition of clock input).

# DIP PIN ASSIGNMENT



Pin assignment is for Dual-in-Line Package. For PLCC pin assignment, see the Pin Conversion Tables on page 6–11 of the Motorola MECL Data Book (DL122/D).

# **LOGIC DIAGRAM**



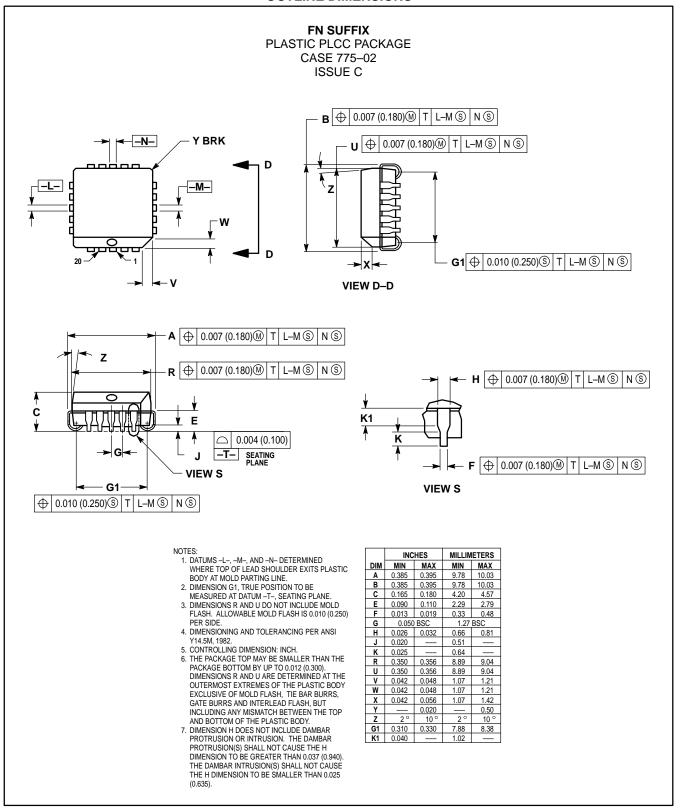
# **APPLICATION INFORMATION**

The MC10H141 is a four-bit universal shift register which performs shift left, or shift right, serial/parallel in, and serial/parallel out operations with no external gating. Inputs S1 and S2 control the four possible operations of the register without external gating of the clock. The flip-flops shift

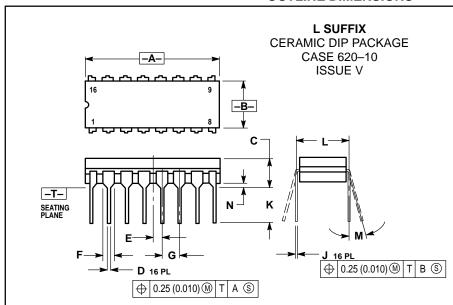
information on the positive edge of the clock. The four operations are stop shift, shift left, shift right, and parallel entry of data. The other six inputs are all data type inputs; four for parallel entry data, and one for shifting in from the left (DL) and one for shifting in from the right (DR).

MOTOROLA 2–132

# **OUTLINE DIMENSIONS**



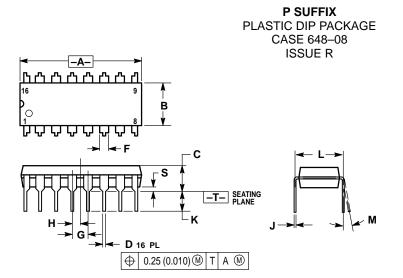
# **OUTLINE DIMENSIONS**



#### NOTES:

- DIMENSIONING AND TOLERANCING PER
- ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
- DIMENSION F MAY NARROW TO 0.76 (0.030) WHERE THE LEAD ENTERS THE CERAMIC

	INC	HES	MILLIN	IETERS		
DIM	MIN	MAX	MIN	MAX		
Α	0.750	0.785	19.05	19.93		
В	0.240	0.295	6.10	7.49		
С		0.200		5.08		
D	0.015	0.020	0.39	0.50		
Е	0.050	BSC	1.27 BSC			
F	0.055	0.065	1.40	1.65		
G	0.100	) BSC	2.54 BSC			
Н	0.008	0.015	0.21	0.38		
K	0.125	0.170	3.18	4.31		
L	0.300	BSC	7.62	BSC		
М	0°	15°	0 °	15°		
N	0.020	0.040	0.51	1.01		



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL
- DIMENSION B DOES NOT INCLUDE MOLD FLASH.
- ROUNDED CORNERS OPTIONAL

	INC	HES	MILLIMETERS		
DIM	MIN MAX		MIN	MAX	
Α	0.740	0.770	18.80	19.55	
В	0.250	0.270	6.35	6.85	
С	0.145	0.175	3.69	4.44	
D	0.015	0.021	0.39	0.53	
F	0.040	0.70	1.02	1.77	
G	0.100 BSC		2.54 BSC		
Н	0.050 BSC		1.27 BSC		
J	0.008	0.015	0.21	0.38	
K	0.110	0.130	2.80	3.30	
L	0.295	0.305	7.50	7.74	
M	0°	10 °	0°	10 °	
S	0.020	0.040	0.51	1.01	

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# How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1-800-441-2447 or 602-303-5454

MFAX: RMFAX0@email.sps.mot.com - TOUCHTONE 602-244-6609 INTERNET: http://Design-NET.com

JAPAN: Nippon Motorola Ltd.; Tatsumi-SPD-JLDC, 6F Seibu-Butsuryu-Center, 3-14-2 Tatsumi Koto-Ku, Tokyo 135, Japan. 03-81-3521-8315

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298



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