# **Quad Line Receiver**

The MC10H115 is a guad differential amplifier designed for use in sensing differential signals over long lines. This 10H part is a functional/ pinout duplication of the standard MECL 10K family part, with 100% improvement in counting frequency and no increase in power-supply current.

The base bias supply (VRR) is made available at Pin 9 to make the device useful as a Schmitt trigger, or in other applications where a stable reference voltage is necessary. Active current sources provide the MC10H115 with excellent common mode rejection. If any amplifier in a package is not used, one input of that amplifier must be connected to VBB (Pin 9) to prevent upsetting the current source bias network.

- Propagation Delay, 1.0 ns Typical
- Power Dissipation 110 mW Typ/Pkg (No Load)
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K-Compatible

#### **MAXIMUM RATINGS**

Characteristic	Symbol	Rating	Unit
Power Supply (V <sub>CC</sub> = 0)	VEE	-8.0 to 0	Vdc
Input Voltage (V <sub>CC</sub> = 0)	VI	0 to V <sub>EE</sub>	Vdc
Output Current — Continuous — Surge	l <sub>out</sub>	50 100	mA
Operating Temperature Range	TA	0 to +75	°C
Storage Temperature Range — Plastic — Ceramic	T <sub>stg</sub>	-55 to +150 -55 to +165	°C

# ELECTRICAL CHARACTERISTICS (VEE = -5.2 V ±5%) (2)

<u></u>								
		0°		25°		75°		
Characteristic	Symbol	Min	Max	Min	Max	Min	Max	Unit
Power Supply Current	ΙΕ	_	29	_	26	_	29	mA
Input Current High	l <sub>inH</sub>	_	150	_	95	_	95	μΑ
Input Leakage Current	I <sub>CBO</sub>	_	1.5	_	1.0	_	1.0	μΑ
Reference Voltage	V <sub>BB</sub>	-1.38	-1.27	-1.35	-1.25	-1.31	-1.19	Vdc
High Output Voltage	Voн	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
Low Output Voltage	V <sub>OL</sub>	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
High Input Voltage (1)	V <sub>IH</sub>	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
Low Input Voltage (1)	$V_{IL}$	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc
Common Mode Range (3)	VCMR	_	_	-2.85 t	to -0.8	_	_	Vdc
Input Sensitivity (4)	$V_{PP}$	_	_	150	typ	_	_	mV <sub>PP</sub>

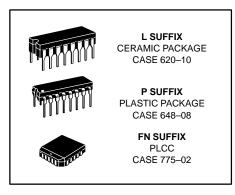
## AC PARAMETERS

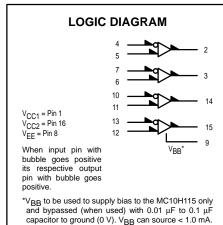
Propagation Delay	t <sub>pd</sub>	0.4	1.3	0.4	1.3	0.45	1.45	ns
Rise Time	t <sub>r</sub>	0.5	1.4	0.5	1.5	0.5	1.6	ns
Fall Time	t <sub>f</sub>	0.5	1.4	0.5	1.5	0.5	1.6	ns

#### NOTES:

- 1. When V<sub>BB</sub> is used as the reference voltage.
- 2. Each MECL 10H series circuit has been designed to meet the specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a 50-ohm resistor to -2.0 volts.
- 3. Differential input not to exceed 1.0 Vdc.
- 4. 150  $\rm mV_{p-p}$  differential input required to obtain full logic swing on output.

# MC10H115





capacitor to ground (0 V). V<sub>BB</sub> can source < 1.0 mA.

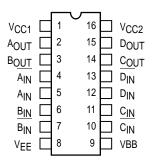
The MC10H115 is designed to be used in sensing (V<sub>BB</sub>) is made available to make the device useful as a Schmitt trigger, or in other cooliestics. differential signals over long lines. The bias supply mitt trigger, or in other applications where a stable reference voltage is necessary.

Active current sources provide these receivers with excellent common-mode noise rejection. If any amplifier in a package is not used, one input of that amplifier must be connected to  $V_{\mbox{\footnotesize{BB}}}$  to prevent unbalancing the current-source bias network.

The MC10H115 does not have internal-input pulldown resistors. This provides high impedance to the amplifier input and facilitates differential connections. Applications:

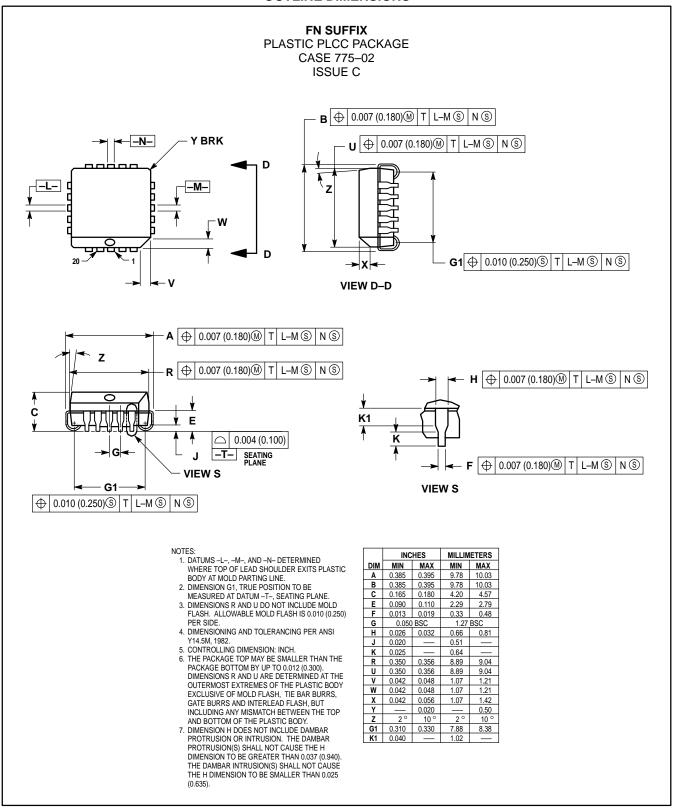
- Low Level Receiver Schmitt Trigger
- · Voltage Level Interface

# DIP **PIN ASSIGNMENT**

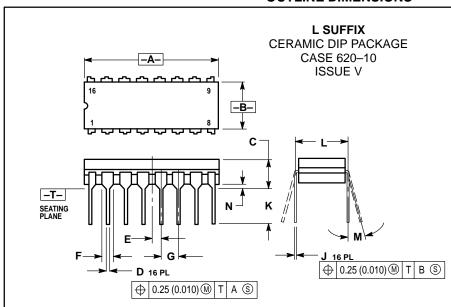


Pin assignment is for Dual-in-Line Package. For PLCC pin assignment, see the Pin Conversion Tables on page 6-11 of the Motorola MECL Data Book (DL122/D).

### **OUTLINE DIMENSIONS**



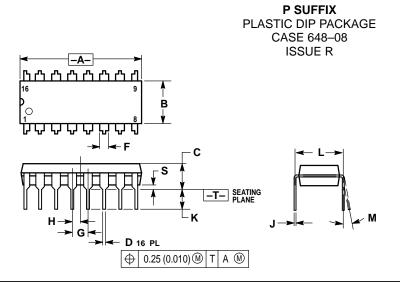
### **OUTLINE DIMENSIONS**



#### NOTES:

- DIMENSIONING AND TOLERANCING PER
- ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
- DIMENSION F MAY NARROW TO 0.76 (0.030) WHERE THE LEAD ENTERS THE CERAMIC

	INCHES		MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.750	0.785	19.05	19.93	
В	0.240	0.295	6.10	7.49	
С		0.200		5.08	
D	0.015	0.020	0.39	0.50	
Е	0.050	BSC	1.27 BSC		
F	0.055	0.065	1.40	1.65	
G	0.100	0.100 BSC		BSC	
Н	0.008	0.015	0.21	0.38	
K	0.125	0.170	3.18	4.31	
L	0.300 BSC		7.62 BSC		
M	0°	15°	0 °	15°	
N	0.020	0.040	0.51	1.01	



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL
- DIMENSION B DOES NOT INCLUDE MOLD FLASH.
- ROUNDED CORNERS OPTIONAL

	INC	HES	MILLIMETERS			
DIM	MIN	MAX	MIN	MAX		
Α	0.740	0.770	18.80	19.55		
В	0.250	0.270	6.35	6.85		
С	0.145	0.175	3.69	4.44		
D	0.015	0.021	0.39	0.53		
F	0.040	0.70	1.02	1.77		
G	0.100 BSC		2.54 BSC			
Н	0.050	0.050 BSC		1.27 BSC		
J	0.008	0.015	0.21	0.38		
K	0.110	0.130	2.80	3.30		
L	0.295	0.305	7.50	7.74		
М	0°	10 °	0°	10 °		
S	0.020	0.040	0.51	1.01		

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