Order Number: MC10EPT22/D Rev. 1.1, 06/1999

# MC100EPT22



SO-8, D SUFFIX 8-LEAD PLASTIC SOIC PACKAGE CASE 751

#### ORDERING INFORMATION

MC100EPT22D SOIC

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#### PIN DESCRIPTION

PIN	FUNCTION
Q0, Q1, Q0, Q1	Diff LVPECL Outputs
D0, D1	LVTTL Inputs
VCC	Positive Supply
GND	Ground

# ECLPS Plus

# Dual LVTTL/LVCMOS to Differential LVPECL Translator

- 420ps Typical Propagation Delay
- Differential LVPECL Outputs
- Small Outline SOIC Package
- PNP LVTTL Inputs for Minimal Loading
- Flow Through Pinouts
- Q Output will default HIGH with inputs open
- ESD Protection: TBD KV HBM, TBD V MM
- Maximum Frequency > 1.1 GHz
- Moisture Sensitivity Level 1, Indefinite Time Out of Drypack
- Flammability Rating: UL-94 code V-0 @ 1/8", Oxygen Index 28 to 34
- Transistor Count = 164 devices

The MC100EPT22 is a dual LVTTL/LVCMOS to differential LVPECL translator. Because LVPECL (Positive ECL) levels are used only +3.3V and ground are required. The small outline 8–lead SOIC package and the single gate of the EPT22 makes it ideal for those applications where space, performance, and low power are at a premium. Because the mature MOSAIC 5 process is used, low cost and high speed can be added to the list of features.



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## ECLinPS Plus™ MC100EPT22

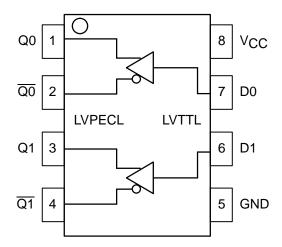


Figure 1. 8-Lead Pinout (Top View) and Logic Diagram

#### **MAXIMUM RATINGS\***

Symbol	Parameter	Value	Unit	
VCC	Power Supply	6.0 to 0	VDC	
VI	Input Voltage (V <sub>I</sub> not more positive than V <sub>CC</sub> )	6.0 to 0	VDC	
lout	Output Current	50 100	mA	
TA	Operating Temperature Range		-40 to +85	°C
T <sub>stg</sub>	Storage Temperature		–65 to +150	°C
θЈΑ	Thermal Resistance (Junction-to-Ambient)	Still Air 500lfpm	190 130	°C/W
θJC	Thermal Resistance (Junction-to-Case)	41 to 44 ± 5%	°C/W	
T <sub>sol</sub>	Solder Temperature (<2 to 3 Seconds: 245°C	265	°C	

<sup>\*</sup> Maximum Ratings are those values beyond which damage to the device may occur.

## LVTTL INPUT DC CHARACTERISTICS ( $V_{CC} = 3.3V \pm 0.3V$ ; GND = 0V; $T_A = -40^{\circ}C$ to +85°C)

Symbol	Characteristic		Тур	Max	Unit
lн	Input HIGH Current (V <sub>in</sub> = 2.7V)			20	μΑ
Інн	Input HIGH Current MAX (V <sub>in</sub> = 6.0V)			100	μΑ
ΙΙL	Input LOW Current (V <sub>in</sub> = 0.5V)			-0.6	mA
VIK	Input Clamp Voltage (I <sub>in</sub> = -18mA)			-1.0	V
V <sub>IH</sub>	Input HIGH Voltage	2.0			V
VIL	Input LOW Voltage			0.8	V

## LVPECL OUTPUT DC CHARACTERISTICS ( $V_{CC} = 3.3V \pm 0.3V$ , GND = 0V) (Note 3.)

		−40°C		25°C			85°C				
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit
ICC	Power Supply Current HIGH (Note 1.)	32	43	55	35	45	60	37	46	62	mA
VOH	Output HIGH Voltage (Note 2.)	2165	2240	2415	2155	2280	2480	2290	2415	2540	mV
VOL	Output LOW Voltage (Note 2.)	1365	1490	1615	1430	1555	1680	1490	1615	1740	mV

NOTE: 100EP circuits are designed to meet the DC specifications shown in the above table after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse airflow greater than 500lfpm is main-

- 1.  $V_{CC}$  = 3.3V, GND = 0V, all other pins floating. 2. All loading with 50 ohms to  $V_{CC}$ -2.0 volts.
- 3. Output parameters vary 1:1 with V<sub>CC</sub>.

# AC CHARACTERISTICS (V $_{CC}$ = $3.3 \text{V} \pm 0.3 \text{V}; \text{ GND}$ = 0 V)

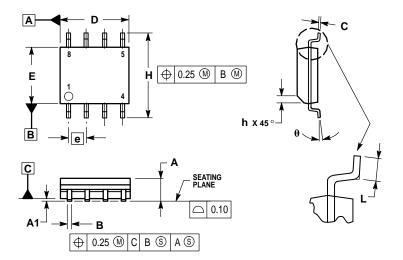
		−40°C		25°C			85°C				
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit
fmax	Maximum Toggle Frequency (Note 4.)	0.8	1.1		0.8	1.1		0.8	1.1		GHz
tPLH, tPHL	Propagation Delay to Output Differential	250	400	650	250	420	675	300	500	700	ps
<sup>†</sup> JITTER	Cycle-to-Cycle Jitter		TBD			TBD			TBD		ps
t <sub>r</sub> & t <sub>f</sub>	Output Rise/Fall Times (20% – 80%) Q, $\overline{Q}$	50	110	200	60	120	220	70	140	250	ps

4. F<sub>max</sub> guaranteed for functionality only. V<sub>OL</sub> and V<sub>OH</sub> levels are guaranteed at DC only.



#### **OUTLINE DIMENSIONS**

SO-8, D SUFFIX PLASTIC SOIC PACKAGE CASE 751-06 ISSUE T



- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- DIMENSIONS ARE IN MILLIMETER.
  DIMENSION D AND E DO NOT INCLUDE MOLD PROTRUSION.
  MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
- DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 TOTAL IN EXCESS OF THE B DIMENSION AT MAXIMUM MATERIAL

	MILLIMETERS							
DIM	MIN	MAX						
Α	1.35	1.75						
A1	0.10	0.25						
В	0.35	0.49						
С	0.19	0.25						
D	4.80	5.00						
Е	3.80	4.00						
е	1.27	BSC						
Н	5.80	6.20						
h	0.25	0.50						
Ĺ	0.40	1.25						
θ	0°	7 °						

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MC100EPT22/D