Quint Differential Line Receiver

The MC10E/100E116 is a quint differential line receiver with emitter-follower outputs. An internally generated reference supply (V_BB) is available for single-ended reception.

- 500ps Max. Propagation Delay
- V_{BB} Supply Output
- Dedicated V_{CCO} Pin for Each Receiver
- Extended 100E VEE Range of 4.2V to 5.46V
- 75kΩ Input Pulldown Resistors

Active current sources plus a deep collector feature of the MOSAIC III process provide the receivers with excellent common-mode noise rejection. Each receiver has a dedicated V_{CCO} supply lead, providing optimum symmetry and stability.

The receiver design features clamp circuitry to cause a defined state if both the inverting and non-inverting inputs are left open; in this case the Q output goes LOW, while the Q output goes HIGH. This feature makes the device ideal for twisted pair applications.

If both inverting and non-inverting inputs are at an equal potential of > -2.5V, the receiver does *not* go to a defined state, but rather current-shares in normal differential amplifier fashion, producing output voltage levels midway between HIGH and LOW, or the device may even oscillate.

The device V_{BB} output is intended for use as a reference voltage for single-ended reception of ECL signals to that device only. When using for this purpose, it is recommended that V_{BB} is decoupled to V_{CC} via a 0.01µF capacitor. Please refer to the interface section of the design guide for information on using the E116 in specialized applications.

The E116 features input pull-down resistors, as does the rest of the ECLinPS family. For applications which require bandwidths greater than that of the E116, the E416 device may be of interest.

PIN NAMES

Pin	Function					
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	Differential Input Pairs Differential Output Pairs Reference Voltage Output.					



MC10E116

MC100E116

QUINT DIFFERENTIAL

LINE RECEIVER

FN SUFFIX

PLASTIC PACKAGE

CASE 776-02

* All V_{CC} and V_{CCO} pins are tied together on the die.



5/95



DC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

		-40°C			0°C			25°C			85°C				
Symbol	Characteristic	Min	Тур	Max	Unit	Cond									
V _{BB}	Output Reference Voltage 10E 100E	-1.43 -1.38		-1.30 -1.26	-1.38 -1.38		-1.27 -1.26	-1.35 -1.38		-1.25 -1.26	-1.31 -1.38		-1.19 -1.26	V	
ΙΗ	Input HIGH Current			200			200			200			200	μΑ	
IEE	Power Supply Current 10E 100E		29 29	35 35		29 29	35 35		29 29	35 35		29 29	35 40	mA	
V _{PP} (DC)	Input Sensitivity	150			150			150			150			mV	1
VCMR	Commom Mode Range	-2.0		-0.6	-2.0		-0.6	-2.0		-0.6	-2.0		-0.6	V	2

1. Differential input voltage required to obtain a full ECL swing on the outputs.

2. V_{CMR} is defined as the range within which the V_{IH} level may vary, with the device still meeting the propagation delay specification. The V_{IL} level must be such that the peak to peak voltage is less than 1.0 V and greater than or equal to V_{PP}(min).

AC CHARACTERISTICS ($V_{EE} = V_{EE}$ (min) to V_{EE} (max); $V_{CC} = V_{CCO} = GND$)

			–40°C		0	°C to 85°0				
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Unit	Condition	
^t PLH ^t PHL	Propagation Delay to Output D (Differential) D (Single-Ended)	150 150	300 300	500 550	200 150	300 300	450 500	ps		
tskew	Within-Device Skew		50			50		ps	1	
tskew	Duty Cycle Skew tPLH - tPHL		±10			±10		ps	2	
V _{PP} (AC)	Minimum Input Swing	150			150			mV	3	
t _r /t _f	Rise/Fall Time	250	375	625	275	375	575	ps	20–80%	

1. Within-device skew is defined as identical transitions on similar paths through a device.

2. Duty cycle skew is defined only for differential operation when the delays are measured from the cross point of the inputs to the cross point of the outputs.

3. Minimum input swing for which AC parameters are guaranteed.





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