

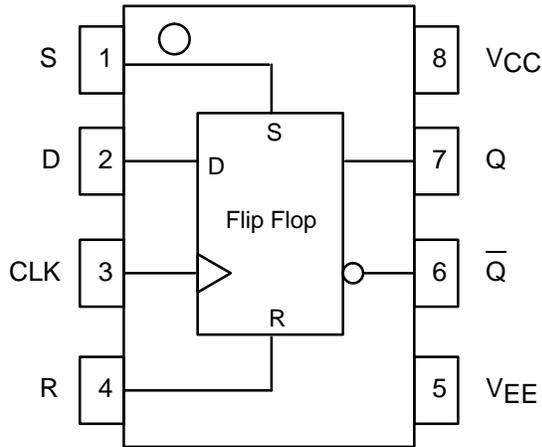
D Flip-Flop With Set and Reset

The MC100LVEL31 is a D flip-flop with set and reset. The device is functionally equivalent to the EL31 device but operates from a -3.3V (or +3.3V) supply. With propagation delays and output transition times essentially equivalent to the EL31, the LVEL31 is ideally suited for those applications which require the ultimate in AC performance at low power supply voltages.

Both set and reset inputs are asynchronous, level triggered signals. Data enters the master portion of the flip-flop when clock is LOW and is transferred to the slave, and thus the outputs, upon a positive transition of the clock.

- 475ps Propagation Delay
- 2.9GHz Toggle Frequency
- 75kΩ Internal Input Pulldown Resistors
- >2000V ESD Protection

LOGIC DIAGRAM AND PINOUT ASSIGNMENT



MC100LVEL31



D SUFFIX
PLASTIC SOIC PACKAGE
CASE 751-05

TRUTH TABLE

D	S	R	CLK	Q
L	L	L	Z	L
H	L	L	Z	H
X	H	L	X	H
X	L	H	X	L
X	H	H	X	Undef

Z = LOW to HIGH Transition



MC100LVEL31

DC CHARACTERISTICS ($V_{EE} = V_{EE}(\text{min})$ to $V_{EE}(\text{max})$; $V_{CC} = \text{GND}$)

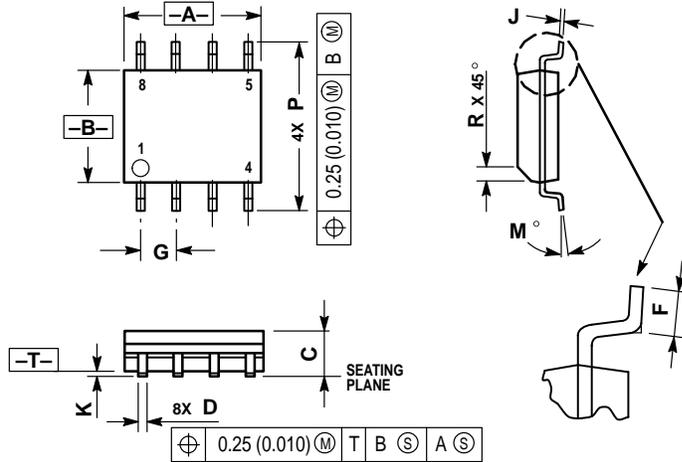
Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
I_{EE}	Power Supply Current		30	35		30	35		30	35		32	38	mA
V_{EE}	Power Supply Voltage	-3.0	-3.3	-3.8	-3.0	-3.3	-3.8	-3.0	-3.3	-3.8	-3.0	-3.3	-3.8	V
I_{IH}	Input HIGH Current			150			150			150			150	μA
I_{IL}	Input LOW Current	0.5			0.5			0.5			0.5			μA

AC CHARACTERISTICS ($V_{EE} = V_{EE}(\text{min})$ to $V_{EE}(\text{max})$; $V_{CC} = \text{GND}$)

Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit
		Min	Typ	Max										
f_{MAX}	Maximum Toggle Frequency	2.7			2.9			2.9			2.9			GHz
t_{PLH} t_{PHL}	Propagation Delay to Output CLK S, R	365 385	465 475	580 620	365 385	465 475	580 620	375 395	475 485	590 630	415 435	530 525	630 670	ps
t_{S} t_{H}	Setup Time Hold Time	150 250	0 100		ps									
t_{RR}	Set/Reset Recovery	400	200		400	200		400	200		400	200		ps
t_{PW}	Minimum Pulse Width CLK, Set, Reset	600			600			600			600			ps
t_{r} t_{f}	Output Rise/Fall Times Q (20% – 80%)	120	220	320	120	220	320	120	220	320	120	220	320	ps

OUTLINE DIMENSIONS

D SUFFIX
 PLASTIC SOIC PACKAGE
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 ISSUE P



NOTES:

1. DIMENSIONS A AND B ARE DATUMS AND T IS A DATUM SURFACE.
2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
3. DIMENSIONS ARE IN MILLIMETER.
4. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
5. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
6. DIMENSION D DOES NOT INCLUDE MOLD PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

DIM	MILLIMETERS	
	MIN	MAX
A	4.80	5.00
B	3.80	4.00
C	1.35	1.75
D	0.35	0.49
F	0.40	1.25
G	1.27 BSC	
J	0.18	0.25
K	0.10	0.25
M	0°	7°
P	5.80	6.20
R	0.25	0.50

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How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution;
P.O. Box 20912; Phoenix, Arizona 85036. 1-800-441-2447 or 602-303-5454

MFAX: RMFAX0@email.sps.mot.com – TOUCHTONE 602-244-6609
INTERNET: <http://Design-NET.com>

JAPAN: Nippon Motorola Ltd.; Tatsumi-SPD-JLDC, 6F Seibu-Butsuryu-Center,
3-14-2 Tatsumi Koto-Ku, Tokyo 135, Japan. 03-81-3521-8315

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park,
51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298

