

Product Preview

Low-Voltage Dual 1:10 Differential ECL/PECL Clock Driver

The MC100EP220 is a dual low skew 1-to-10 differential driver, designed with clock distribution in mind. The V_{BB} output provides a DC threshold bias for single ended sources. The V_{BB} can be connected to the true input or the complementary input, the latter will produce an inverted output. If used, the V_{BB} output should be bypassed to ground.

- 150ps Part-to-Part Skew
- 50ps Output-to-Output Skew
- Differential Design
- V_{BB} Output
- Voltage and Temperature Compensated Outputs
- Low Voltage V_{EE} Range of -2.375 to $-3.8V$
- $75k\Omega$ Input Pulldown Resistors

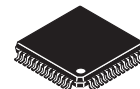
The EP220 is specifically designed, modeled and produced with low skew as the key goal. Optimal design and layout serve to minimize gate-to-gate skew within a device, and empirical modeling is used to determine process control limits that ensure consistent t_{pd} distributions from lot to lot. The net result is a dependable, guaranteed low skew device.

To ensure that the tight skew specification is met it is necessary that both pairs of the differential outputs are terminated into 50Ω , even if only one side is being used. In applications which do not use all of the outputs, it is best to leave unused pairs open to minimize power consumption in the device.

The MC100EP220, as with most other ECL devices, can be operated from a positive V_{CC} supply in PECL mode. This allows the EP220 to be used for high performance clock distribution in $+3.3V$ or $+2.5V$ systems. Designers can take advantage of the EP220's performance to distribute low skew clocks across the backplane. In a PECL environment, series or Thevenin line terminations are typically used as they require no additional power supplies. For more information on using PECL, designers should refer to Motorola Application Note AN1406/D.

MC100EP220

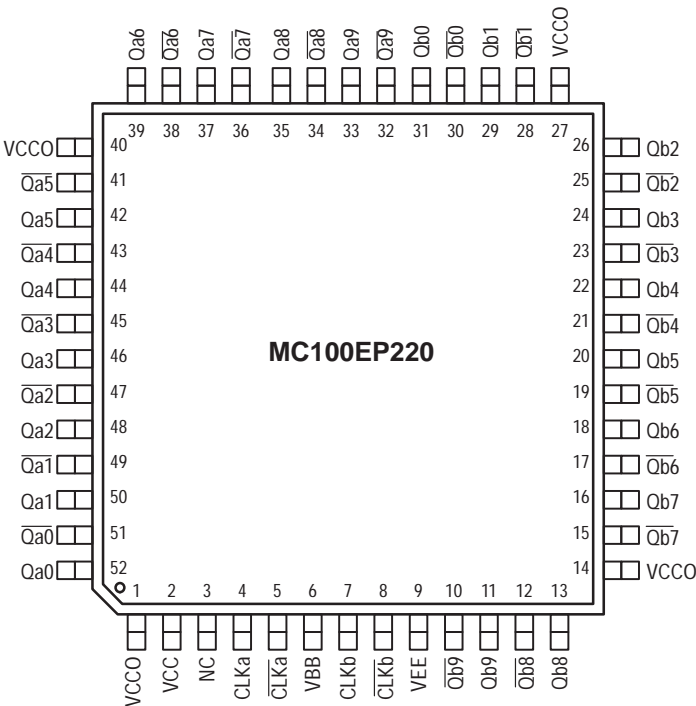
LOW-VOLTAGE DUAL 1:10 DIFFERENTIAL ECL/PECL CLOCK DRIVER



FA SUFFIX
52-LEAD TQFP PACKAGE
CASE 848D-03



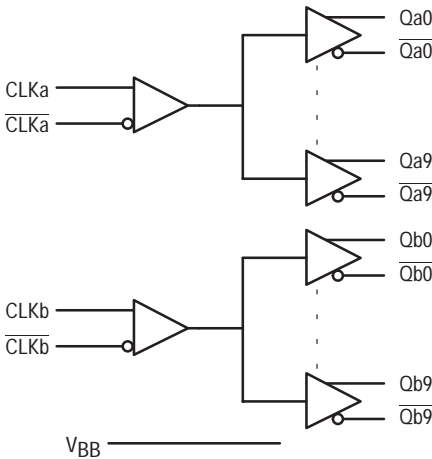
Pinout: 52-Lead TQFP
(Top View)



PIN NAMES

Pins	Function
CLKn, $\overline{\text{CLKn}}$	Differential Input Pairs
Qan, $\overline{\text{Qan}}$	Differential Outputs
Qbn, $\overline{\text{Qbn}}$	Differential Outputs
VBB	VBB Output

LOGIC SYMBOL



ECL DC CHARACTERISTICS

Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V _{OH}	Output HIGH Voltage	-1.025	-0.955	-0.880	-1.025	-0.955	-0.880	-1.025	-0.955	-0.880	-1.025	-0.955	-0.880	V
V _{OL}	Output LOW Voltage	-1.810	-1.705	-1.620	-1.810	-1.705	-1.620	-1.810	-1.705	-1.620	-1.810	-1.705	-1.620	V
V _{IH}	Input HIGH Voltage	-1.165		-0.880	-1.165		-0.880	-1.165		-0.880	-1.165		-0.880	V
V _{IL}	Input LOW Voltage	-1.810		-1.475	-1.810		-1.475	-1.810		-1.475	-1.810		-1.475	V
V _{BB}	Output Reference Voltage	-1.38		-1.26	-1.38		-1.26	-1.38		-1.26	-1.38		-1.26	V
V _{EE}	Power Supply Voltage	-2.375		-3.8	-2.375		-3.8	-2.375		-3.8	-2.375		-3.8	V
I _{IH}	Input HIGH Current			150			150			150			150	μA
I _{EE}	Power Supply Current													mA

PECL DC CHARACTERISTICS

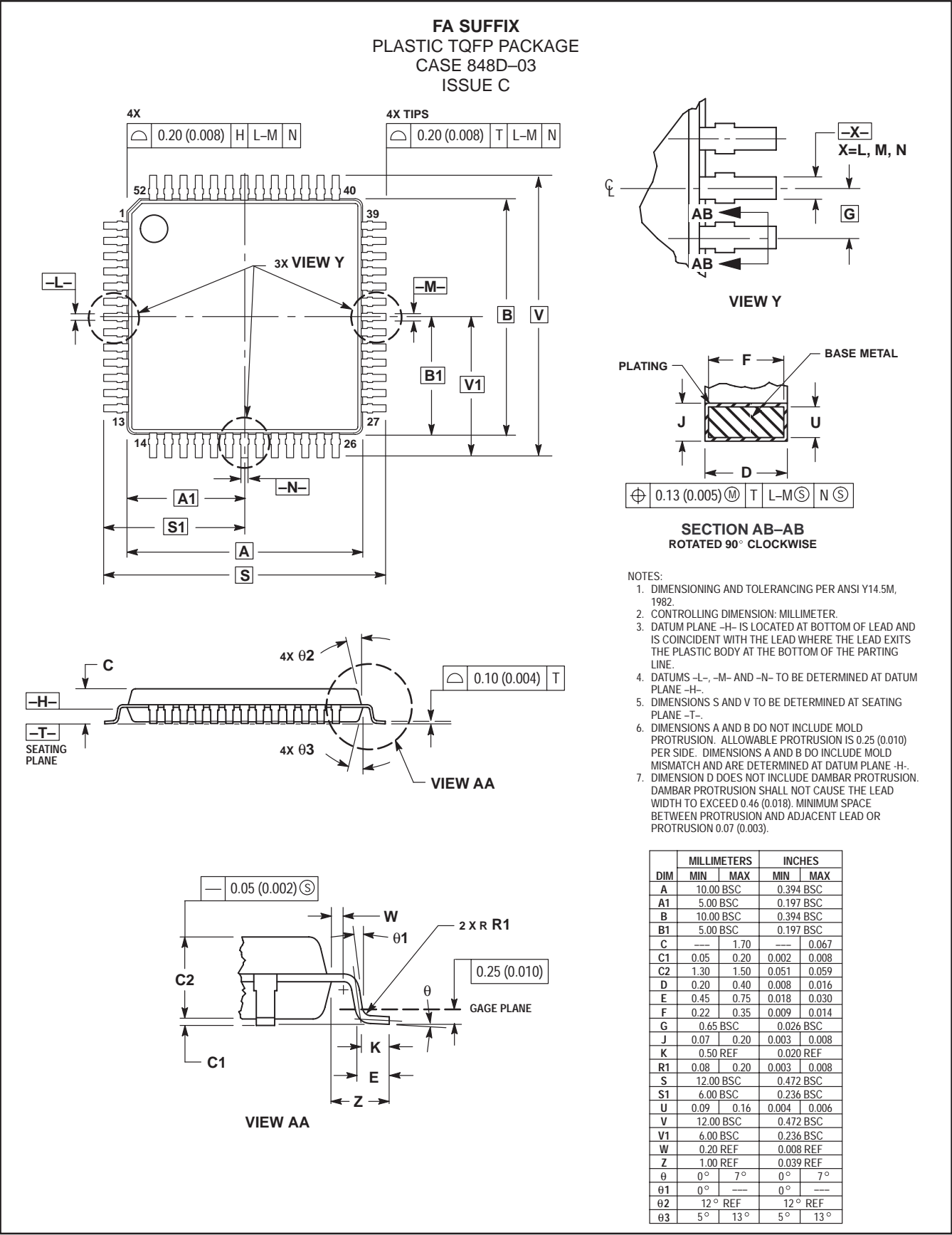
Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V _{OH}	Output HIGH Voltage (Note 1)	2.275	2.345	2.420	2.275	2.345	2.420	2.275	2.345	2.420	2.275	2.345	2.420	V
V _{OL}	Output LOW Voltage (Note 1)	1.490	1.595	1.680	1.490	1.595	1.680	1.490	1.595	1.680	1.490	1.595	1.680	V
V _{IH}	Input HIGH Voltage (Note 1)	2.135		2.420	2.135		2.420	2.135		2.420	2.135		2.420	V
V _{IL}	Input LOW Voltage (Note 1)	1.490		1.825	1.490		1.825	1.490		1.825	1.490		1.825	V
V _{BB}	Output Reference Voltage (Note 1)	1.92		2.04	1.92		2.04	1.92		2.04	1.92		2.04	V
V _{CC}	Power Supply Voltage	2.375		3.8	2.375		3.8	2.375		3.8	2.375		3.8	V
I _{IH}	Input HIGH Current			150			150			150			150	μA
I _{EE}	Power Supply Current		125			125			125			125		mA


1. These values are for V_{CC} = 3.3V. Level Specifications will vary 1:1 with V_{CC}.

AC CHARACTERISTICS (V_{EE} = V_{EE} (min) to V_{EE} (max); V_{CC} = V_{CCO} = GND)

Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit	Condition
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max		
t _{PLH} t _{PHL}	Propagation Delay to Output IN (differential) IN (single-ended)								500 500					ps	
t _{skew}	Within-Device Skew Part-to-Part Skew (Diff)		50 150			50 150			50 150			50 150		ps	
f _{max}	Maximum Input Frequency		1.5			1.5			1.5			1.5		GHz	
V _{PP}	Minimum Input Swing	500			500			500			500			mV	
V _{CMR}	Common Mode Range													V	
t _r /t _f	Output Rise/Fall Time		200			200			200			200		ps	20%–80%

OUTLINE DIMENSIONS



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