Product Preview Low-Voltage 1:10 Differential ECL/PECL Clock Driver

The MC100EP111 is a low skew 1–to–10 differential driver, designed with clock distribution in mind. It accepts two clock sources into an input multiplexer. The input signals can be either differential or single–ended if the V_{BB} output is used. The selected signal is fanned out to 10 identical differential outputs.

- 100ps Part-to-Part Skew
- 35ps Output-to-Output Skew
- Differential Design
- VBB Output
- Voltage and Temperature Compensated Outputs
- Low Voltage VEE Range of -2.375 to -3.8V
- 75kΩ Input Pulldown Resistors

The EP111 is specifically designed, modeled and produced with low skew as the key goal. Optimal design and layout serve to minimize gate-to-gate skew within a device, and empirical modeling is used to determine process control limits that ensure consistent t_{pd} distributions from lot to lot. The net result is a dependable, guaranteed low skew device.

To ensure that the tight skew specification is met it is necessary that both sides of the differential output are terminated into 50Ω , even if only one side is being used. In most applications, all ten differential pairs will be used and therefore terminated. In the case where fewer than ten pairs are used, it is necessary to terminate at least the output pairs on the same package side as the pair(s) being used on that side, in order to maintain minimum skew. Failure to do this will result in small degradations of propagation delay (on the order of 10–20ps) of the output(s) being used which, while not being catastrophic to most designs, will mean a loss of skew margin.

The MC100EP111, as with most other ECL devices, can be operated from a positive V_{CC} supply in PECL mode. This allows the EP111 to be used for high performance clock distribution in +3.3V or +2.5V systems. Designers can take advantage of the EP111's performance to distribute low skew clocks across the backplane or the board. In a PECL environment, series or Thevenin line terminations are typically used as they require no additional power supplies. For more information on using PECL, designers should refer to Motorola Application Note AN1406/D.

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MC100EP111

LOW-VOLTAGE





LOGIC SYMBOL



ECL DC CHARACTERISTICS

			–40°C			0°C		25°C						
Symbol	Characteristic	Min	Тур	Max	Unit									
VOH	Output HIGH Voltage	-1.025	-0.955	-0.880	-1.025	-0.955	-0.880	-1.025	-0.955	-0.880	-1.025	-0.955	-0.880	V
V _{OL}	Output LOW Voltage	-1.810	-1.705	-1.620	-1.810	-1.705	-1.620	-1.810	-1.705	-1.620	-1.810	-1.705	-1.620	V
V_{IH}	Input HIGH Voltage	-1.165		-0.880	-1.165		-0.880	-1.165		-0.880	-1.165		-0.880	V
VIL	Input LOW Voltage	-1.810		-1.475	-1.810		-1.475	-1.810		-1.475	-1.810		-1.475	V
V_{BB}	Output Reference Voltage	-1.38		-1.26	-1.38		-1.26	-1.38		-1.26	-1.38		-1.26	V
V_{EE}	Power Supply Voltage	-2.375		-3.8	-2.375		-3.8	-2.375		-3.8	-2.375		-3.8	V
IIH	Input HIGH Current			150			150			150			150	μΑ
IEE	Power Supply Current													mA

PECL DC CHARACTERISTICS

			–40°C			0°C			25°C			85°C		
Symbol	Characteristic	Min	Тур	Max	Unit									
V _{OH}	Output HIGH Voltage (1.)	2.275	2.345	2.420	2.275	2.345	2.420	2.275	2.345	2.420	2.275	2.345	2.420	V
VOL	Output LOW Voltage (1.)	1.490	1.595	1.680	1.490	1.595	1.680	1.490	1.595	1.680	1.490	1.595	1.680	V
VIH	Input HIGH Voltage (1.)	2.135		2.420	2.135		2.420	2.135		2.420	2.135		2.420	V
v_{IL}	Input LOW Voltage (1.)	1.490		1.825	1.490		1.825	1.490		1.825	1.490		1.825	V
V_{BB}	Output Reference Voltage (Note 1.)	1.92		2.04	1.92		2.04	1.92		2.04	1.92		2.04	V
VCC	Power Supply Voltage	2.375		3.8	2.375		3.8	2.375		3.8	2.375		3.8	V
IIH	Input HIGH Current			150			150			150			150	μΑ
IEE	Power Supply Current													mA

1. These values are for V_{CC} = 3.3V. Level Specifications will vary 1:1 with V_{CC}.

AC CHARACTERISTICS (V_{EE} = V_{EE} (min) to V_{EE} (max); V_{CC} = V_{CCO} = GND)

		-40°C			0°C			25°C			85°C				
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit	Condition
^t PLH ^t PHL	Propagation Delay to Output IN (differential) IN (single–ended)								400 400					ps	
^t skew	Within–Device Skew Part–to–Part Skew (Diff)		35 100			35 100			35 100			35 100		ps	
f _{max}	Maximum Input Frequency		1.5			1.5			1.5			1.5		GHz	
V _{PP}	Minimum Input Swing	500			500			500			500			mV	
VCMR	Common Mode Range													V	
t _r /t _f	Output Rise/Fall Time		200			200			200			200		ps	20%–80%





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How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 5405; Denver, Colorado 80217. 303–675–2140 or 1–800–441–2447 JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 81–3–3521–8315

Mfax™: RMFAX0@email.sps.mot.com – TOUCHTONE 602–244–6609 INTERNET: http://www.mot.com/sps/

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MC100EP111/D

