

## **LA7938**

# Electronic Channel Select System Control Circuit for TV/VCR Use

#### Overview

The SANYO LA7938 monolithic linear TV/VCR electronic tuner system controller IC integrates all the peripheral circuitry for a TV or VTR tuner, with the exception of the microcontroller, into a single chip.

It incorporates a 2-input/4-output band-switch, 5.0V and 5.75V voltage regulators, comparator, sync signal processing circuit, AFT DC shift circuit and constant current circuit. Each PNP output of the band-switch circuit typically sources 40mA, eliminating the need for external current drivers.

The LA7938 operates from a recommended supply voltage range of 8.7 to 12.5V. It is available in 22-pin shrink DIPs.

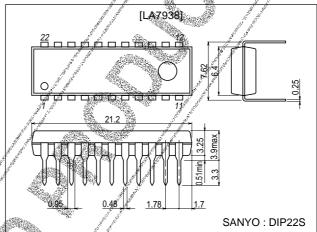
#### **Features**

- Integrates all tuner peripheral circuits except controller.
- Band-switch outputs source up to 40mA.
- Regulators each supply up to 50mA.
- 22-pin shrink DIP.

## Package Dimensions

unit:mm

3059-DIP22S



## **Specifications**

#### **Maximum Ratings** at $Ta = 25^{\circ}C$

Parameter	per a de la companya	Symbol		Conditions	Ratings	Unit
Allowable power dissipation	A 10	Pd max	Ta≤65°C		1000	mW
Operating temperature	gard gard	Topr:	30 30		-20 to +65	°C
Storage temperature		Tstg	ad the state of th		-55 to +150	°C
[Band-switch]	///		J. P. Market		•	
V <sub>CC1</sub> maximum supply voltage	/ <i>680</i> 000	¥ <sub>18</sub> max			13	V
		l <sub>19</sub> max 🦯 ,	<i>*</i>		-50	mA
Maximum load current		🍪 l <sub>20</sub> max 🥖			-50	mA
		I <sub>21</sub> max			-50	mA
		l <sub>22</sub> max			-50	mA
Applied input voltage	484	√v <sub>6</sub> max			3.5	V
Applied input voitage	b	√ √ <sub>5</sub> max			3.5	V

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## **LA7938**

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Parameter	Symbol	Conditions	Ratings	Unit	
[Sync detector]			•	•	
Positive input voltage	V <sub>1</sub> max		3.5	V	
Negative input voltage	−V <sub>1</sub> max		<b>−1.4</b>	V	
Applied input voltage (Pin 3)	V <sub>3</sub> max	V <sub>CC</sub> 1=13V	10	V	
Applied input voltage	V <sub>4</sub> max	V <sub>CC</sub> 1=V <sub>CC</sub> 2=12V	4.6	V	
[Voltage regulators]		A Company of the Comp	All and the second		
V <sub>CC2</sub> supply voltage	V <sub>13</sub> max		13.	V	
+5.75V output current	I <sub>12</sub> max		<b>−50</b> *	mA	
+5.0V output current	lg max		_50 <u>*</u>	,/mA	
[Comparator]		and the second s	A. 11	o de la companya della companya della companya de la companya della companya dell	
Maximum input voltage	V <sub>8</sub> max	V <sub>CC</sub> 2=13V	/ //3	V	
Waximum input voltage	V <sub>10</sub> max	V <sub>CC</sub> 2=13V	/ 13	V	
Applied output voltage	V <sub>11</sub> max		6	V	
[+31V constant current source]					
Applied voltage	V <sub>14</sub> max		43	V	
[AFT shift circuit]			A A A		
Maximum input voltage	V16 max	V <sub>CC</sub> 1=13V	13	V	

<sup>\*:</sup> The rating for the total current drawn from both the 5.0V and 5.75V supplies is 70 mÅ.

### Operating Conditions at Ta = 25°C

Parameter	Symbol	100	Conditions	//	Ratings	Unit
Operating voltage range	V <sub>CC</sub> op				8.7 to 12.5	V

## (Band Switch Truth Table)

Input		Output					
Α	В	F1	,F2 ,F3 F4				
(Pin 6)	(Pin 5)	(Pin 19)	(Pin 20) (Pin 21) (Pin 22)				
L	L	Н	// Z				
Н	L	z,	Н 2 / Z				
L	Н	Z 🕺					
Н	Н	Z / /	Ž Z H				

Z HIGH-impedance

## Operating Characteristics at Ta = 25 °C, V<sub>CC</sub>1, V<sub>CC</sub>2=12N

Parameter	Symbol Conditions		Ratings			Unit
Farameter	Syllibol	Conditions	min	typ	max	Offic
Quiescent current drain 1	lcc1	11		9.0		mA
Quiescent current drain 2	l <sub>CC</sub> 2			7.0		mA
[Band-switch]		T pr		•	•	
// %	F <sub>1</sub> (sat.)	1 <sub>O</sub> =-40mA			0.7	V
Output saturation voltage	F <sub>2</sub> (sat.)	I <sub>O</sub> =-40mA			0.7	V
Output saturation voltage	F <sub>3</sub> (sat.)	I <sub>O</sub> =-40mA			0.7	V
	F <sub>4</sub> (sat.)	I <sub>O</sub> =-40mA			0.7	V
Input high-levet voltage	√ √ <sup>N</sup> 5HI	Open gate type microcomputer must be in OFF				
Input high-level voltage	√ V <sub>6HI</sub>	state (pull-up resistance on chip).				
Input low-level voltage	V <sub>5</sub> LO				0.8	V
Input low-lever voltage	V <sub>6</sub> LO				0.8	V
Output leakage current	I <sub>FL</sub>		-50			μA
[Sync circuit]						
Input threshold voltage	V <sub>1TH</sub>		0.4	0.72	1.5	V
Pin 2 output saturation voltage	V <sub>2(sat)</sub>	I <sub>SINK</sub> =10mA			1.0	V
Pin 3 high-level input	V <sub>3HI</sub>		5.0			V
Pin 3 low-level input	V <sub>3HO</sub>				3.0	V
Pin 4 output saturation voltage	V <sub>4(sat)</sub>	I <sub>SINK</sub> =2mA			0.7	V
[+5.75V, +5.0V regulators]	•					
+5.75V output voltage	V <sub>12</sub>	I <sub>12</sub> =-20mA	5.35	5.75	6.15	V
+5.75 output voltage regulation	V <sub>12Reg</sub>	$I_{12}$ =5mA $\rightarrow$ 20mA	-25		+25	mV
+5.0V output voltage	V <sub>9</sub>	Ig=-20mA	4.6	5.0	5.4	V
+5.0V output voltage regulation	V <sub>9Reg</sub>	Ig=5mA →20mA		50	100	mV

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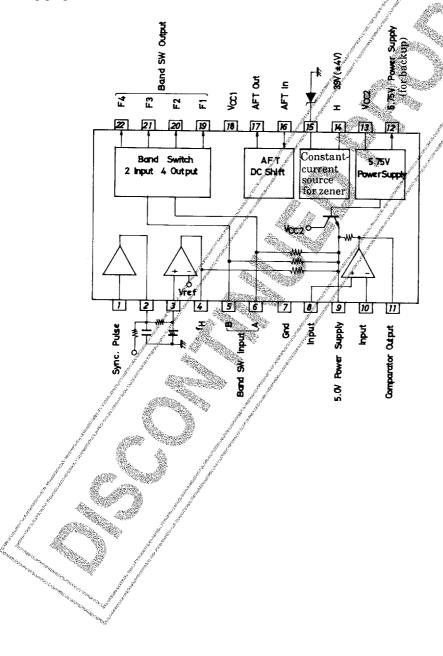
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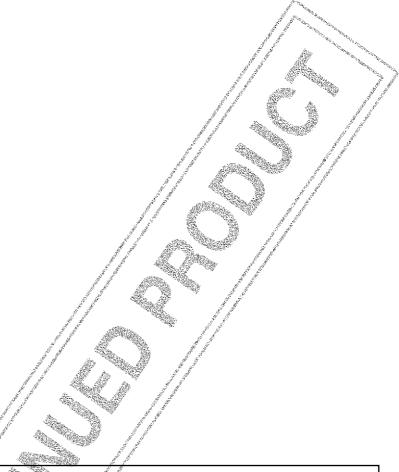
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Parameter	Symbol	O a malifelia man		Ratings			
	Symbol	Conditions		typ	max	Unit	
[31V current source]			•	•			
Pin 15 output current	l <sub>15</sub>		4.2	6.0	7.8	mA	
[AFT shift current]	•		A STATE OF	in a second	•		
DC shift voltage	V <sub>16</sub> -V <sub>17</sub>		4.23	4.73	5.23	V	
Pin 17 maximum output voltage	V <sub>17</sub> max		5.35	5.75			
[Comparator]	•		and the same of th	A Silver	Market War Waller		
Maximum operating input voltage	V <sub>8 to 10</sub> max	jl.	9,0	Ę.,	35.0	V. V.	
Minimum operating input voltage	V <sub>8 to 10</sub> min	get get	1/8	100	0.7	V	
Output saturation voltage	V <sub>11(sat)</sub>	I <sub>SINK</sub> =2mA		362.97	0.7	z <sup>er.</sup> V	

## **Block Diagram**

[For backup purposes]





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