AUTOMOTIVE GRADE

CS8481

3.3V/250mA, 5V/100mA Micropower Low Dropout Regulator with ENABLE

Description

The CS8481 is a precision, dual micropower linear voltage regulator. The switched 3.3V primary output (V_{OUT1}) supplies up to 250mA while the secondary 5V (V_{OUT2}) is capable of supplying 100mA. Both outputs have a maximum dropout voltage of 600mV and low reverse current. Quiescent current drain is typically 150µA when supplying 100µA from each output.

The ENABLE input provides logic level control of the primary output.

With the primary output disabled, quiescent current drain is typically 100µA when supplying 100µA from the secondary output.

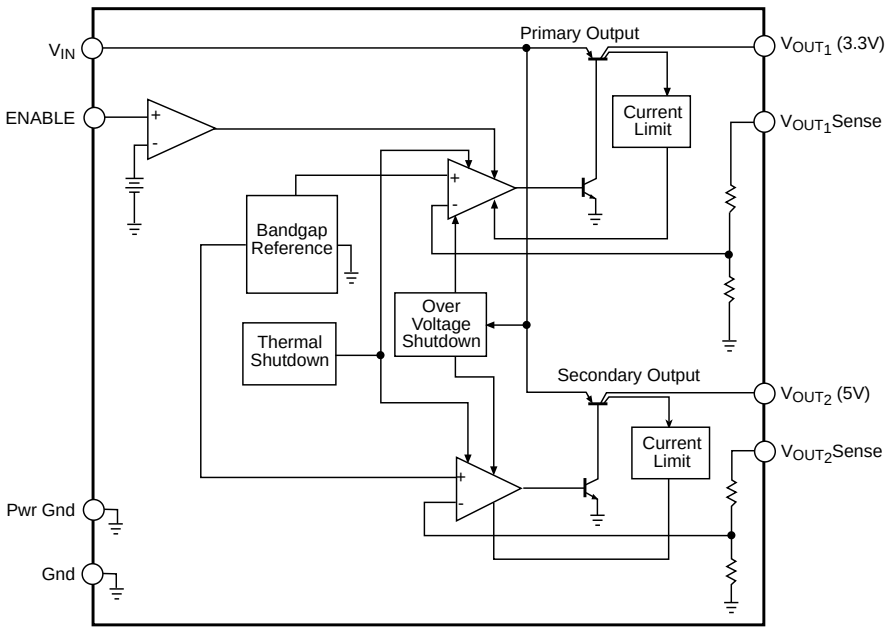
The CS8481 is extremely robust with protection provided for reverse battery, short circuit, overvoltage, and overtemperature on both outputs.

The CS8481 is available in a 5-lead D^2PAK.

Features

- **3.3V, 250mA Primary Output**
- **5V, 100mA Secondary Output**
- **3% Tolerance, Both Outputs**
- **ON/OFF Control for Primary Output**
- **Low Quiescent Current Drain (100µA V_{OUT2})**
- **Low Reverse Current**
- **Protection Features**
  - **Reverse Battery (-15V)**
  - **74V Peak Transient Voltage**
  - **Short Circuit**
  - **Overtemperature**
  - **Overvoltage (34V)**

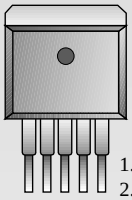
Block Diagram



Package Options

5 Lead D^2PAK

Tab (Gnd)



1. V_{IN}
2. V_{OUT1}
3. Gnd
4. V_{OUT2}
5. ENABLE

Consult factory for 8L and 16L SO, 8L and 16L PDIP, 7L D^2PAK and 5L TO-220.

ON Semiconductor

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Absolute Maximum Ratings

Input Voltage (V_{IN})
- Operating Range 30V
- Reverse Battery ..... -15V
- Peak Transient Voltage (60V Load Dump @ 14V V_{IN}) 74V

ENABLE 10V
Power Dissipation ..... Internally Limited
Maximum Junction Temperature ..... -40°C to +150°C
Storage Temperature Range ..... -55°C to +150°C
Electrostatic Discharge (Human Body Model) 4kV
Lead Temperature Soldering
- Reflow (SMD styles only) ..... 60 sec. max above 183°C, 230°C peak

Electrical Characteristics: $6V \le V_{IN} \le 26V$, $I_{OUT1} = I_{OUT2} = 100\mu A$, $-40°C \le T_A \le 125°C$, $-40°C \le T_C \le 125°C$, unless otherwise specified

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
■ Primary Output Stage (V_{OUT1})					
Output Voltage, V_{OUT1}	$100\mu A \le I_{OUT1} \le 250mA$	3.2	3.3	3.4	V
Line Regulation	$6V \le V_{IN} \le 26V$		5	50	mV
Load Regulation	$1mA \le I_{OUT1} \le 250mA$, $V_{IN} = 14V$		5	50	mV
Quiescent Current	ENABLE = HIGH, $V_{IN} = 16V$, $I_{OUT1} = 250mA$		22	50	mA
Ripple Rejection	$f = 120Hz$, $I_{OUT1} = 125mA$, $7V \le V_{IN} \le 17V$	60	70		dB
Current Limit	$9V \le V_{IN} \le 26V$	260	400		mA
Short Circuit Current Limit	$V_{OUT1} = 0V$, $V_{IN} = 16V$	25			mA
Reverse Current	$V_{OUT1} = 3.3V$, $V_{IN} = 0V$		100	1500	µA
■ Secondary Output (V_{OUT2})					
Output Voltage, V_{OUT2}	$100\mu A \le I_{OUT2} \le 100mA$	4.85	5.00	5.15	V
Dropout Voltage	$I_{OUT2} = 100mA$ $I_{OUT2} = 100\mu A$		400 100	600 150	mV mV
Line Regulation	$6V \le V_{IN} \le 26V$		5	50	mV
Load Regulation	$100\mu A \le I_{OUT2} \le 100mA$, $V_{IN} = 14V$		5	50	mV
Quiescent Current	ENABLE = LOW, $V_{IN} = 12.8V$ ENABLE = HIGH, $V_{IN} = 16V$, $I_{OUT2} = 100mA$		100 8	150 30	µA mA
Ripple Rejection	$f = 120Hz$, $I_{OUT2} = 10mA$, $7V \le V_{IN} \le 17V$	60	70		dB
Current Limit	$9V \le V_{IN} \le 26V$	105	200		mA
Short Circuit Current Limit	$V_{OUT2} = 0V$, $V_{IN} = 16V$, $I_{OUT1} = 0A$	25			mA
Reverse Current	$V_{OUT2} = 5V$, $V_{IN} = 0V$		100	250	µA
■ Enable Function (ENABLE)					
Input Threshold	ENABLE = LOW, $6V \le V_{IN} \le 26V$ ENABLE = HIGH, $6V \le V_{IN} \le 26V$	 2.0	1.2 1.2	0.8	V V
Input Bias Current	$0V \le V_{ENABLE} \le 5V$	-2	0	2	µA
■ Protection Circuits					
Overtemperature Threshold	Guaranteed by Design				
Overvoltage Shutdown		30	34	38	V

Package Lead Description

PACKAGE LEAD #	LEAD SYMBOL	FUNCTION
5 Lead D^2PAK		
1	V_{IN}	Supply voltage to IC, usually direct from battery.
2	V_{OUT1}	3.3V regulated output which is activated by ENABLE input.
3	Gnd	Ground connection.
4	V_{OUT2}	Standby output 5V, 100mA capability; always on.
5	ENABLE	CMOS compatible input lead; switches V_{OUT1}. When ENABLE is high, V_{OUT1} is active.

Definition of Terms

Current Limit
Peak current that can be delivered to the output.

Dropout Voltage
The input-output voltage differential at which the circuit ceases to regulate against further reduction in input voltage. Measured when the output voltage has dropped 100mV from the nominal value obtained at 14V input, dropout voltage is dependent upon load current and junction temperature.

Input Output Differential
The voltage difference between the unregulated input voltage and the regulated output voltage for which the regulator will operate.

Input Voltage
The DC voltage applied to the input terminals with respect to ground.

Line Regulation
The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

Load Regulation
The change in output voltage for a change in load current at constant chip temperature.

Long Term Stability
Output voltage stability under accelerated life-test conditions after 1000 hours with maximum rated voltage and junction temperature.

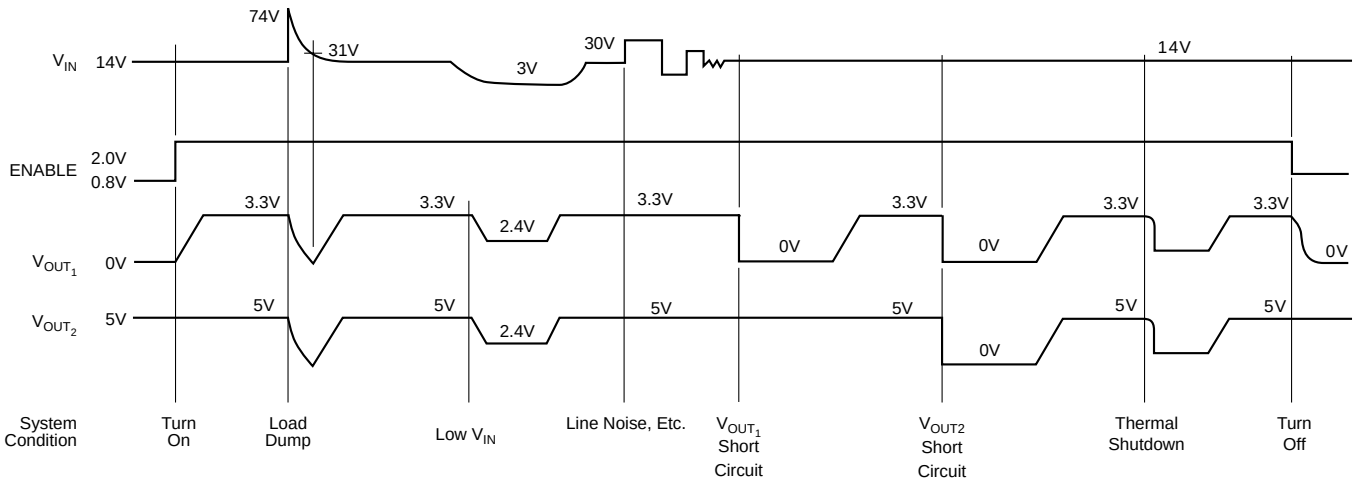
Quiescent Current
The part of the positive input current that does not contribute to the positive load current. i.e., the regulator ground lead current.

Ripple Rejection
The ratio of the peak-to-peak input ripple voltage to the peak-to-peak output ripple voltage.

Short Circuit Current Limit
Peak current that can be delivered by the output when forced to 0V.

Temperature Stability of V_{OUT}
The percentage change in output voltage for a thermal variation from room temperature to either temperature extreme.

Typical Circuit Waveform



Application Notes

General

The CS8481 is a micropower dual regulator. All bias required to operate the internal circuitry is derived from the standby output, V_{OUT2}. If this output experiences an over current situation and collapses, then V_{OUT1} will also collapse (see timing diagrams).

If there is critical circuitry that must remain active under most conditions it should be connected to V_{OUT2}. Any circuitry that is likely to be subjected to a short circuit, e.g., circuitry outside the module, should be connected to V_{OUT1}.

External Capacitors

Output capacitors are required for stability with the CS8481. Without them, the regulator outputs will oscillate. Actual size and type may vary depending upon the application load and temperature range. Capacitor effective series resistance (ESR) is also a factor in the IC stability. Worst-case is determined at the minimum ambient temperature and maximum load expected.

Output capacitors can be increased in size to any desired value above the minimum. One possible purpose of this would be to maintain the output voltages during brief conditions of negative input transients that might be characteristic of a particular system.

Capacitors must also be rated at all ambient temperatures expected in the system. To maintain regulator stability down to -40°C, capacitors rated at that temperature must be used.

More information on capacitor selection for Smart Regulators™ is available in the Smart Regulator application note, *Compensation for Linear Regulators*.

ENABLE

The ENABLE function controls V_{OUT1}. When ENABLE is high, V_{OUT1} is on. When ENABLE is low, V_{OUT1} is off.

Calculating Power Dissipation in a Dual Output Linear Regulator

The maximum power dissipation for a dual output regulator (Figure 1) is:

$$P_{D(MAX)} = (V_{IN(MAX)} - V_{OUT1(MIN)})(I_{OUT1(MAX)}) + (V_{IN(MAX)} - V_{OUT2(MIN)})(I_{OUT2(MAX)}) + (V_{IN(MAX)})(I_Q) \quad (1)$$

where

$V_{IN(MAX)}$ is the maximum input voltage;
$V_{OUT1(MIN)}$ is the minimum output voltage from V_{OUT1};
$V_{OUT2(MIN)}$ is the minimum output voltage from V_{OUT2};
$I_{OUT1(MAX)}$ is the maximum output current for the application;
$I_{OUT2(MAX)}$ is the maximum output current for the application; and
I_Q is the quiescent current the regulator consumes at both $I_{OUT1(MAX)}$ and $I_{OUT2(MAX)}$.

Once the value of $P_{D(MAX)}$ is known, the maximum permissible value of $R_{\Theta JA}$ can be calculated:

$$R_{\Theta JA} = \frac{150°C - T_A}{P_D} \quad (2)$$

The value of $R_{\Theta JA}$ can then be compared with those in the package section of the data sheet. Those packages with $R_{\Theta JA}$'s less than the calculated value in equation 2 will keep the die temperature below 150°C.

In some cases, none of the packages will be sufficient to dissipate the heat generated by the IC, and an external heat sink will be required.

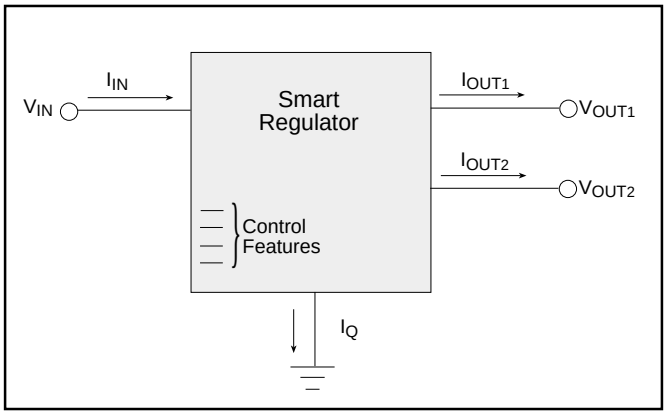


Figure 1: Dual output regulator with key performance parameters labeled.

Heat Sinks

A heat sink effectively increases the surface area of the package to improve the flow of heat away from the IC and into the surrounding air.

Each material in the heat flow path between the IC and the outside environment will have a thermal resistance. Like series electrical resistances, these resistances are summed to determine the value of $R_{\Theta JA}$.

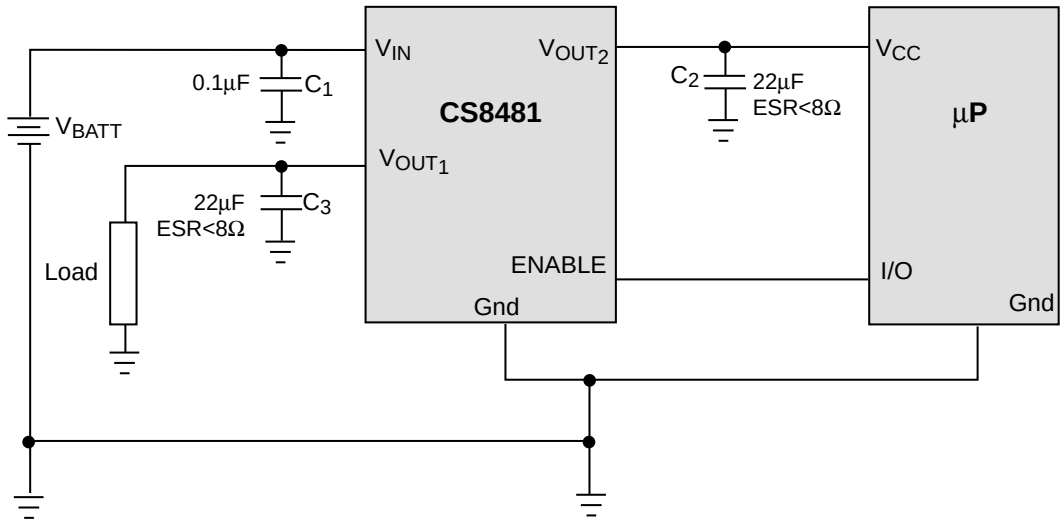
$$R_{\Theta JA} = R_{\Theta JC} + R_{\Theta CS} + R_{\Theta SA} \quad (3)$$

where:

$R_{\Theta JC}$ = the junction–to–case thermal resistance;
$R_{\Theta CS}$ = the case–to–heat sink thermal resistance; and
$R_{\Theta SA}$ = the heat sink–to–ambient thermal resistance.

$R_{\Theta JC}$ appears in the package section of the data sheet. Like $R_{\Theta JA}$, it too is a function of package type. $R_{\Theta CS}$ and $R_{\Theta SA}$ are functions of the package type, heat sink and the interface between them. These values appear in heat sink data sheets of heat sink manufacturers.

Test & Application Circuit



* C1 required if regulator is located far from power supply filter.
** C2 and C3 required for stability. Capacitor must operate at minimum temperature expected during system operations.

Package Specification

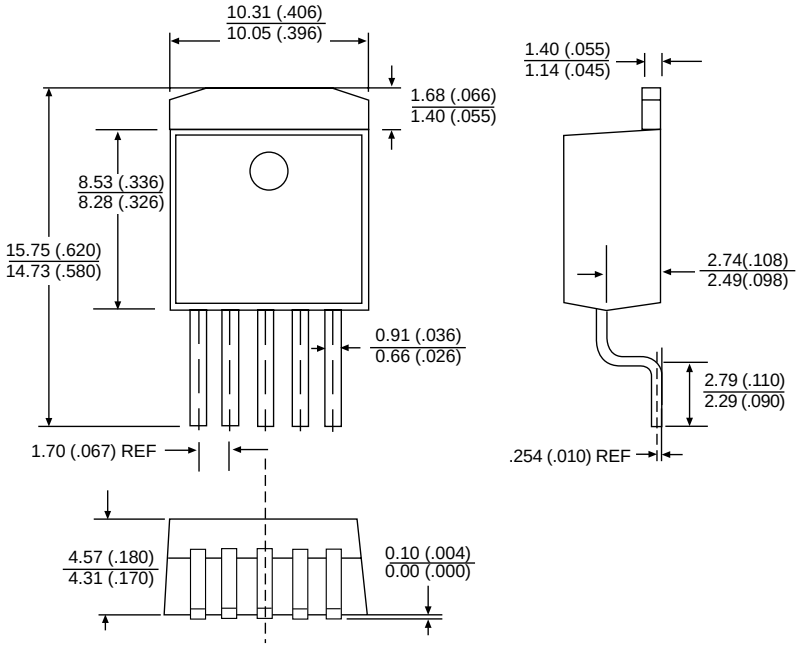
PACKAGE DIMENSIONS IN MM(INCHES)

PACKAGE THERMAL DATA

Thermal Data		5 Lead D^2 PAK	
$R_{\Theta JC}$	typ	2.4	°C / W
$R_{\Theta JA}$	typ	10-50*	°C / W

** Depending on thermal properties of substrate.* $R_{\Theta JA} = R_{\Theta JC} + R_{\Theta CA}$

5 Lead D^2PAK (DP)



Ordering Information

Part Number	Description
CS8481YDP5	5 Lead D^2PAK
CS8481YDPR5	5 Lead D^2PAK *(tape & reel)*

Notes

Notes