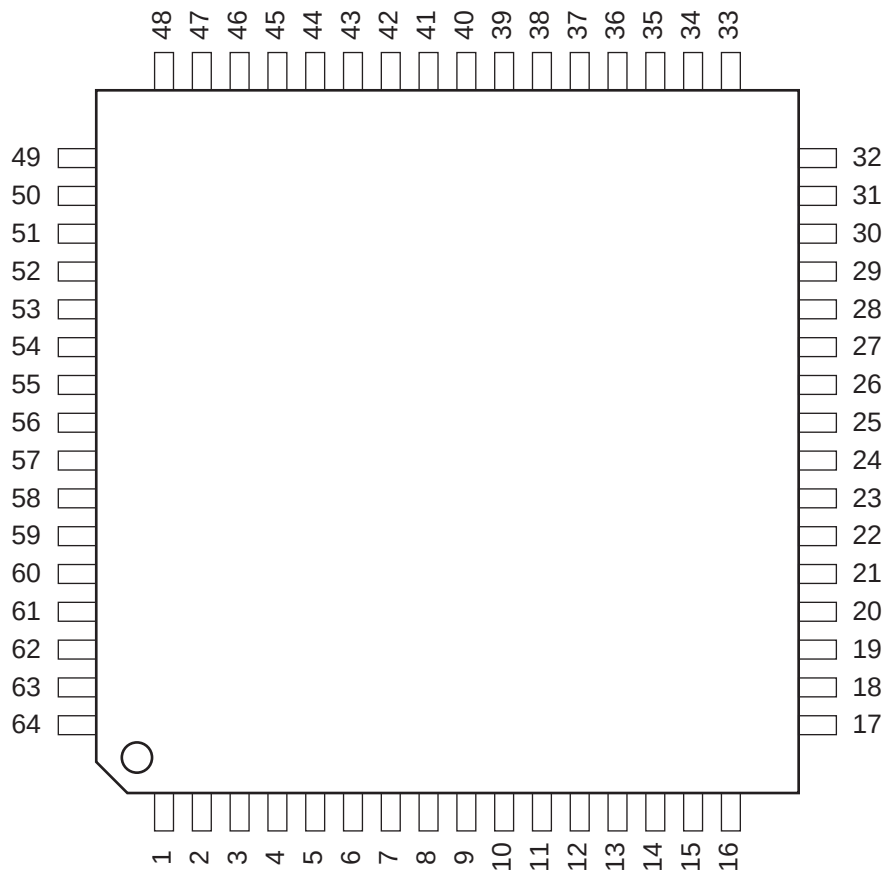


8 × 8 DIGITAL CROSSPOINT SWITCH

—TOP VIEW—



INPUTS

CNFG	: LATCH CONTROL
CS	: CHIP SELECT
DI0 - DI7, $\overline{\text{DI0}}$ - $\overline{\text{DI7}}$	: DATA
IA0 - IA2	: INPUT ADDRESS BUS
LOAD	: LATCH CONTROL
OA1 - OA3	: OUTPUT ADDRESS BUS
RES	: RESET CONTROL
TRI	: OUTPUT CONTROL

OUTPUTS

DO0 - DO7, $\overline{\text{DO0}}$ - $\overline{\text{DO7}}$: DATA

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{CC}	17	O	$\overline{\text{DO6}}$	33	O	DO1	49	—	V _{EE}
2	I	DI6	18	O	DO6	34	—	V _{CC}	50	I	DI1
3	I	$\overline{\text{DI6}}$	19	—	V _{CC}	35	O	$\overline{\text{DO0}}$	51	I	$\overline{\text{DI1}}$
4	—	V _{CC}	20	O	$\overline{\text{DO5}}$	36	O	DO0	52	—	V _{CC}
5	I	DI7	21	O	DO5	37	—	V _{CC}	53	I	DI2
6	I	$\overline{\text{DI7}}$	22	—	V _{CC}	38	O	OA2	54	I	$\overline{\text{DI2}}$
7	—	V _{CC}	23	O	$\overline{\text{DO4}}$	39	O	OA1	55	—	V _{CC}
8	I	CS	24	O	DO4	40	O	OA0	56	I	DI3
9	I	RES	25	—	V _{CC}	41	I	TRI	57	I	$\overline{\text{DI3}}$
10	I	CNFG	26	O	$\overline{\text{DO3}}$	42	I	IA2	58	—	V _{CC}
11	I	LOAD	27	O	DO3	43	I	IA1	59	I	DI4
12	—	V _{EE}	28	—	V _{CC}	44	I	IA0	60	I	$\overline{\text{DI4}}$
13	—	V _{CC}	29	O	$\overline{\text{DO2}}$	45	—	V _{LL}	61	—	V _{CC}
14	O	$\overline{\text{DO7}}$	30	O	DO2	46	I	DI0	62	I	DI5
15	O	DO7	31	—	V _{EE}	47	I	$\overline{\text{DI0}}$	63	I	$\overline{\text{DI5}}$
16	—	V _{CC}	32	O	$\overline{\text{DO1}}$	48	—	V _{CC}	64	—	V _{CC}

