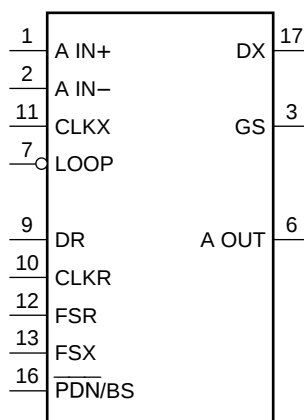
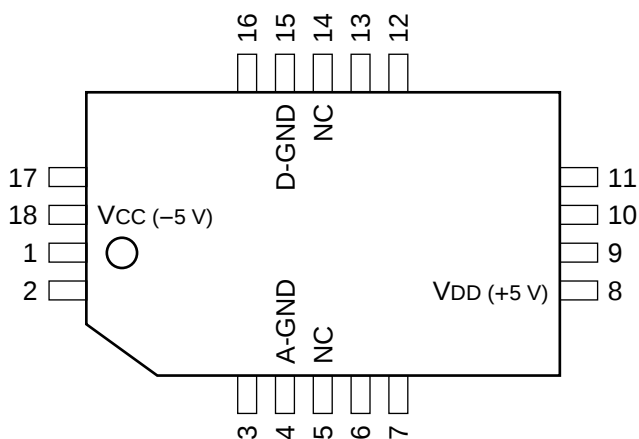


C-MOS PCM CODEC

—TOP VIEW—



(VDD = +5 V, VSS = -5 V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	A IN+	10	I	CLKR
2	I	A IN-	11	I	CLKX
3	O	GSX	12	I	FSR
4	—	A-GND	13	I	FSX
5	—	NC	14	—	NC
6	O	A OUT	15	—	D-GND
7	I	LOOP	16	I	PDN/BS
8	—	VDD	17	O	DX
9	I	DR	18	—	VSS

INPUT

A IN+ : NON-INVERTING INPUT OF OPERATIONAL AMPLIFIER

A IN- : INVERTING INPUT OF OPERATIONAL AMPLIFIER

CLKR : RECEIVE CHANNEL DATA CLOCK

CLKX : TRANSMIT CHANNEL DATA CLOCK

DR : PCM RECEIVE DATA

FSR : RECEIVE CHANNEL FRAME SYNC CLOCK

FSX : TRANSMIT CHANNEL FRAME SYNC CLOCK

LOOP : ANALOG LOOP-BACK TEST CONTROL

PDN/BS : POWER DOWN/BIT STILL CONTROL

OUTPUT

A OUT : RECEIVE AMPLIFIER

DX : PCM TRANSMIT DATA

GSX : OUTPUT OF OPERATIONAL AMPLIFIER

