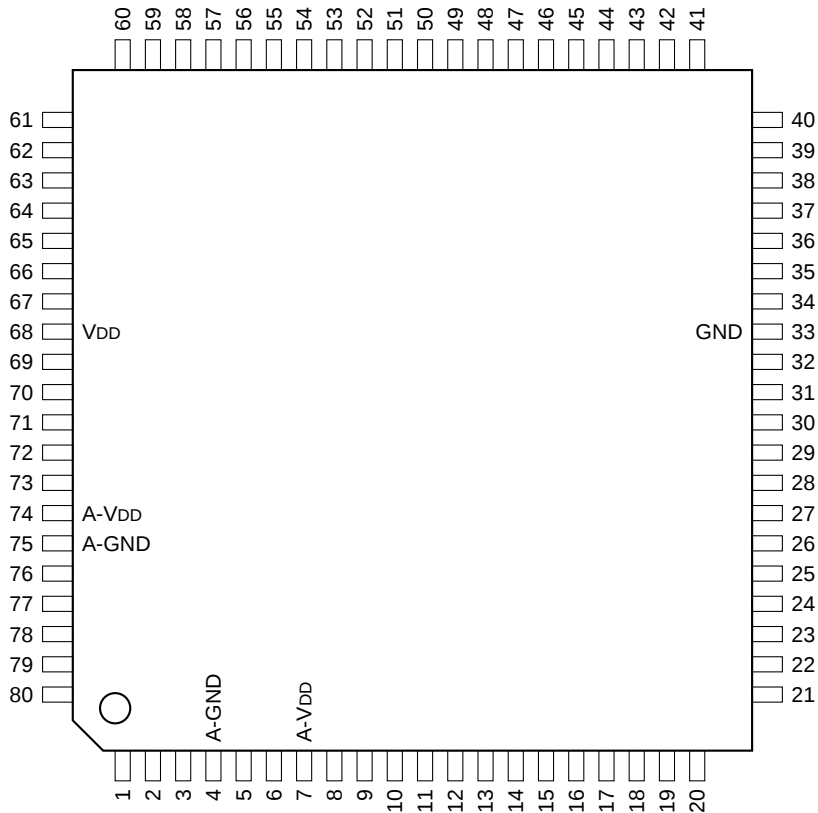


C-MOS 8-BIT SINGLE-CHIP MICROCOMPUTER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	P15/ANI5	21	I/O	P42/AD2	41	I/O	P65/ $\overline{\text{WR}}$	61	I	P00/INTP0/TI00
2	I/O	P16/ANI6	22	I/O	P43/AD3	42	I/O	P66/ $\overline{\text{WAIT}}$	62	I/O	P01/INTP1/TI01
3	I/O	P17/ANI7	23	I/O	P44/AD4	43	I/O	P67/ASTB	63	I/O	P02/INTP2
4	—	A-GND	24	I/O	P45/AD5	44	I/O	P30/TO0	64	I/O	P03/INTP3
5	I/O	P130/ANO0	25	I/O	P46/AD6	45	I/O	P31/TO1	65	I/O	P04/INTP4
6	I/O	P131/ANO1	26	I/O	P47/AD7	46	I/O	P32/TO2	66	I/O	P05/INTP5
7	—	A-VREF1	27	I/O	P50/A8	47	I/O	P33/TI1	67	I/O	P06/INTP6
8	I/O	P70/SI2/RXD	28	I/O	P51/A9	48	I/O	P34/TI2	68	—	VDD
9	I/O	P71/SQ2/TXD	29	I/O	P52/A10	49	I/O	P35/PCL	69	I	X2
10	I/O	P72/ $\overline{\text{SCK2}}$ /ASCK	30	I/O	P53/A11	50	I/O	P36/BUZ	70	I	X1
11	I/O	P20/SI1	31	I/O	P54/A12	51	I/O	P37	71	—	VPP
12	I/O	P21/SO1	32	I/O	P55/A13	52	I/O	P120/RTP0	72	I	XT2
13	I/O	P22/ $\overline{\text{SCK1}}$	33	—	GND	53	I/O	P121/RTP1	73	I	XT1/P07
14	I/O	P23/STB	34	I/O	P56/A14	54	I/O	P122/RTP2	74	—	A-VDD
15	I/O	P24/BUSY	35	I/O	P57/A15	55	I/O	P123/RTP3	75	—	A-VREF0
16	I/O	P25/SI0/SB0	36	I/O	P60	56	I/O	P124/RTP4	76	I/O	P10/ANI0
17	I/O	P26/SO0/SB1	37	I/O	P61	57	I/O	P125/RTP5	77	I/O	P11/ANI1
18	I/O	P27/ $\overline{\text{SCK0}}$	38	I/O	P62	58	I/O	P126/RTP6	78	I/O	P12/ANI2
19	I/O	P40/AD0	39	I/O	P63	59	I/O	P127/RTP7	79	I/O	P13/ANI3
20	I/O	P41/AD1	40	I/O	P64/ $\overline{\text{RD}}$	60	I	$\overline{\text{RESET}}$	80	I/O	P14/ANI4

INPUT

ANI0 - ANI7	: ANALOG SIGNAL FOR ADC
ASCK	: SERIAL CLOCK
BUSY	: BUSY
INT0 - INT6T	: INTERRUPT
INTP0 - INTP6	: INTERRUPT
P00, P07	: PORT
$\overline{\text{RESET}}$	: SYSTEM RESET
RXD	: SERIAL DATA
SI0 - SI2	: SERIAL DATA
TI00	: EXTERNAL COUNT CLOCK
TI01	: CAPTURE TRIGGER SIGNAL
TI1, TI2	: EXTERNAL COUNT CLOCK
$\overline{\text{WAIT}}$	: WAIT
X1, X2	: CRYSTAL OSCILLATOR FOR MAIN SYSTEM
XT1, XT2	: CRYSTAL OSCILLATOR FOR SUB SYSTEM

OUTPUT

A8 - A15	: UPPER ADDRESS BUS
ANO0, ANO1	: ANALOG SIGNAL FROM ADC
ASTB	: STROBE
BUZ	: BUZZER
PCL	: CLOCK
$\overline{\text{RD}}$	: MEMORY READ STROBE
RTP0 - RTP7	: REAL TIME OUTPUT PORT
SO0 - SO2	: SERIAL DATA
STB	: STROBE
TO0	: 16-BIT TIMER
TO1, TO2	: 8-BIT TIMER
TXD	: SERIAL DATA
$\overline{\text{WR}}$	: MEMORY WRITE STROBE

INPUT/OUTPUT

AD0 - AD7	: LOWER ADDRESS/DATA BUS
P01 - P06, P10 - P17,	: PORT
P20 - P27, P30 - P37,	
P40 - P47, P50 - P57,	
P60 - P67, P70 - P72,	
P120 - P127,	
P130, P131	
$\overline{\text{SB0}}, \overline{\text{SB1}}$	: SERIAL DATA
$\overline{\text{SCK0}} - \overline{\text{SCK2}}$	: SERIAL CLOCK

