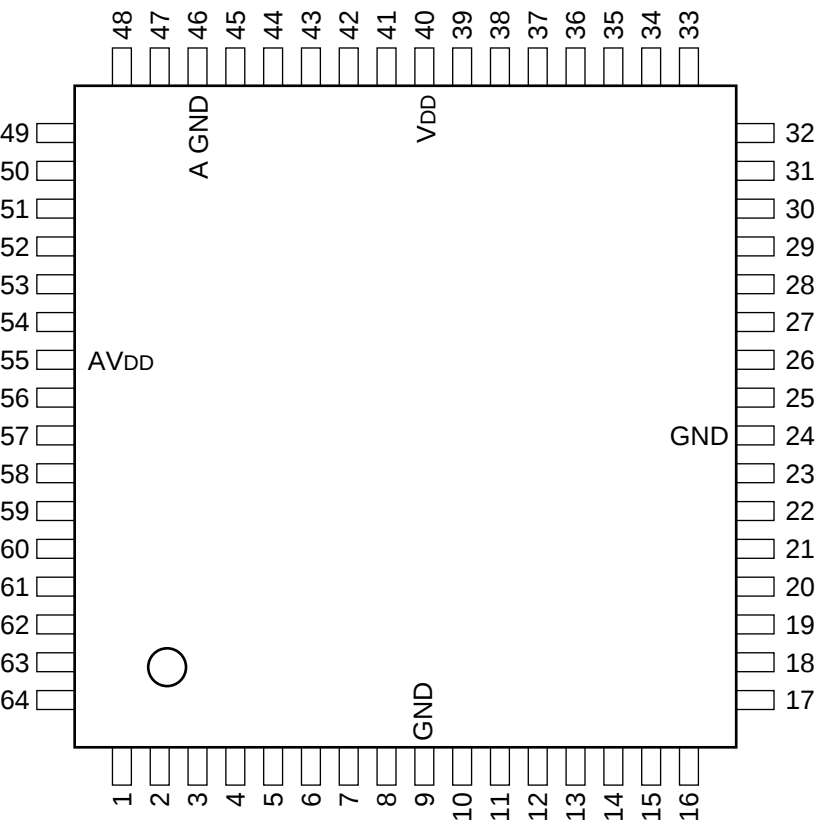


C-MOS 8-BIT SINGLE CHIP MICROCOMPUTER WITH MASK ROM

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	P30/TO0	17	I/O	P47/AD7	33	I/O	P66/WAIT	49	I	P12/AN12
2	I/O	P31/TO1	18	I/O	P50/A8	34	I/O	P67/ASTB	50	I	P13/AN13
3	I/O	P32/TO2	19	I/O	P51/A9	35	I	RESET	51	I	P14/AN14
4	I/O	P33/T11	20	I/O	P52/A10	36	I	P00/INTP0/T10	52	I	P15/AN15
5	I/O	P34/T12	21	I/O	P53/A11	37	I/O	P01/INTP1	53	I	P16/AN16
6	I/O	P35/PCL	22	I/O	P54/A12	38	I/O	P02/INTP2	54	I	P17/AN17
7	I/O	P36/BUZ	23	I/O	P55/A13	39	I/O	P03/INTP3	55	—	AVDD
8	I/O	P37	24	—	GND	40	—	VDD	56	—	AVREF
9	—	GND	25	I/O	P56/A14	41	O	X2	57	I/O	P20/SI1
10	I/O	P40/AD0	26	I/O	P57/A15	42	I	X1	58	I/O	P21/SO1
11	I/O	P41/AD1	27	I/O	P60	43	—	MODE	59	I/O	P22/SCK1
12	I/O	P42/AD2	28	I/O	P61	44	—	XT2	60	I/O	P23/STB
13	I/O	P43/AD3	29	I/O	P62	45	I	P04/XT1	61	I/O	P24/BUSY
14	I/O	P44/AD4	30	I/O	P63	46	—	A GND	62	I/O	P25/S10/SB0
15	I/O	P45/AD5	31	I/O	P64/RD	47	I	P10/AN10	63	I/O	P26/S00/SB1
16	I/O	P46/AD6	32	I/O	P65/WR	48	I	P11/AN11	64	I/O	P27/SCK0

35	$\overline{\text{RESET}}$	P30/TO0	1	INPUT	
		P31/TO1	2		ANI0 - ANI7 ; ANALOG 0 - 7
41	X2	P32/TO2	3		AVREF ; ANALOG REFERENCE VOLTAGE
		P33/TI1	4		BUSY ; BUSY
42	X1	P34/TI2	5		INTP0 - INTP3 ; INTERRUPT
44	XT2	P35/PCL	6		FROM PERIPHERALS 0 - 3
		P36/BUZ	7		MODE ; ROM-LESS MODE
		P37	8		P00, P04 ; PORT 0
					P10 - P17 ; PORT 1
36	P00/INTP1/T10				$\overline{\text{RESET}}$; RESET
		P40/AD0	10		SI0, SI1 ; SERIAL 0, 1
37	P01/INTP1	P41/AD1	11		TI0 - TI2 ; TIMER 0 - 2
38	P02/INTP2	P42/AD2	12		$\overline{\text{WAIT}}$; WAIT
39	P03/INTP3	P43/AD3	13		X1, X2 ; CRYSTAL 1, 2 (MAIN SYSTEM CLOCK)
45	P04/XT1	P44/AD4	14		XT1, XT2 ; CRYSTAL 1, 2 (SUB SYSTEM CLOCK)
56	AVREF	P45/AD5	15	OUTPUT	
		P46/AD6	16		A8 - A15 ; ADDRESS BUS 8 - 15
		P47/AD7	17		$\overline{\text{ASTB}}$; ADDRESS STROBE
					BUZ ; BUZZER CLOCK
47	P10/ANI0	P50/A8	18		PCL ; PROGRAMMABLE CLOCK
48	P11/ANI1	P51/A9	19		$\overline{\text{RD}}$; READ STROBE
49	P12/ANI2	P52/A10	20		SO0, SO1 ; SERIAL 0, 1
50	P13/ANI3	P53/A11	21		STB ; STROBE
51	P14/ANI4	P54/A12	22		TO0 - TO2 ; TIMER 0 - 2
52	P15/ANI5	P55/A13	23		$\overline{\text{WR}}$; WRITE STROBE
53	P16/ANI6	P56/A14	25	INPUT/OUTPUT	
54	P17/ANI7	P57/A15	26		AD0 - AD7 ; ADDRESS / DATA BUS 0 - 7
57	P20/SI1	P60	27		P01 - P03 ; PORT 0
		P61	28		P20 - P27 ; PORT 2
58	P21/SO1	P62	29		P30 - P37 ; PORT 3
59	P22/ $\overline{\text{SCK1}}$	P63	30		P40 - P47 ; PORT 4
60	P23/STB				P50 - P57 ; PORT 5
61	P24/BUSY	P64/ $\overline{\text{RD}}$	31		P60 - P67 ; PORT 6
62	P25/SI0/SB0	P65/ $\overline{\text{WR}}$	32		SB0, SB1 ; SERIAL BUS 0, 1
63	P26/S00/SB1	P66/ $\overline{\text{WAIT}}$	33		$\overline{\text{SCK0}}$, $\overline{\text{SCK1}}$; SERIAL CLOCK 0, 1
64	P27/ $\overline{\text{SCK0}}$	P67/ $\overline{\text{ASTB}}$	34		
MODE					

