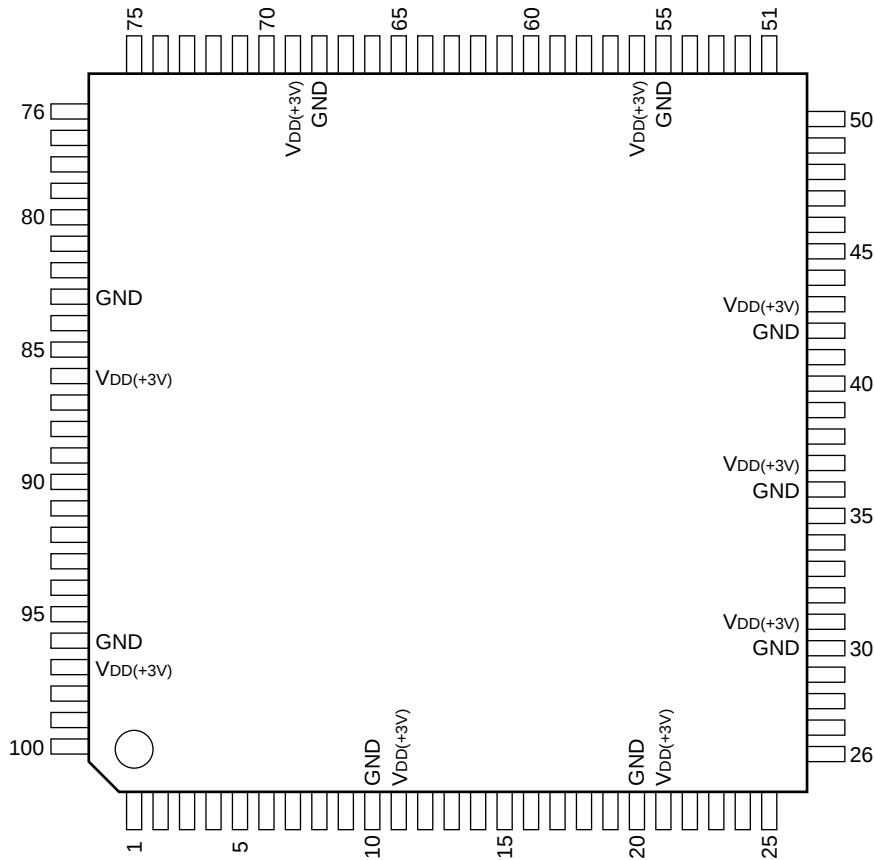

C-MOS 16-BIT DIGITAL SIGNAL PROCESSOR

- TOP VIEW -

(V_{DD} = +3V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	RESET	21	–	V _{DD}	41	I/O	D4	61	I	SI2	81	I	HA0
2	I	INT4	22	O	DA3	42	–	GND	62	I/O	P3	82	I	HA1
3	I	INT3	23	O	DA2	43	–	V _{DD}	63	I/O	P2	83	–	GND
4	I	INT2	24	O	DA1	44	I/O	D3	64	I/O	P1	84	I	X2
5	I	INT1	25	O	DA0	45	I/O	D2	65	I/O	P0	85	I	X1
6	I	I.C.	26	I/O	D15	46	I/O	D1	66	O	HRE	86	–	V _{DD}
7	O	X/Y	27	I/O	D14	47	I/O	D0	67	O	HWE	87	O	CLKOUT
8	O	DA13	28	I/O	D13	48	I	SI1	68	–	GND	88	O	TDO
9	O	DA12	29	I/O	D12	49	I	SIEN1	69	–	V _{DD}	89	O	TICE
10	–	GND	30	–	GND	50	I	SCK1	70	I/O	HD7	90	I	TCK
11	–	V _{DD}	31	–	V _{DD}	51	O	SIAK1	71	I/O	HD6	91	I	TDI
12	O	DA11	32	I/O	D11	52	O	SO1	72	I/O	HD5	92	I	TMS
13	O	DA10	33	I/O	D10	53	O	SORQ1	73	I/O	HD4	93	I	HOLD $\overline{\text{RQ}}$
14	O	DA9	34	I/O	D9	54	I	SOEN1	74	I/O	HD3	94	O	HOLD $\overline{\text{AK}}$
15	O	DA8	35	I/O	D8	55	–	GND	75	I/O	HD2	95	O	MWR
16	O	DA7	36	–	GND	56	–	V _{DD}	76	I/O	HD1	96	–	GND
17	O	DA6	37	–	V _{DD}	57	I	SOEN2	77	I/O	HD0	97	–	V _{DD}
18	O	DA5	38	I/O	D7	58	O	SO2	78	I	HCS	98	O	MRD
19	O	DA4	39	I/O	D6	59	I	SCK2	79	I	HRD	99	O	BSTB
20	–	GND	40	I/O	D5	60	I	SIEN2	80	I	HWR	100	I	WAIT

INPUT

HA0, HA1	; HD7-HD0 ACCESS REGISTERS POINTER
HCS	; HOST CHIP SELECT
HOLDRQ	; HOLD REQUEST
HRD	; HOST READ
HWR	; HOST WRITE
I. C.	; INTERNAL CONNECTED
INT1-INT4	; MASKABLE EXTERNAL INTERRUPT
RESET	; INTERNAL SYSTEM RESET
SCK1, SCK2	; SERIAL CLOCK
SI1, SI2	; SERIAL DATA
SIEN1, SIEN2	; SERIAL INPUT ENABLE
SOEN1, SOEN2	; SERIAL OUTPUT ENABLE
TCK	; TEST CLOCK
TDI	; TEST DATA
TMS	; TEST MODE SELECT
WAIT	; WAIT (0 : WAIT, 1 : NOT WAIT)
X1, X2	; OSCILLATOR/CLOCK

OUTPUT

BSTB	; BUS STROBE
CLKOUT	; INTERNAL SYSTEM CLOCK
DA0-DA13	; EXTERNAL DATA/ MEMORY ADDRESS BUS
HOLDAK	; HOLD ACKNOWLEDGE
HRE	; HOST READ ENABLE
HWE	; HOST WRITE ENABLE
MRD	; EXTERNAL MEMORY READ
MWR	; EXTERNAL MEMORY WRITE
SIACK1	; SERIAL INPUT ACKNOWLEDGE
SO1, SO2	; SERIAL DATA
SORQ1,	; SERIAL OUTPUT REQUEST
TDO	; TEST DATA
TICE	; TEST IN-CIRCUIT EMULATOR
X/Y	; MEMORY SELECT

INPUT/OUTPUT

D0-D15	; 16-BIT DATA BUS
HD0-HD7	; 8-BIT HOST DATA BUS
P0-P3	; INPUT/OUTPUT PORT

