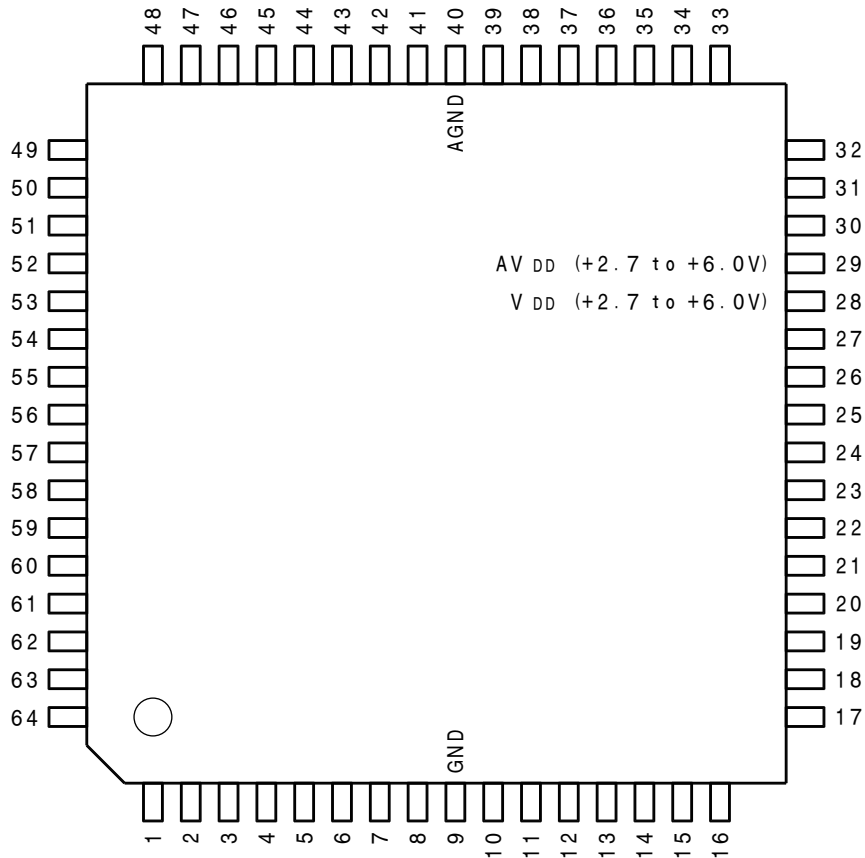

C-MOS 4-BIT SINGLE CHIP MICROCOMPUTER WITH ONE TIME PROM

-TOP VIEW-

(AV_{DD} = V_{DD} = +2.7 to +6.0V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I/O	P43	17	I/O	PT00/P20	33	I	AN6	49	I/O	P83
2	I/O	P42	18	I/O	MAT/P103	34	I	AN5	50	I/O	P82
3	I/O	P41	19	I/O	MAZ/P102	35	I	AN4	51	I/O	P81
4	I/O	P40	20	O	MAI/P101	36	I	AN3/P113	52	I/O	P80
5	I/O	P33/MD3	21	O	MAR/P100	37	I	AN2/P112	53	I/O	P73/KR7
6	I/O	P32/MD2	22	I	RESET	38	I	AN1/P111	54	I/O	P72/KR6
7	I/O	P31/MD1	23	I	X1	39	I	AN0/P110	55	I/O	P71/KR5
8	I/O	P30/MD0	24	-	X2	40	-	AGND	56	I/O	P70/KR4
9	-	GND	25	-	VPP	41	I	T10/P13	57	I/O	P63/KR3
10	I/O	SB1/SI/P03	26	I	XT1	42	I	P12/INT2	58	I/O	P62/KR2
11	I/O	SB0/S0/P02	27	-	XT2	43	I	P11/INT1	59	I/O	P61/KR1
12	I/O	SCK/P01	28	-	VDD	44	I	P10/INT0	60	I/O	P60/KR0
13	I	INT4/P00	29	-	AVDD	45	I/O	P93	61	I/O	P53
14	I/O	BUZ/P23	30	I	AVREF+	46	I/O	P92	62	I/O	P52
15	I/O	PCL/P22	31	I	AVREF-	47	I/O	P91	63	I/O	P51
16	I/O	PPO/P21	32	I	AN7	48	I/O	P90	64	I/O	P50

23	X1	MD0/P30	8	PORT	
24	X2	MD1/P31	7	P00-P03	; PORT 0 IN/OUT
		MD2/P32	6	P10-P13	; PORT 1 IN
26	XT1	MD3/P33	5	P20-P23	; PORT 2 IN/OUT
27	XT2		4	P30-P33	; PORT 3 IN/OUT
		P40	3	P40-P43	; PORT 4 IN/OUT
39	AN0/P110	P41	2	P50-P53	; PORT 5 IN/OUT
38	AN1/P111	P42	1	P60-P63	; PORT 6 IN/OUT
37	AN2/P112	P43		P70-P73	; PORT 7 IN/OUT
36	AN3/P113			P80-P83	; PORT 8 IN/OUT
35	AN4	P50	64	P90-P93	; PORT 9 IN/OUT
34	AN5	P51	63	P100-P103	; PORT 10 IN/OUT
33	AN6	P52	62	P110-P113	; PORT 11 IN
32	AN7	P53	61		
				OTHERS	
44	P10/INT0	P60/KR0	60	AN0-AN7	; ANALOG 0-7 IN
43	P11/INT1	P61/KR1	59	AVREF +	; ANALOG REFERENCE (+) IN
42	P12/INT2	P62/KR2	58	AVREF -	; ANALOG REFERENCE (-) IN
41	P13/TI0	P63/KR3	57	BUZ	; BUZZER CLOCK OUT
				INT0, 1, 4	; EXTERNAL VECTORED INTERRUPT 0, 1, 4 IN
30	AVREF +	P70/KR4	56	INT2	; EXTERNAL TEST 2 IN
31	AVREF -	P71/KR5	55	KR0-KR7	; KEY RETURN 0-7 IN
		P72/KR6	54	MAI	; INTEGRATION CONTROL (MFT A/D MODE) OUT
22	RESET	P73/KR7	53	MAR	; REFERENCE INTEGRATION CONTROL OUT (MFT A/D MODE)
21	MAR/P100	P80	52	MAT	; EXTERNAL COMPARE TIMING IN/OUT (MFT A/D MODE)
20	MAI/P101	P81	51	MAZ	; AUTOZERO CONTROL (MFT A/D MODE) OUT
19	MAZ/P102	P82	50	MD0-MD3	; MODE SELECT AT PROM WRITE/VERIFY
18	MAT/P103	P93	49	PCL	; PROGRAMMABLE CLOCK OUT
				PPO	; PROGRAMMABLE PULSE (MFT TIMER MODE) OUT
17	P20/PT00	P90	48	PT00	; PROGRAMMABLE TIMER 0 OUT
16	P21/PPO	P91	47	RESET	; RESET IN
15	P22/PCL	P92	46	SB0, 1	; SERIAL BUS 0, 1 IN/OUT
14	P23/BUZ	P93	45	SCK	; SERIAL CLOCK IN/OUT
				SO	; SERIAL OUT
13	P00/INT4			SI	; SERIAL IN
12	P01/SCK			TI0	; TIMER 0 IN
11	P02/SO/SB0			X1, 2	; MAIN SYSTEM CLOCK OSCILLATION 1, 2 IN
10	P03/SI/SB1			XT1, 2	; SUB SYSTEM CLOCK OSCILLATION 1, 2 IN

