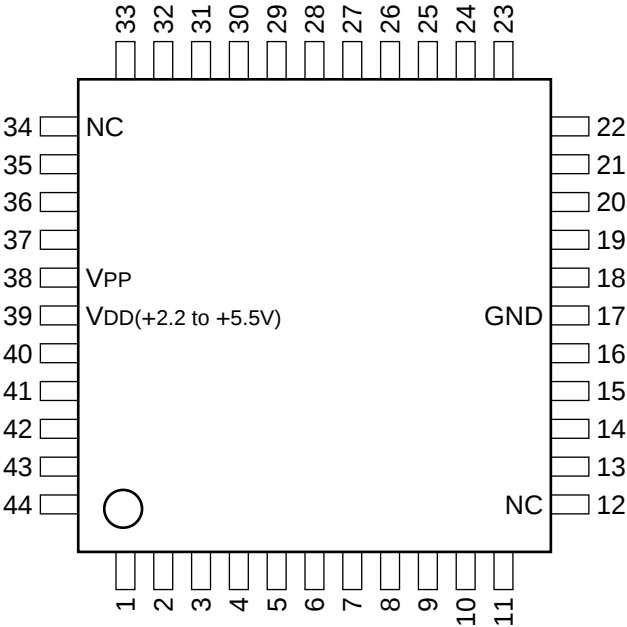

C-MOS 4-BIT SINGLE CHIP ONE TIME PROM

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	P72/KR6	12	—	NC	23	I/O	MD3	34	—	NC
2	I/O	P71/KR5	13	I/O	P43	24	I/O	MD2	35	I	P12/INT2
3	I/O	P70/KR4	14	I/O	P42	25	I/O	MD1	36	I	P11/INT1
4	I/O	P63/KR3	15	I/O	P41	26	I/O	MD0	37	I	P10/INT0
5	I/O	P62/KR2	16	I/O	P40	27	I/O	P81	38	—	VPP
6	I/O	P61/KR1	17	—	GND	28	I/O	P80	39	—	VDD
7	I/O	P60/KR0	18	I	XT1	29	I/O	P03/SI/SB1	40	I/O	P23/BUZ
8	I/O	P53	19	—	XT2	30	I/O	P02/SO/SB0	41	I/O	P22/PCL
9	I/O	P52	20	I	RESET	31	I/O	P01/SCK	42	I/O	P21/PTO1
10	I/O	P51	21	I	X1	32	I	P00/INT4	43	I/O	P20/PTO0
11	I/O	P50	22	I	X2	33	I	P13/TI0	44	I/O	P73/KR7

INPUT

INT0, INT1, INT4 ; EXTERNAL VECTORED INTERRUPT
INT2 ; EXTERNAL TEST INPUT
KR0 - KR7 ; KEY RETURN
P00 - P03 ; PORT0
P10 - P13 ; PORT1
 $\overline{\text{RESET}}$; RESET
SCK ; SERIAL CLOCK
SI ; SERIAL INPUT
TI0 ; TIMER
X1, X2 ; MAIN SYSTEM CLOCK OSCILLATOR
XT1, XT2 ; SUB SYSTEM CLOCK OSCILLATOR

OUTPUT

BUZ ; BUZZER CLOCK
PCL ; PROGRAMMABLE CLOCK
PTO0, PTO1 ; PROGRAMMABLE TIMER
SB0, SB1 ; SERIAL BUS
SO ; SERIAL OUTPUT

INPUT/OUTPUT

MD0 - MD3 ; MODE SELECT
P20 - P23 ; PORT2
P40 - P43 ; PORT4
P50 - P53 ; PORT5
P60 - P63 ; PORT6
P70 - P73 ; PORT7
P80, P81 ; PORT8