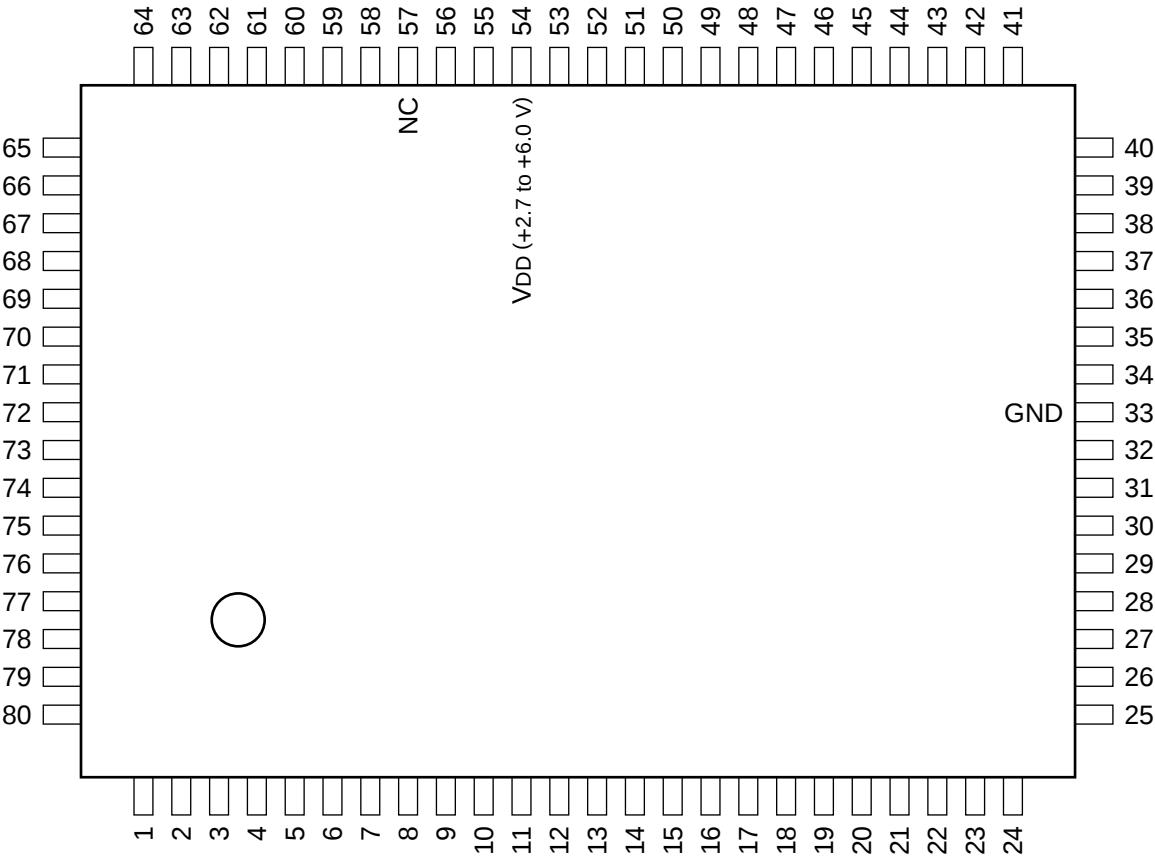


C-MOS 4-BIT SINGLE-CHIP MICROCOMPUTER
—TOP VIEW—



(VDD = +2.7 to +6.0 V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	S12	21	O	COM0	41	I/O	P03/SI/SB1	61	I/O	P61/KR1
2	O	S13	22	O	COM1	42	I	P10/INT0	62	I/O	P62/KR2
3	O	S14	23	O	COM2	43	I	P11/INT1	63	I/O	P63/KR3
4	O	S15	24	O	COM3	44	I	P12/INT2	64	I/O	P70/KR4
5	O	S16	25	O	BIAS	45	I	P13/TI0	65	I/O	P71/KR5
6	O	S17	26	I	VLC0	46	I/O	P20/PTO0	66	I/O	P72/KR6
7	O	S18	27	I	VLC1	47	I/O	P21	67	I/O	P73/KR7
8	O	S19	28	I	VLC2	48	I/O	P22/PCL	68	I	RESET
9	O	S20	29	I/O	P40	49	I/O	P23/BUZ	69	O	S0
10	O	S21	30	I/O	P41	50	I/O	P30/LCDCL	70	O	S1
11	O	S22	31	I/O	P42	51	I/O	P31/SYNC	71	O	S2
12	O	S23	32	I/O	P43	52	I/O	P32	72	O	S3
13	O	S24/BP0	33	—	GND	53	I/O	P33	73	O	S4
14	O	S25/BP1	34	I/O	P50	54	—	VDD	74	O	S5
15	O	S26/BP2	35	I/O	P51	55	I	XT1	75	O	S6
16	O	S27/BP3	36	I/O	P52	56	I	XT2	76	O	S7
17	O	S28/BP4	37	I/O	P53	57	—	NC	77	O	S8
18	O	S29/BP5	38	I	P00/INT4	58	I	X1	78	O	S9
19	O	S30/BP6	39	I/O	P01/SCK	59	I	X2	79	O	S10
20	O	S31/BP7	40	I/O	P02/SO/SB0	60	I/O	P60/KR0	80	O	S11

INPUT

INT0, INT1, INT4	: EXTERNAL VECTORED INTERRUPT
INT2	: EXTERNAL TEST
KR0 - KR7	: KEY RETURN
P00 - P03	: PORT 0
P10 - P13	: PORT 1
$\overline{\text{RESET}}$	: RESET
SI	: SERIAL INPUT
TI0	: TIMER INPUT 0
VLC0 - VLC2	: LCD POWER SUPPLY
X1, X2	: MAIN SYSTEM CLOCK OSCILLATION
XT1, XT2	: SUBSYSTEM CLOCK OSCILLATION

OUTPUT

BIAS	: LCD POWER SUPPLY BIAS CONTROL
BP0 - BP7	: BIT PORT
BUZ	: BUZZER CLOCK
COM0 - COM3	: COMMON
LCDCL	: LCD CLOCK
PCL	: PROGRAMMABLE CLOCK
S0 - S31	: SEGMENT
SO	: SERIAL OUTPUT
SYNC	: LCD SYNCHRONIZATION
PTO0	: PROGRAMMABLE TIMER

INPUT/OUTPUT

P20 - P23	: PORT2
P30 - P33	: PORT3
P40 - P43	: PORT4
P50 - P53	: PORT5
P60 - P63	: PORT6
P70 - P73	: PORT7
SB0, SB1	: SERIAL BUS
$\overline{\text{SCK}}$	: SERIAL CLOCK

