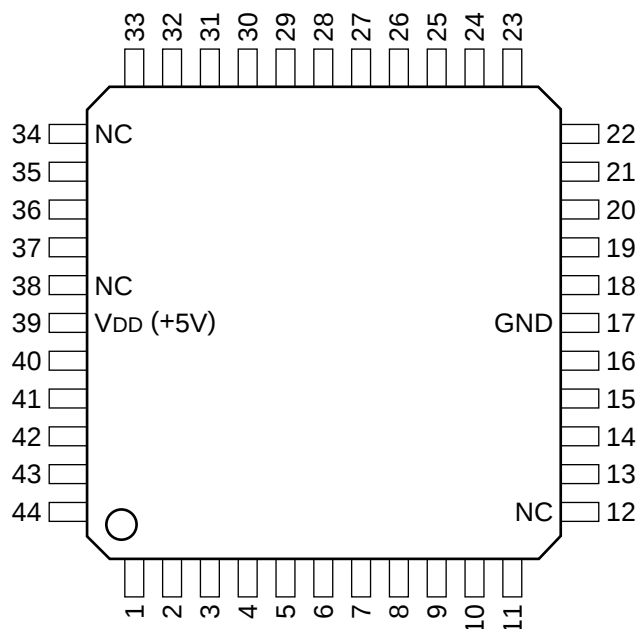

C-MOS 4-BIT SINGLE CHIP MICROCOMPUTER

—TOP VIEW—



(VDD = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	P72/KR6	12	—	NC	23	I/O	P33	34	—	NC
2	I/O	P71/KR5	13	I/O	P43	24	I/O	P32	35	I	P12/INT2
3	I/O	P70/KR4	14	I/O	P42	25	I/O	P31	36	I	P11/INT1
4	I/O	P63/KR3	15	I/O	P41	26	I/O	P30	37	I	P10/INT0
5	I/O	P62/KR2	16	I/O	P40	27	I/O	P81	38	—	NC
6	I/O	P61/KR1	17	—	GND	28	I/O	P80	39	—	VDD
7	I/O	P60/KR0	18	I	XT1	29	I/O	P03/SI/SB1	40	I/O	P23/BUZ
8	I/O	P53	19	—	XT2	30	I/O	P02/SO/SB0	41	I/O	P22/PCL
9	I/O	P52	20	I	RESET	31	I/O	P01/SCK	42	I/O	P21
10	I/O	P51	21	I	X1	32	I	P00/INT4	43	I/O	P20/PTO0
11	I/O	P50	22	I	X2	33	I	P13/T10	44	I/O	P73/KR7

32	P00/INT4	P40	16
31	P01/SCK	P41	15
30	P02/SO/SB0	P42	14
29	P03/SI/SB1	P43	13
37	P10/INT0	P50	11
36	P11/INT1	P51	10
35	P12/INT2	P52	9
33	P13/TI0	P53	8
43	P20/PTO0	P60/KR0	7
42	P21	P61/KR1	6
41	P22/PCL	P62/KR2	5
40	P23/BUZ	P63/KR3	4
26	P30	P70/KR4	3
25	P31	P71/KR5	2
24	P32	P72/KR6	1
23	P33	P73/KR7	44
18	XT1	P80	28
19	XT2	P81	27
20	RESET		
21	X1		
22	X2		

INPUT

INT0 }
 INT1 } ; INTERRUPT0, 1, 4
 INT4 }
 INT2 ; TEST2
 KR0-KR7 ; KEY RETURN
 P10-P13 ; PORT1
 RESET ; SYSTEM RESET
 SI ; SERIAL DATA
 TI0 ; TIMER INPUT0
 X1, X2 ; MAIN SYSTEM X'TAL
 XT1, XT2 ; SUB SYSTEM X'TAL

OUTPUT

BUZ ; BUZZER CLOCK
 PCL ; PROGRAMMABLE CLOCK
 PTO0 ; PROGRAMMABLE TIMER OUTPUT0
 SO ; SERIAL DATA

INPUT/OUTPUT

P00-P03 ; PORT0
 P20-P23 ; PORT2
 P30-P33 ; PORT3
 P40-P43 ; PORT4
 P50-P53 ; PORT5
 P60-P63 ; PORT6
 P70-P73 ; PORT7
 P80-P81 ; PORT8
 SCK ; SERIAL CLOCK
 SB0, SB1 ; SERIAL BUS0, 1

