

INPUT

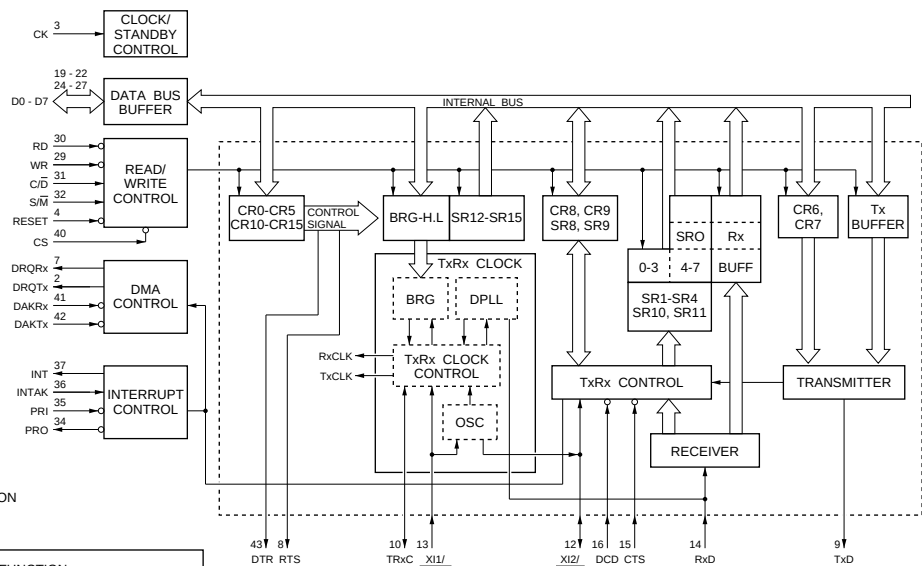
C/D : CONTROL/DATA SELECT
 CK : SYSTEM CLOCK
 CS : CHIP SELECT
 CTS : CLEAR TO SEND
 DAKRX : DMA ACKNOWLEDGE RX
 DAKTX : DMA ACKNOWLEDGE TX
 DCD : DATA CARRIER DETECT
 INTAK : INTERRUPT ACKNOWLEDGE
 PRI : PRIORITY
 RD : READ ENABLE
 RESET : RESET
 Rx D : RECEIVE DATA
 S/M : REGISTER SUB/MAIN SELECT
 WR : WRITE ENABLE
 XT1/STRXC : CRYSTAL INPUT 1/SOURCE OF TRANSMIT RECEIVE CLOCK

OUTPUT

DRQRX : DMA REQUEST RX
 DRQTX : DMA REQUEST TX
 DTR : DATA TERMINAL READY
 INT : INTERRUPT
 PRO : PRIORITY
 RTS : REQUEST TO SEND
 Tx D : TRANSMIT DATA

INPUT/OUTPUT

D0 - D7 : DATA BUS
 TRxC : TRANSMIT RECEIVE CLOCK
 XT2/SYNC : CRYSTAL INPUT 2/SYNCHRONIZATION



I/INPUTS					FUNCTION
CS	WR	RD	S/M	C/D	
0	0	1	0	0	TRANSMIT DATA WRITE TO Tx BUFFER
0	1	0	0	0	RECEIVED DATA READ FROM Rx BUFFER
0	0	1	0	1	DATA WRITE TO CONTROL MAIN REGISTER
			1	1	DATA WRITE TO CONTROL SUB REGISTER
0	1	0	0	1	DATA READ FROM STATUS MAIN REGISTER
			1	1	DATA READ FROM STATUS SUB REGISTER
0	1	1	X	X	HIGH-IMPEDANCE
0	0	0	X	X	INHIBIT

0 : LOW LEVEL
 1 : HIGH LEVEL
 X : DON'T CARE

INPUT

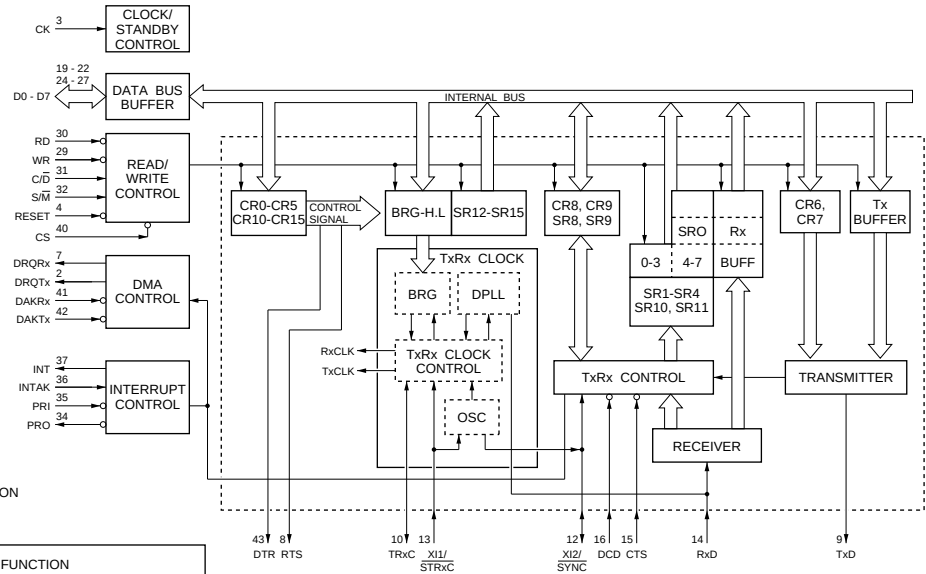
C/D : CONTROL/DATA SELECT
CK : SYSTEM CLOCK
CS : CHIP SELECT
CTS : CLEAR TO SEND
DAKRX : DMA ACKNOWLEDGE RX
DAKTx : DMA ACKNOWLEDGE TX
DCD : DATA CARRIER DETECT
INTAK : INTERRUPT ACKNOWLEDGE
PRI : PRIORITY
RD : READ ENABLE
RESET : RESET
Rx/D : RECEIVE DATA
S/M : REGISTER SUB/MAIN SELECT
WR : WRITE ENABLE
XT1/STRxC : CRYSTAL INPUT 1/SOURCE OF TRANSMIT RECEIVE CLOCK

OUTPUT

DRQRx : DMA REQUEST RX
DRQTx : DMA REQUEST TX
DTR : DATA TERMINAL READY
INT : INTERRUPT
PRO : PRIORITY
RTS : REQUEST TO SEND
Tx/D : TRANSMIT DATA

INPUT/OUTPUT

D0 - D7 : DATA BUS
TRxC : TRANSMIT RECEIVE CLOCK
XT2/SYNc : CRYSTAL INPUT 2/SYNCHRONIZATION



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