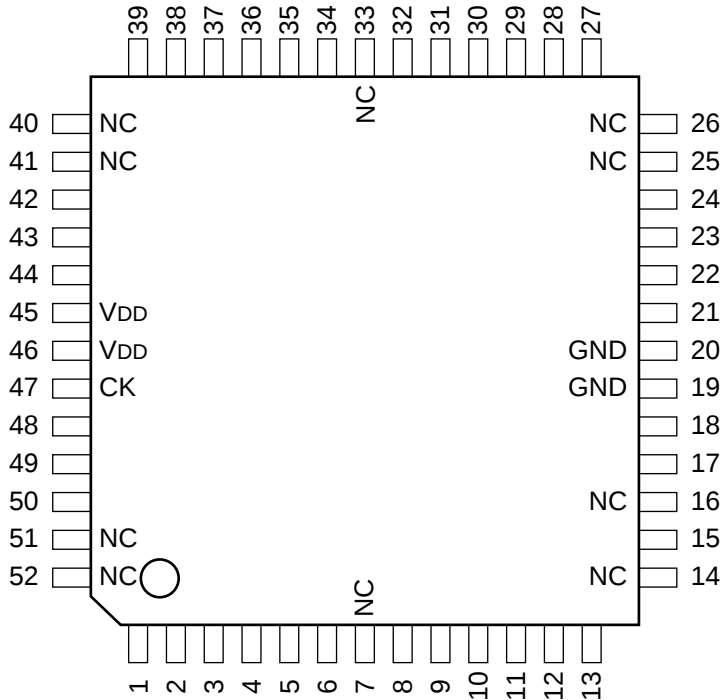
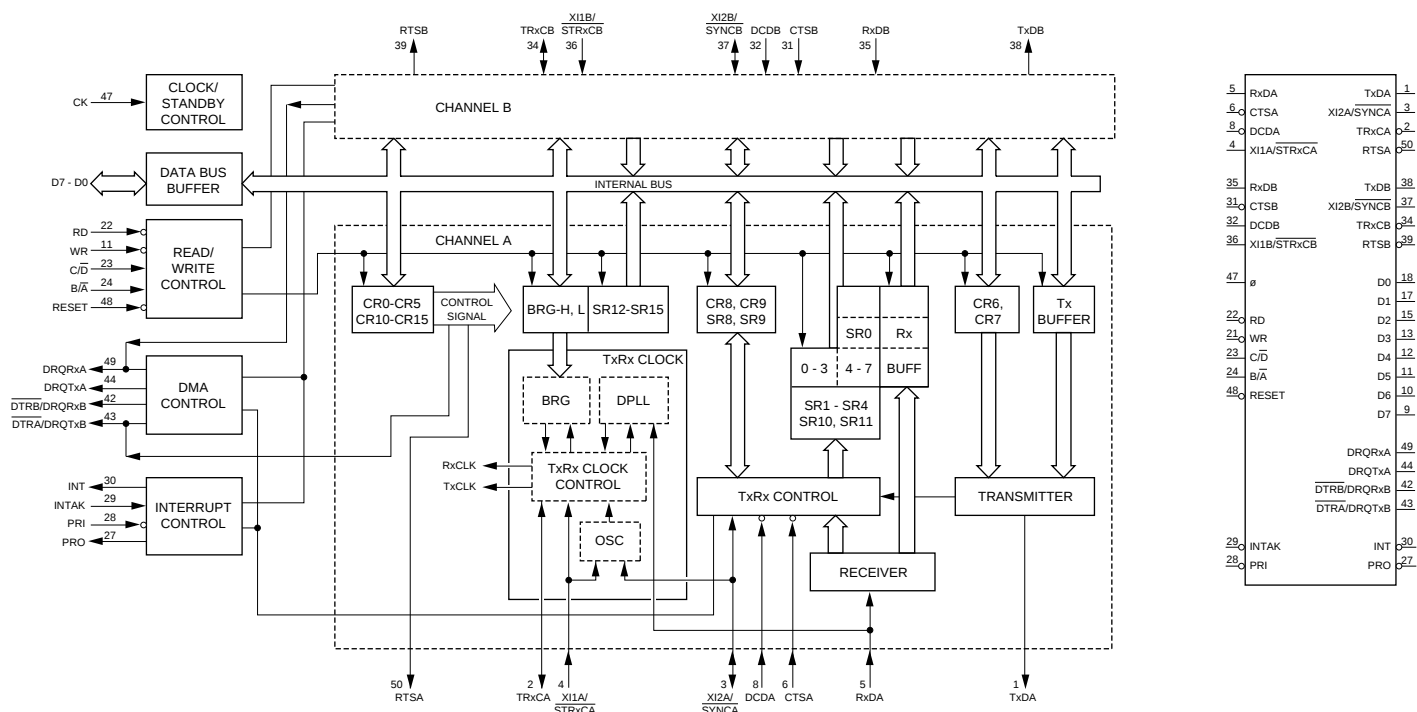


C-MOS ADVANCED MULTI-PROTOCOL SERIAL CONTROLLER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	TxDA	19	—	GND	37	I/O	X12B/SYNCB
2	I/O	TRxCA	20	—	GND	38	O	TxDB
3	I/O	X12A/SYNCA	21	I	WR	39	O	RTSB
4	I	XI1A/STRxCA	22	I	RD	40	—	NC
5	I	RxDA	23	I	C/D	41	—	NC
6	I	CTSA	24	I	B/A	42	O	DTRB/DRQRxB
7	—	NC(OPEN)	25	—	NC	43	O	DTRA/DRQTxB
8	I	DCDA	26	—	NC	44	O	DRQTxA
9	I/O	D7	27	O	PRO	45	—	VDD
10	I/O	D6	28	I	PRI	46	—	VDD
11	I/O	D5	29	I	INTAK	47	I	CK
12	I/O	D4	30	O	INT	48	I	RESET
13	I/O	D3	31	I	CTSB	49	O	DRQRxA
14	—	NC	32	I	DCDB	50	O	RTSA
15	I/O	D2	33	—	NC(OPEN)	51	—	NC
16	—	NC	34	I/O	TRxCB	52	—	NC
17	I/O	D1	35	I	RxDB			
18	I/O	D0	36	I	XI1B/STRxCB			



INPUTS				FUNCTION	
WR	RD	B/A	C/D		
0	1	0	0	CHANNEL A	WRITE (TxD)
		1		CHANNEL B	
1	0	0	0	CHANNEL A	READ (Rx/D)
		1		CHANNEL B	
0	1	0	1	CHANNEL A	WRITE (CONTROL REGISTER)
		1		CHANNEL B	
1	0	0	1	CHANNEL A	READ (STATUS REGISTER)
		1		CHANNEL B	
1	1	x	x		HIGH-IMPEDANCE
0	0	x	x		INHIBIT

0 : LOW LEVEL
1 : HIGH LEVEL
x : DON'T CARE.

CK : SYSTEM CLOCK INPUT
WR : WRITE ENABLE INPUT
RD : READ ENABLE INPUT
B/A : CHANNEL B/A SELECT INPUT
C/d : CONTROL/DATA SELECT INPUT
D0 - D7 : DATA BUS INPUTS/OUTPUTS
INT : INTERRUPT OUTPUT
INTAK : INTERRUPT ACKNOWLEDGE INPUT
PRI : PRIORITY INPUT
DRQTxA : DMA REQUEST TxA OUTPUT
DRQrxA : DMA REQUEST RxA OUTPUT
PRO : PRIORITY OUTPUT

DTRA/DRQTxB : DATA TERMINAL READY A/DMA REQUEST TxB OUTPUT
DTRB/DRQrxB : DATA TERMINAL READY B/DMA REQUEST RxB OUTPUT
CTSA, CTSB : CLEAR TO SEND A/B INPUT
DCDA, DCDB : DATA CARRIER DETECT A/B INPUT
RTSA, RTSB : REQUEST TO SEND A/B OUTPUT
RESET : RESET INPUT