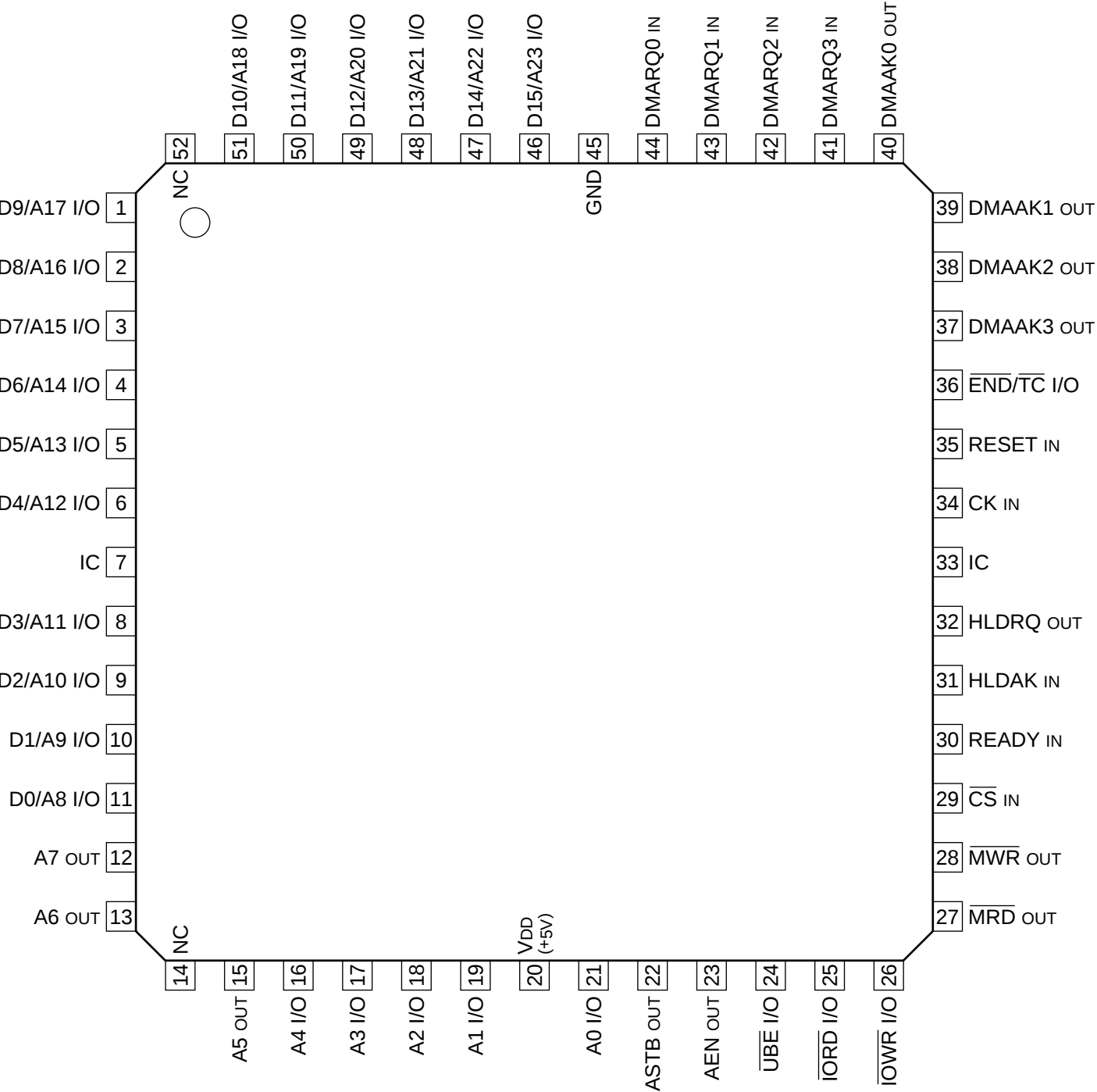

C-MOS DIRECT MEMORY ACCESS CONTROLLER
-TOP VIEW-



INPUT

CK ; CLOCK
 CS ; CHIP SELECT
 DMARQ0-3 ; DMA REQUEST (*1)
 HLDAC ; HOLD ACKNOWLEDGE

OUTPUT

AEN ; ADDRESS ENABLE
 ASTB ; ADDRESS STROBE
 DMAAK0-3 ; DMA ACKNOWLEDGE (*1)
 HLDRQ ; HOLD REQUEST
 MRD ; MEMORY READ
 MWR ; MEMORY WRITE

INPUT/OUTPUT

A0-A7 ; ADDRESS BUS INPUTS/OUTPUTS
 D0-D15 ; DATA BUS INPUTS/OUTPUTS
 END/TC ; END/TERMINAL COUNT
 IORD ; I/O READ
 IOWR ; I/O WRITE
 UBE ; UPPER BYTE ENABLE

 *1 DMA ; DIRECT MEMORY ACCESS

