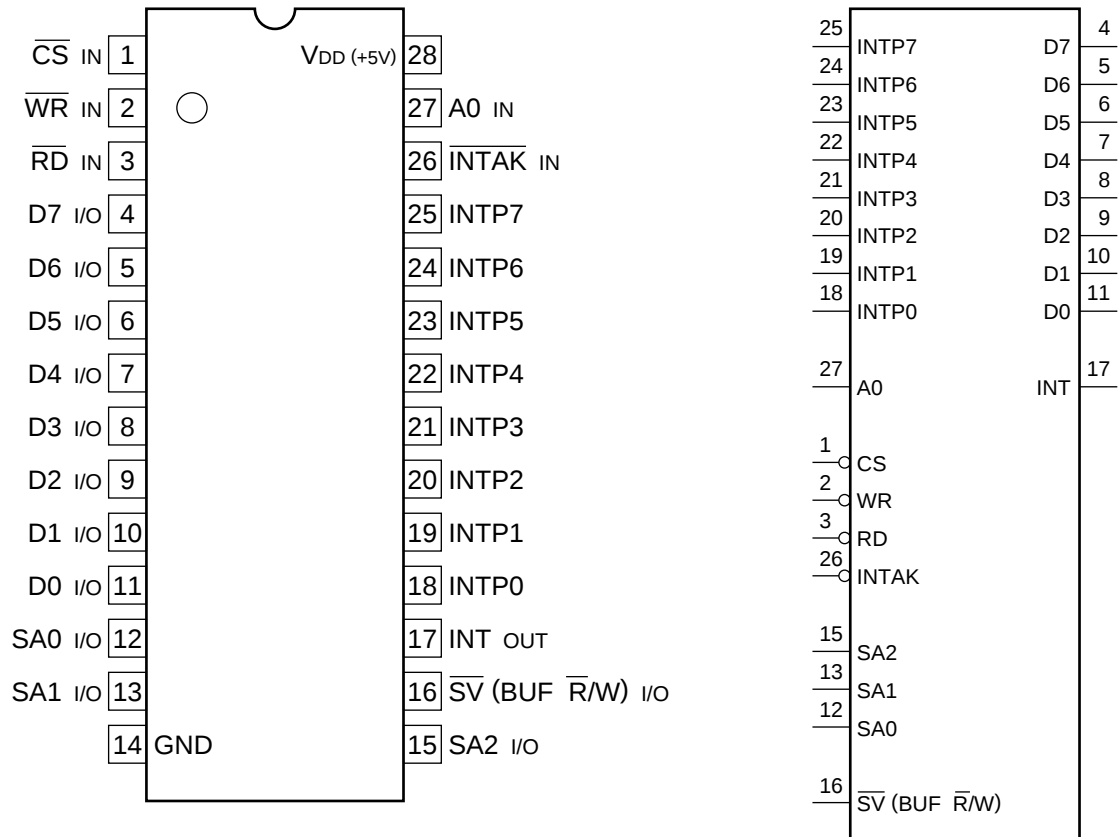


C-MOS INTERRUPT CONTROL UNIT



INPUT

A0	; ADDRESS
$\overline{\text{CS}}$	; CHIP SELECT
$\overline{\text{INTAK}}$	; INTERRUPT ACKNOWLEDGE
INTP0-INTP7	; INTERRUPT REQUEST FROM PERIPHERAL
$\overline{\text{RD}}$	; READ STROBE
$\overline{\text{WR}}$	; WRITE STROBE

OUTPUT

```
INT          ; INTERRUPT
```

INPUT/OUTPUT

D0-D7 ; DATA BUS
SA0-SA2 ; SLAVE ADDRESS
 $\overline{SV}$ (BUF $\overline{R/W}$) ; SLAVE / BUFFER READ WRITE

