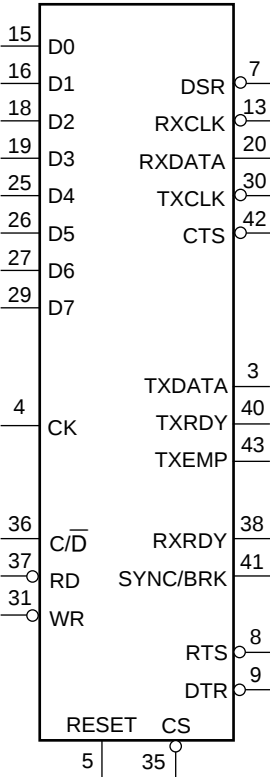
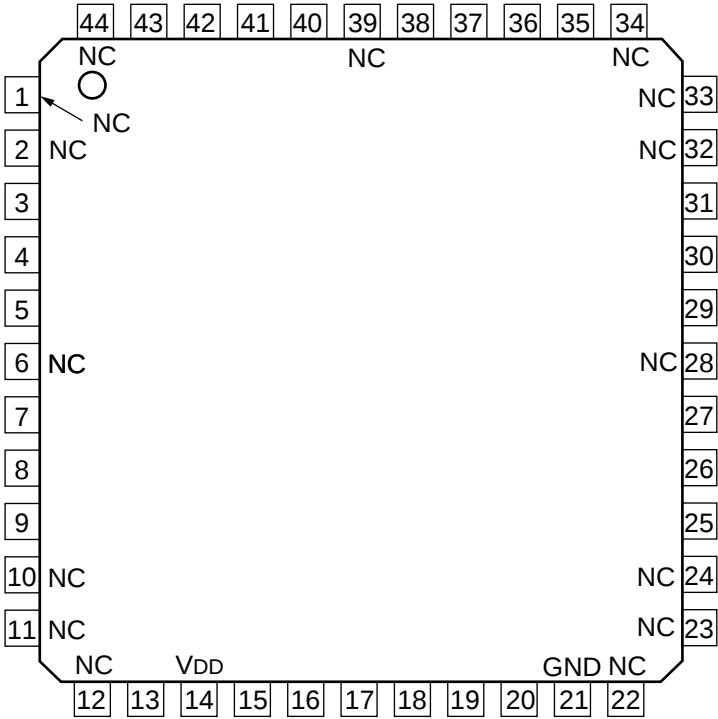


C-MOS SERIAL CONTROLLER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	NC	12	—	NC	23	—	NC	34	—	NC
2	—	NC	13	I	$\overline{\text{RXCLK}}$	24	—	NC	35	I	$\overline{\text{CS}}$
3	O	TXDATA	14	—	V _{DD}	25	I/O	D4	36	I	$\overline{\text{C/D}}$
4	I	CK	15	I/O	D0	26	I/O	D5	37	I	$\overline{\text{RD}}$
5	I	RESET	16	I/O	D1	27	I/O	D6	38	O	RXRDY
6	—	NC	17	—	IC	28	—	NC	39	—	NC
7	I	DSR	18	I/O	D2	29	I/O	D7	40	O	TXRDY
8	O	$\overline{\text{RTS}}$	19	I/O	D3	30	I	$\overline{\text{TXCLK}}$	41	I/O	SYNC/BRK
9	O	$\overline{\text{DTR}}$	20	I	RXDATA	31	I	$\overline{\text{WR}}$	42	I	$\overline{\text{CTS}}$
10	—	NC	21	—	GND	32	—	NC	43	O	TXEMP
11	—	NC	22	—	NC	33	—	NC	44	—	NC

INPUT

$\overline{C/D}$: CONTROL/DATA SELECT
CK : CLOCK
CS : CHIP SELECT
DSR : DATA SET READY
RD : READ STROBE
RESET : RESET
RXCLK : RECEIVER CLOCK
RXDATA : RECEIVE DATA
TXCLK : TRANSMITTER CLOCK
WR : WRITE STROBE

OUTPUT

CTS : CLEAR TO SEND
DTR : DATA TERMINAL READY
RTS : REQUEST TO SEND
RXRDY : RECEIVER READY
TXDATA : TRANSMIT DATA
TXEMP : TRANSMITTER EMPTY
TXRDY : TRANSMIT READY

INPUT/OUTPUT

D0 - D7 : DATA
SYNC/BRK : SYNCHRONIZATION/BREAK

