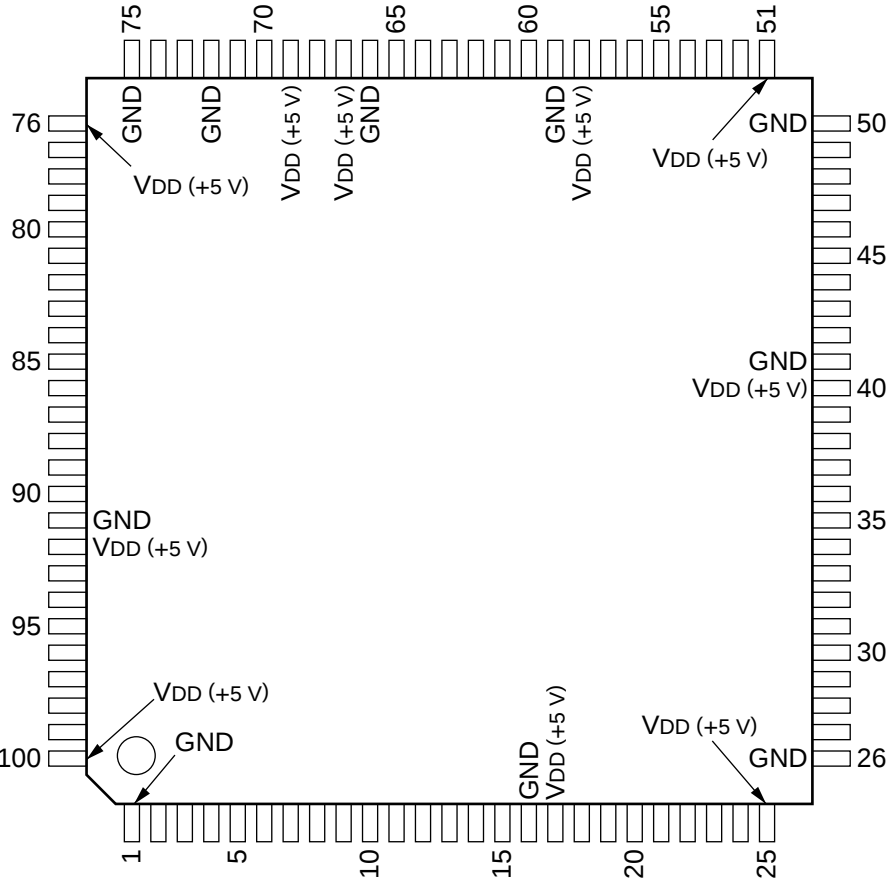

C-MOS 32/16-BIT MICROPROCESSOR
—TOP VIEW—



(V_{DD} = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	GND	21	I/O	INTP02/TO01	41	—	GND	61	O	A7	81	I	READY
2	O	IORD	22	I	INTP01	42	I/O	D8	62	O	A8	82	O	CS0/REFRQ
3	O	IOWR	23	I/O	INTP00/TO00	43	I/O	D9	63	O	A9	83	O	CS1
4	I	NMI	24	I	INTP13/TI	44	I/O	D10	64	O	A10	84	O	CS2
5	I	HLDRQ	25	—	V _{DD}	45	I/O	D11	65	O	A11	85	O	CS3
6	O	HLDAR	26	—	GND	46	I/O	D12	66	—	GND	86	O	A12
7	I/O	RXD/P09/TC	27	I	INTP12	47	I/O	D13	67	—	V _{DD}	87	O	A13
8	I/O	TXD/P08/UBE	28	I	INTP11	48	I/O	D14	68	O	CLKOUT	88	O	A14
9	I/O	SCLK/P07	29	I	INTP10	49	I/O	D15	69	—	V _{DD}	89	O	A15
10	I/O	SO/P06	30	I	RESET	50	—	GND	70	I	X2	90	O	A16
11	I/O	SI/P05	31	—	IC	51	—	V _{DD}	71	I	X1	91	—	GND
12	I/O	DACK1/P04	32	I/O	D0	52	O	A0	72	—	GND	92	—	V _{DD}
13	I/O	DREQ1/P03	33	I/O	D1	53	O	A1	73	O	UCAS	93	O	A17
14	I/O	DACK0/P02	34	I/O	D2	54	O	A2	74	O	LCAS	94	O	A18
15	I/O	DREQ0/P01	35	I/O	D3	55	O	A3	75	—	GND	95	O	A19
16	—	GND	36	I/O	D4	56	O	A4	76	—	V _{DD}	96	O	A20
17	—	V _{DD}	37	I/O	D5	57	O	A5	77	O	RAS	97	O	A21
18	I/O	TCLR/P00	38	I/O	D6	58	—	V _{DD}	78	O	UMWR	98	O	A22
19	O	BLOCK/ WDTOUT	39	I/O	D7	59	—	GND	79	O	LMWR/WE	99	O	A23
20	I	INTP03	40	—	V _{DD}	60	O	A6	80	O	MRD	100	—	V _{DD}