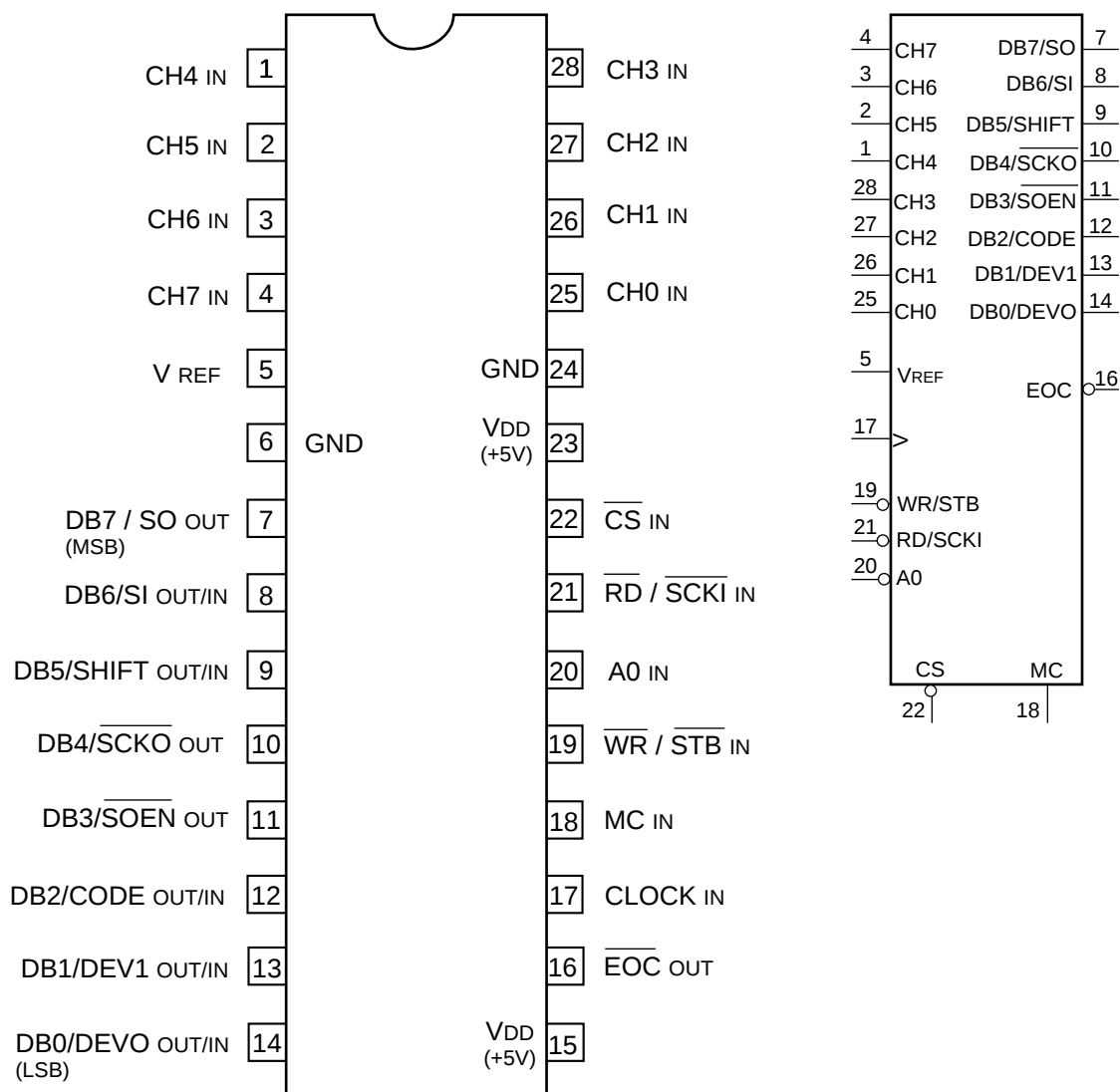

UPD7004C(1/2)
IL00

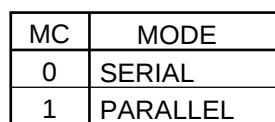
C-MOS 10-BIT SUCCESSIVE COMPARATOR TYPE A/D CONVERTER

— TOP VIEW —



A0 ; CONTROL ADDRESS INPUT
CH0 to 7 ; ANALOG INPUTS
CODE ; CODE SELECT (2' S COMPLEMENT/
BINARY) INPUT
CS ; CHIP SELECT INPUT
DB0 to 7 ; DATA BUS INPUTS/OUTPUTS
DEV0,
DEV1 ; CLOCK RATE SELECT INPUT
EOC ; CONVERSION ENDING SIGNAL
OUTPUT
MC ; MODE SELECT INPUT
RD ; READ SIGNAL INPUT

SCKI ; SERIAL CLOCK INPUT
SCKO ; SERIAL CLOCK OUTPUT
SHIFT ; SHIFT SELECT (LSB FIRST/
MSB FIRST)
SI ; SERIAL INPUT
SO ; SERIAL OUTPUT
SOEN ; SERIAL OUTPUT ENABLE OUTPUT
STB ; ADDRESS WRITE STROBE SIGNAL
INPUT
WR ; WRITE SIGNAL INPUT



PARALLEL MODE				MODE
CS	WR	RD	A0	
1	X	X	X	HIGH IMPEDANCE
0	1	1	X	HIGH IMPEDANCE
0	0	1	0	*1 ANALOG CHANNEL SELECT
0	0	1	1	*2 CODE SELECT/ *3 CLOCK RATE SELECT
0	1	0	0	*4 LOW-BYTE DATA OUTPUT
0	1	0	1	*4 HIGH-BYTE DATA OUTPUT
0	0	0	X	INHIBIT

SEL2	SEL1	SEL0	MPX CHAN.
0	0	0	CH0
0	0	1	CH1
0	1	0	CH2
0	1	1	CH3
1	0	0	CH4
1	0	1	CH5
1	1	0	CH6
1	1	1	CH7

CODE	CODE SELECT
0	BINARY DATA
1	2'S COMPLEMENT DATA

DEV1	DEV0	CLOCK RATE
0	0	1
0	1	1/2
1	0	1/4
1	1	1/8

	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
HIGH – BYTE	MSB	2ND	3RD	4TH	5TH	6TH	7TH	8TH
LOW – BYTE	9TH	0	0	0	0	0	0	0