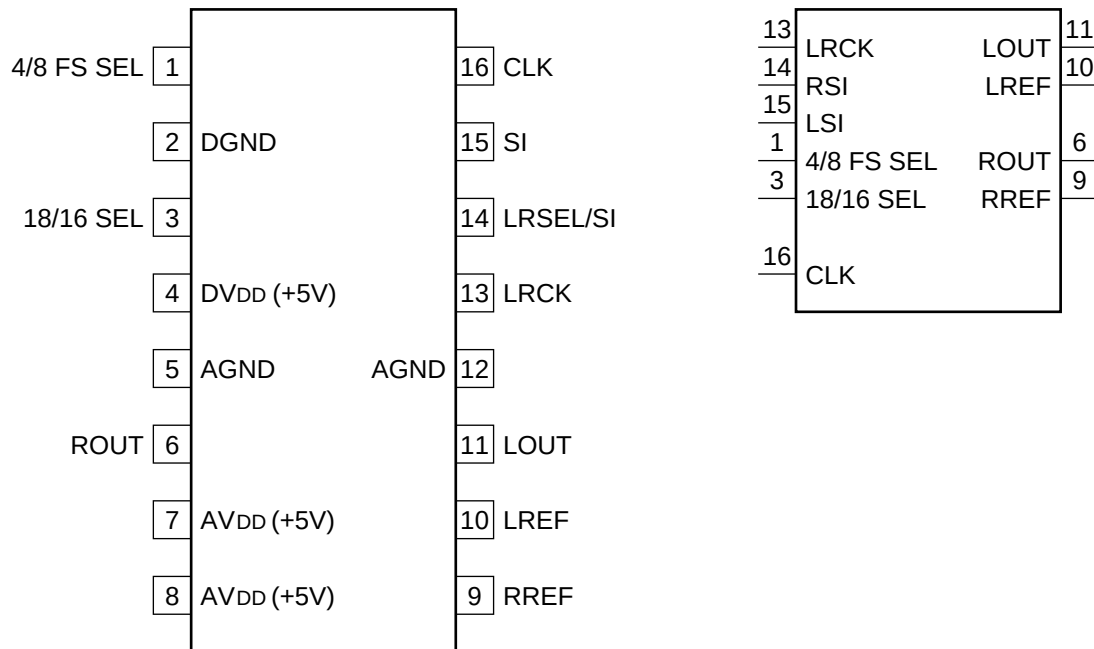


- TOP VIEW -



INPUT

4/8 FS SEL ; INPUT L/R DATA SELECT
18/16 SEL ; 18 BIT/16 BIT SELECT
CLK ; SYSTEM CLOCK
LRCK/WDCK ; L/R CLOCK, OR WORD CLOCK
LRSEL/RSI ; L/R POLARITY SELECT FOR LRCK, OR R DATA
SI/LSI ; L/R DATA, OR L DATA

OUTPUT

```

LOUT      ; L ANALOG
ROUT      ; R ANALOG

```

OTHERS

```
LREF      ; L D/A REFERENCE IN
RREF      ; R D/A REFERENCE IN
```

