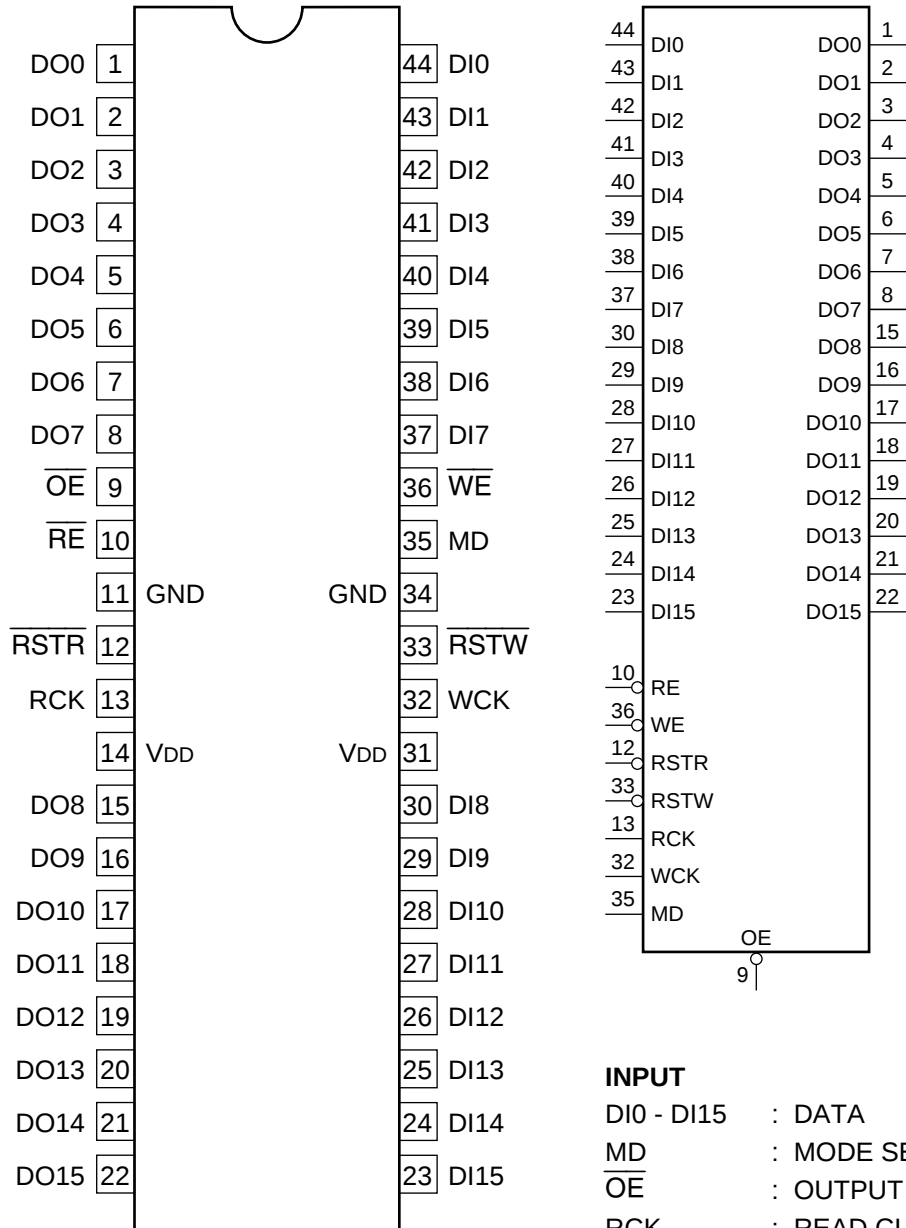


C-MOS 80 K-BIT FIFO MEMORY

—TOP VIEW—

**INPUT**

DI0 - DI15 : DATA
 MD : MODE SETTING
 OE : OUTPUT ENABLE
 RCK : READ CLOCK
 RE : READ ENABLE
 RSTR : RESET READ
 RSTW : RESET WRITE
 WCK : WRITE CLOCK
 WE : WRITE ENABLE

OUTPUT

DO0 - DO15 : DATA

