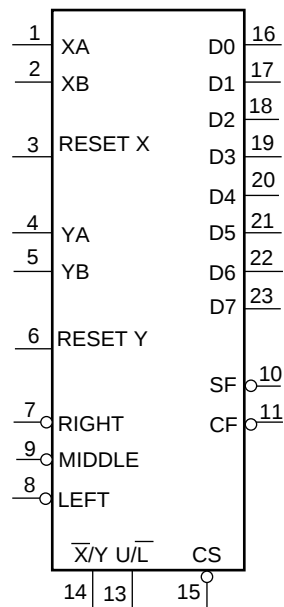
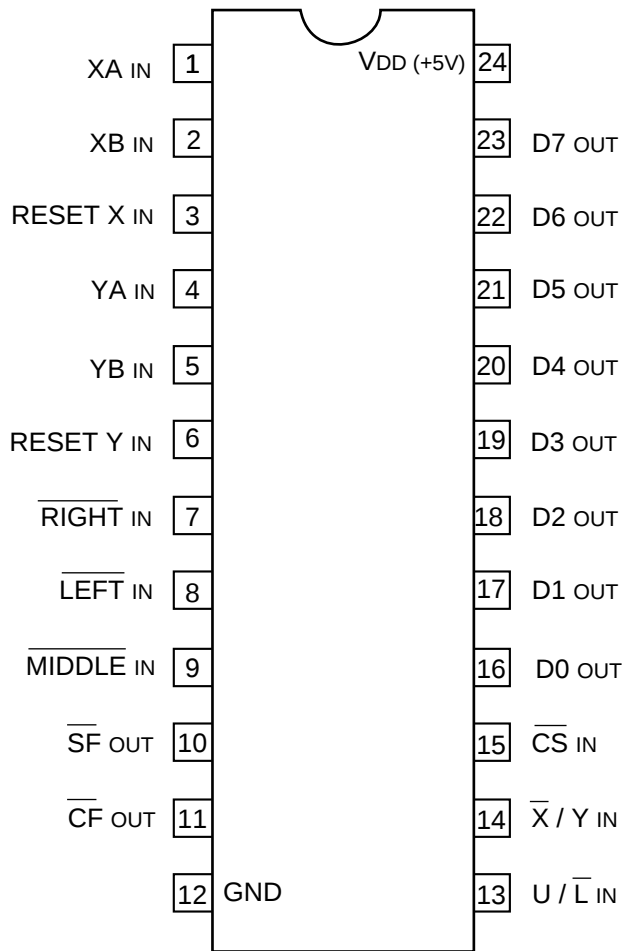


C-MOS INCREMENTAL ROTARY ENCODER
—TOP VIEW—



$\overline{CS}$; CHIP SELECT
 $\overline{X/Y}$; X/Y COUNTER SELECT
XA, YA ; INPUT A PHASE
XB, YB ; INPUT B PHASE
RIGHT, LEFT, MIDDLE ; SWITCH INPUT
FOR INTERNAL STATUS
 $U/\overline{L}$; UPPER/LOWER BYTE SELECT
D0 to D7 ; DATA
CF ; COUNT FLAG
SF ; SWITCH FLAG
RESET X ; COUNTER RESET
RESET Y ; COUNTER RESET

