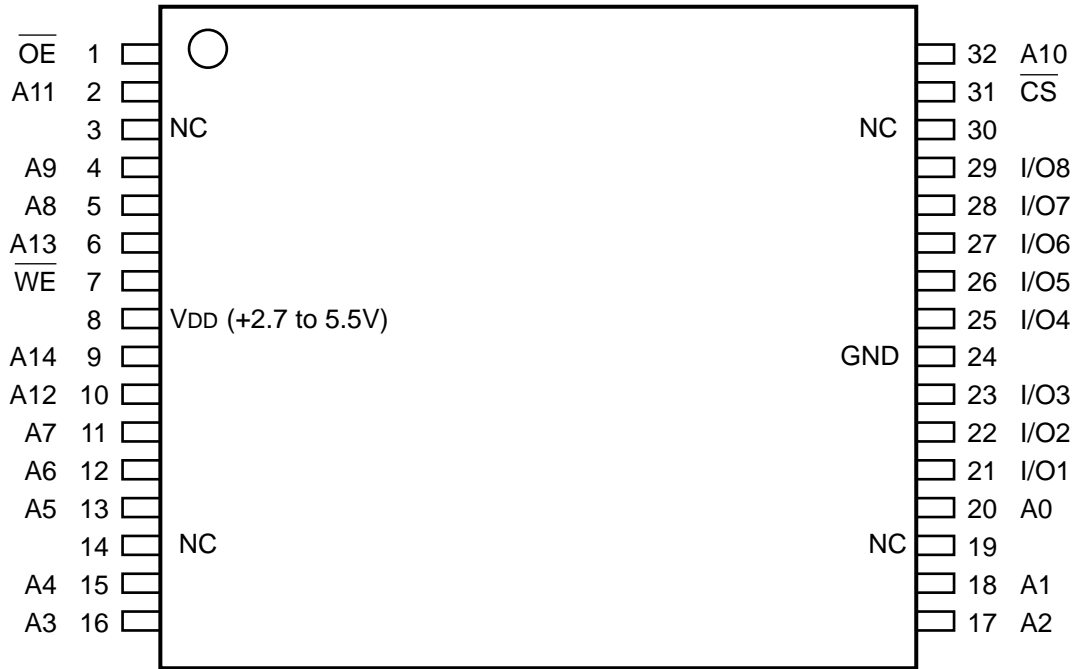

C-MOS 256k (32kx8) BIT STATIC RAM

-TOP VIEW-



A0-14 ; ADDRESS INPUTS
 I/O1-18 ; DATA INPUTS/OUTPUTS
 CS ; CHIP SELECT INPUT
 WE ; WRITE ENABLE INPUT
 OE ; OUTPUT ENABLE INPUT

