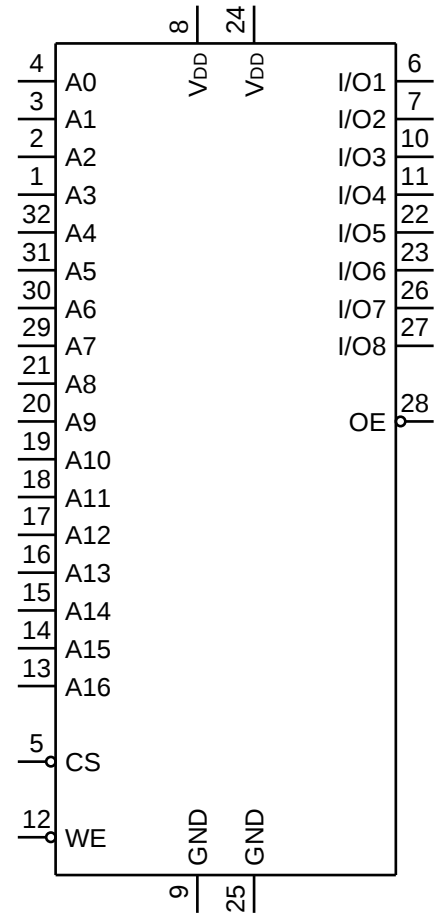
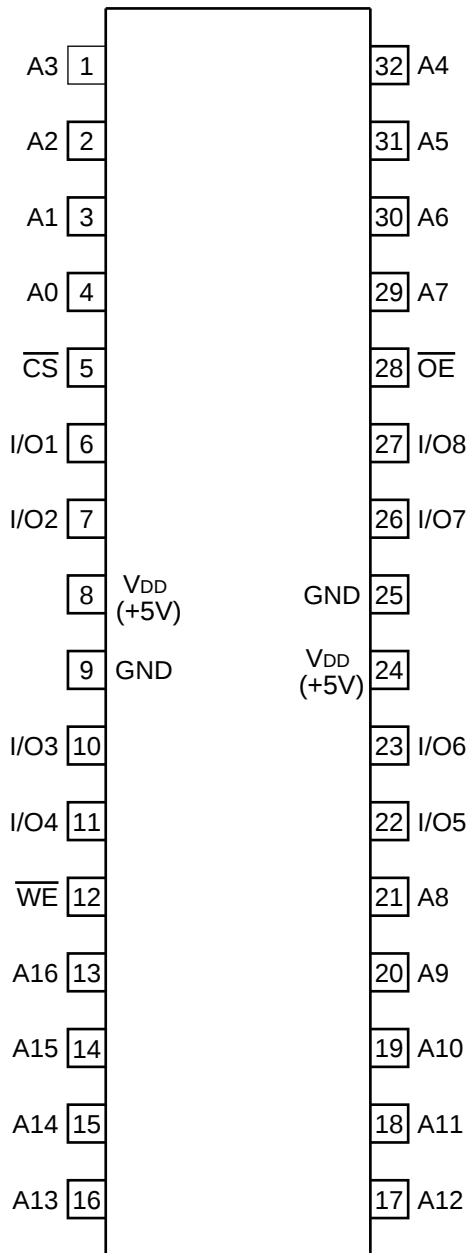

C-MOS 128K-WORD x 8-BIT STATIC RAM

- TOP VIEW -

**INPUT**

A0-A16 ; ADDRESS
 $\overline{\text{CS}}$; CHIP SELECT
 $\overline{\text{WE}}$; WRITE ENABLE

OUTPUT

$\overline{\text{OE}}$; OUTPUT ENABLE

INPUT/OUTPUT

I/O 1-I/O 8 ; DATA INPUT/OUTPUT

MODE	$\overline{\text{CS}}$	$\overline{\text{OE}}$	$\overline{\text{WE}}$	I / O
STAND BY	1	X	X	HI-Z
READ	0	0	1	DATA OUT
OUTPUT DISABLE	0	1	1	HI-Z
WRITE	0	X	0	DATA IN

0 ; LOW LEVEL
 1 ; HIGH LEVEL
 X ; DON'T CARE
 HI-Z ; HIGH IMPEDANCE

