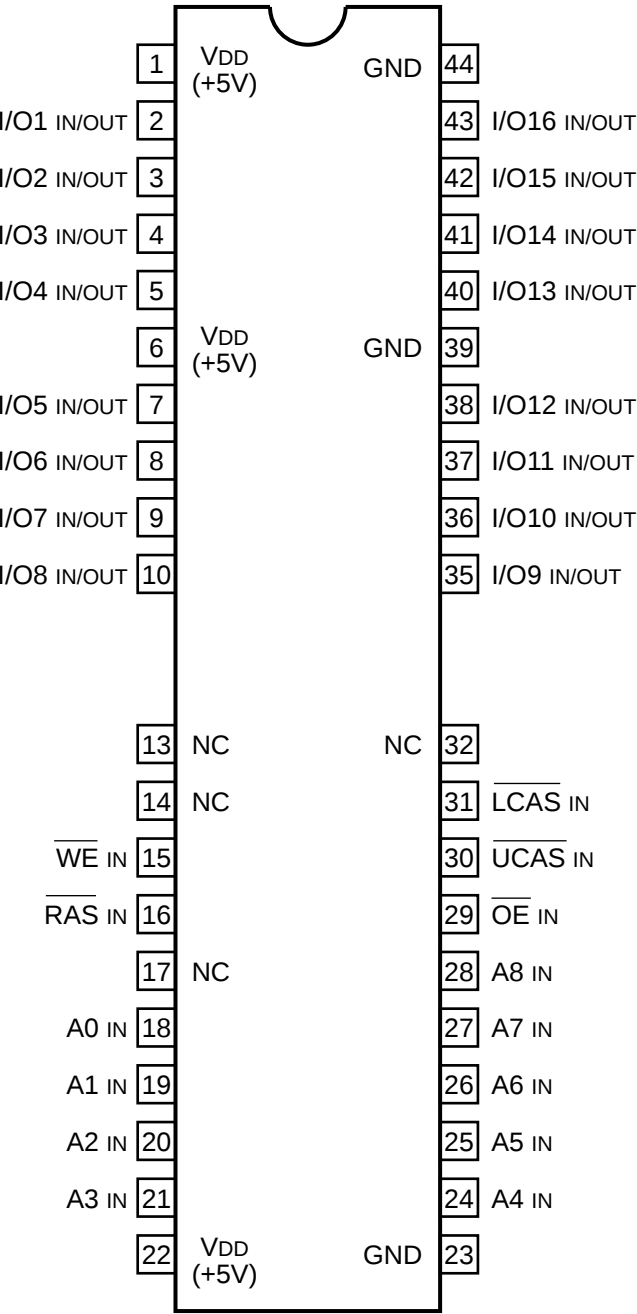

C-MOS 4M (262,144 x 16) BIT DYNAMIC RAM
-TOP VIEW-



18	A0	I/O1	2
19	A1	I/O2	3
20	A2	I/O3	4
21	A3	I/O4	5
24	A4	I/O5	7
25	A5	I/O6	8
26	A6	I/O7	9
27	A7	I/O8	10
28	A8	I/O9	35
		I/O10	36
		I/O11	37
16	RAS	I/O12	38
30	UCAS	I/O13	40
31	LCAS	I/O14	41
15	WE	I/O15	42
29	OE	I/O16	43

A0-8 ; ADDRESS INPUTS
I/O1-16 ; DATA INPUTS/OUTPUTS
RAS ; ROW ADDRESS STROBE INPUT
UCAS ; COLUMN ADDRESS STROBE (UPPER) INPUT
LCAS ; COLUMN ADDRESS STROBE (LOWER) INPUT
WE ; WRITE ENABLE INPUT
OE ; OUTPUT ENABLE INPUT

